COURSE STRUCTURE AND SYLLABUS

(Aligned with AICTE Model Curriculum)

For

B.Tech.

Electronics and Communication Technology

With effective from the Academic Year 2021-2022



 INSTITUTE OF
 Accredited by NAAC with "A" Grade

 INSTITUTE OF
 Recognised by UGC under section 2(f) &12(B)

 TECHNOLOGY &
 Approved by AICTE - New Delhi

 Permanently Affiliated to JNTUK, SBTET
 Ranked as "A" Grade by Govt. of A.P.

COURSE STRUCTURE AND DETAILED SYLLABUS For B.Tech.in

Electronics and Communication Technology With effect from the academic year 2021-2022

Course Structure for I B.Tech ECT Under the Regulations of SITE-21											
Semester -I											
S.No	Course Code	Subject Code	Course	L	Т	Р	С				
1	BS	21CMMAT1010	Engineering Mathematics - I	3	0	0	3				
2	BS	21ETPHT1020	Engineering Physics	3	0	0	3				
3	BS	21CMCHT1030	Engineering Chemistry	3	0	0	3				
4	ES	21CMCST1040	Programming for Problem Solving	3	0	0	3				
5	ES	21ETMEL1050	Computer aided Engineering Graphics	2	0	2	3				
6	BS LAB	21ETPHL1060	Engineering Physics Lab	0	0	3	1.5				
7	BS LAB	21CMCHL1070	Engineering Chemistry Lab	0	0	3	1.5				
8	ES LAB	21CMCSL1080	Programming for Problem Solving Lab	0	0	3	1.5				
9	MC	21CMMSN1090	Constitution of India, Professional Ethics & Human Rights	2	0	0	0				
			TOTAL				19.5				

Course Structure for II B.Tech ECT Under the Regulations of SITE-21											
Semester -II											
S.No	Course Code	Subject Code	Course	L	Т	Р	С				
1	HS	21CMEGT2010	Technical English	3	0	0	3				
2	BS	21CMMAT2020	Engineering Mathematics - II	3	0	0	3				
3	ES	21CMEET2030	Basic Electrical Engineering	3	0	0	3				
4	ES	21CMCST2040	Python Programming	1	0	4	3				
5	ES	21ETETT2050	Network Analysis	3	0	0	3				
6	HS LAB	21CMEGL2060	English Communication Skills Lab	0	0	3	1.5				
7	ES LAB	21CMEEL2070	Basic Electrical Engineering Lab	0	0	3	1.5				
8	ES LAB	21ETMEL2080	Engineering Workshop Lab	0	0	3	1.5				
9	МС	21CMCHN2090	Environmental Science	2	0	0	0				
	•	-	TOTAL				19.5				

ENGINEERI	NG MATHEMAT	FICS-I	
	2 Differential Equation		
	on to all the branches	,	
S	SEMESTER I		
Subject Code	21CMMAT1010	IA Marks	30
Number of Lecture Hours/Week	3	Exam Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
	Credits – 03		
Course Objectives:			
1. To solve the differential equation			
2. To enlighten the learners in the	1	-	
3. To familiarize with functions of		_	mization
4. To solve the partial differential	-		
5. To apply double integration tec	hniques in evaluating	areas bounded by	region.
Unit -1			Hours
Differential Equations of first order a	and first degree :		
Linear differential equations - Bernou	lli's equations – Exa	ct equations and	10
Equations reducible to exact form.			10
Applications: Newton's law of cooling	; - Law of natural gro	owth and decay -	
Orthogonal trajectories.			
Unit -2			
Linear differential equations of higher			
homogeneous differential equations of l			10
- with non-homogeneous term of the ty $u^{\mathbb{R}} = a^{\mathbb{R}} V(u)$ and $u^{\mathbb{R}} V(u)$. Method of $V(u)$			
x^n , $e^{ax} V(x)$ and $x^n V(x)$ – Method of Va Applications: LCR circuit.	arration of parameters		
Unit – 3			
Partial differentiation:			
Introduction – Homogeneous function -	- Euler's theorem– To	tal derivative_	
Chain rule– Jacobian – Functional depe			10
series expansion of functions of two van			_ •
Applications: Maxima and Minima o		variables without	
constraints and Lagrange's method.			
Unit – 4			
PDE of first order:			
Formation of partial differential equatio	ns by elimination of a	rbitrary constants	08
and arbitrary functions - Solutions of fin	rst order linear (Lagram	nge) equation and	00
nonlinear (standard types) equations.			
Unit – 5			
Multiple integrals: Double and Triple			
integration in double integrals - Change	e of variables to polar,	, cylindrical and	12
spherical coordinates.			
Applications: Finding Areas and Volum	nes.		
Course outcomes:	ana alala (-		
On completion of this course, students a		incoming fields (I 2)	\
1. Solve the differential equations	related to various eng	meeting fields (L3))

- 2. Solve the differential equations of higher order related to various engineering fields (L3)
- 3. familiarize with functions of several variables which is useful in optimization (L3)
- 4. Solve the partial differential equations of first order (L3)
- 5. Apply double integration techniques in evaluating areas bounded by region (L3).

Question paper pattern:

- 1. Question paper consists of 10 questions.
- 2. Each full question carrying 14 marks.
- 3. Each full question will have sub question covering all topics under a unit.
- 4. The student will have to answer 5 full questions selecting one full question from each unit.

Text Books:

1. B. S. Grewal, Higher Engineering Mathematics, 44th Edition, Khanna Publishers.

2. B. V. Ramana, Higher Engineering Mathematics, 2007 Edition, Tata Mc. Graw Hill

Education.

Reference Books:

1. Erwin Kreyszig, Advanced Engineering Mathematics, 10th Edition, Wiley-India.

2. Joel Hass, Christopher Heil and Maurice D. Weir, Thomas calculus, 14thEdition, Pearson.

3. Lawrence Turyn, Advanced Engineering Mathematics, CRC Press, 2013.

4. Srimantha Pal, S C Bhunia, Engineering Mathematics, Oxford University Press.

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	3	3	-	-	-	-	-	-	-	-	-	-
2	3	3	-	-	-	-	-	-	-	-	-	-
3	3	3	-	-	-	-	-	-	-	-	-	-
4	3	3	-	-	-	-	-	-	-	-	-	-
5	3	3	-	-	-	-	-	-	-	-	-	-
Cou rse	3	3	-	-	-	-	-	-	-	-	-	-

	IGINEERING PHYSIC		
(Introductio	on to Electromagnet SEMESTER I	ic Theory)	
	SEIVIESTEINT		
Subject Code	21ETPHT1020	IA Marks	30
Number of Lecture HR/Week	03	Exam Marks	70
Total Number of Lecture Hr	50	Exam Hours	03
	Credits – 0	3	
 COURSE OBJECTIVES: The objectives of this course, help th To impart the knowledge of dielectric medium. To impart the knowledge of propagation of EM waves. 	Electrostatics and N	-	um and in
Unit -1			Hours
Electrostatics in vacuum: Coulor Electrostatic potential or Scalar potential surfaces, Relation betwo Applications of Gauss law-Calculation due to the uniform charge distribution and (e) solid cylinder, Divergence and discrete and continuous charge distribution Unit -2	otential (V) due to een E&V, Gauss la on of Electric field st on over a (i) wire (ii) nd Curl of electrosta	a point charge, Equi aw in electrostatics, rength and potential sheet (c) solid sphere	10
Electrostatics in dielectric medium: Electric dipole, Types of dielectric polarization (P), Dielectric polar constant, Relation between D, E polarization, Boundary conditions a polarizations- Electronic (Quantitat polarizations (Qualitative) - Lorentz i	s, Electric displacer izability, Susceptib and P, Bound cha t interface of diele ive),Ionic (Quantitat	nent (D), Dielectric ility and Dielectric arge due to electric ctric media,Types of ive) and Orientation	10
Unit – 3			
Magneto statics: Biot- Savart's la current carrying conductor, Magnet Helmholtz coils, Magnetic field ind magnetic field (Gauss law in ma (Ampere's circuital law); Magnetic charged particle in electrical field an	tic field on the axi uction due to a solo igneto statics), Cur Scalar and Vector	s of a current loop, enoid, Divergence of I of Magnetic field potential, Motion of	11
Unit – 4	0 0		
	tial form of Faraday' I and magnetic ve tance of Solenoid, E	ctor potential using nergy density stored	10

Maxwell's equations and EM waves: Maxwell's equation in vacuum and nonconducting medium; Wave equation of EM waves; Plane electromagnetic waves in vacuum, their transverse nature; Relation between electric and magnetic fields of an electromagnetic wave; Energy density in EM fields, Pointing Theorem, polarization of EM waves, Momentum carried by electromagnetic waves and radiation pressure.

9

COURSE OUTCOMES:

On completion of the course student will able to

- 1. Formulate the electric field and electric potential using fundamental laws in electrostatics.
- 2. Understand the microscopic behavior of dielectrics in electrical field.
- 3. Calculate the static magnetic fields due to current carrying conductors.
- 4. **Estimate** the physical parameters of a system using the basic laws of electricity and magnetism.
- 5. **Recognize** the relation between electrical fields and time varying magnetic fields.
- 6. **Apply** Maxwell's equations for the propagation of EM waves.

Question paper pattern:

- 1. Question paper consists of 10 questions.
- 2. Each full question carrying 14 marks.
- 3. Each full question will have sub question covering all topics under a unit.
- 4. The student will have to answer 5 full questions selecting one full question from each unit.

TEXT BOOKS:

- 1. Saroj K. Dash, Smaruti R. Khuntia, Fundamentals of Electromagnetic theory.
- 2. David Griffiths, Introduction to Electrodynamics.

REFERENCE BOOKS:

- 1. W. Saslow, Electricity, magnetism and light.
- 2. S.L Gupta& D.L. Gupta, Unified physics.
- 3. Ch. Srinivas, Ch. Seshubabu, Engineering Physics, Cengage learning.

ENGINEERING CHEMISTRY SEMESTER I										
Subject Code	21CMCHT1030	IA Marks	30							
Number of Lecture Hours/Week	3	Exam Marks	70							
Total Number of Lecture Hours	48	Exam Hours	03							
	Credits – 03	·								

COURSE OBJECTIVES:

The objectives of this course, help the students to

- 1. Explain the mechanism of corrosion
- 2. Interpret various boiler troubles and importance of water quality standards.
- 3. Learn preparation of semiconducting materials, nano materials and liquid crystals their applications
- 4. Acquire knowledge on nonconventional energy resources and different types of batteries
- 5. Know various spectroscopic techniques.
- 6. Acquire knowledge on volumetric analysis.

Unit -1	Hours
Electrochemistry and Corrosion Electro chemistry: Introduction, electrode potential, standard electrodes – Hydrogen and Calomel electrodes, Nernst equation and applications. Corrosion: Introduction, Mechanism of Wet chemical corrosion, control methods – proper designing, cathodic protection- Sacrificial anodic and impressed current cathodic protection.	9
Unit -2	
Water Chemistry and Surface Properties Water chemistry: Surface and subsurface water quality parameters – turbidity, pH, total dissolved salts, chloride content, Hardness of water, Temporary and Permanent hardness, Units, determination of hardness by complex metric method. Boiler troubles, Caustic Embrittlement, Priming and foaming, Boiler corrosion. Break point chlorination. Surface properties: Determination of surface tension and viscosity of liquids.	9
Unit -3	

Material Chemistry Non-elemental semiconducting materials: Stoichiometric, controlled valency and chalcogen photo/semiconductors and preparation of semiconductors (distillation, zone refining, Czochralski crystal pulling, epitaxy, diffusion and ion implantation). Liquid crystals: Introduction, types and applications. Nanoparticles: Introduction, preparation methods – Sol-gel method, Chemical reduction method – Preparation of carbon nanotubes (Arc discharge, chemical vapour deposition and laser ablation methods) properties and applications.	10
Unit -4	
 ENERGY SOURCES: Non-conventional energy sources, Design, working, schematic diagram, advantages and disadvantages of photovoltaic cell, hydropower, geothermal power, tidal and wave power, ocean thermal energy conversion. Batteries and fuel cells: Primary and secondary batteries - Dry cell, Lead Acid Cell, Lithium ion battery and Zinc air cells and fuel cells - H₂-O₂, CH₃OH-O₂, Phosphoric acid and molten carbonate. 	10
Unit -5	
SPECTROSCOPY AND CHROMATOGRAPHY TECHNIQUES Regions of electromagnetic spectrum - Principles of vibrational and rotational spectroscopy. Vibrational and rotational spectroscopy of diatomic molecules: Rigid diatomic molecules - selection rule - simple Harmonic Oscillator - diatomic vibrating rotator. Nuclear magnetic resonance – Principle and Instrumentation.	10
Principles of chromatography – Thin Layer & Paper Chromatography.	
 COURSE OUTCOMES: On completion of the course student will be able to Interpret the mechanism of corrosion Summarize the problems faced in industries due to boiler troubles. Recall the properties and applications of advanced materials. Summarize the advantages of non-conventional energy resources and b Able to gain knowledge on spectroscopic techniques and the ra electromagnetic spectrum used for exciting different molecular energy Determine the strength of acid, base and some elements by vol instrumental analysis. 	nges of the levels.
 Question paper pattern: 1. Question paper consists of 10 questions. 2. Each full question carrying 14 marks. 3. Each full question will have sub question covering all topics under a unit. 4. The student will have to answer 5 full questions selecting one full question unit. 	

TEXT BOOKS:

- 1. P.C. Jain and M. Jain "**Engineering Chemistry**", 15/e, Dhanpat Rai & Sons, Delhi, (Latest edition).
- 2. Shikha Agarwal, "**Engineering Chemistry**", Cambridge University Press, New Delhi, (2019).
- 3. S.S. Dara, "A Textbook of Engineering Chemistry", S.Chand & Co, (2010).
- 4. Shashi Chawla, "Engineering Chemistry", Dhanpat Rai Publicating Co. (Latest edition).
- 5. Fundamentals of Molecular Spectroscopy, by C. N. Banwell.

REFERENCE BOOKS:

- 1. K. Sesha Maheshwarammam and Mridula Chugh, "**Engineering Chemistry**", Pearson India Edn.
- 2. O.G. Palana, "**Engineering Chemistry**", Tata McGraw Hill Education Private Limited, (2009).
- 3. CNR Rao and JM Honig (Eds) "**Preparation and characterization of materials**" Academic press, New York (latest edition)

СО	PO1	PO2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
1	3	-	-	-	-	-	-	-	-	-	-	-
2	-	3	-	-	-	-	-	-	-	-	-	-
3	-	3	-	-	-	-	-	-	-	-	-	-
4	-	3	-	-	-	-	-	-	-	-	-	-
5	-	-	3	-	-	-	-	-	-	-	-	-
6	3	-	-	-	-	-	-	-	-	-	-	-
Course	2	2	1	-	-	-	-	-	-	-	-	-

History & Hardware: Computer Hardware, Components, Types of Software, Memory Units. Introduction to Problem solving: Algorithm, Characteristics of Algorithms, Pseudo Code, Flowchart, Types of Languages, Relation between Data, Information, Input and Output. Basics of C: History and Features of C, Importance of C, Procedural Language, Compiler versus Interpreter, Structure of C Program, Program Development Steps, Programming Errors.Unit -2Overview of C: Character Set, C-Tokens, Data Types, Variables, Constants, Operators, Operator Precedence and Associativity, Evaluation of C-Expressions, Input/output Functions. Conditional Branching: if statement, ifelse statement, Nested ifelse statement, Ifelseif ladder, switch statement. Unconditional Branching: go to. Control flow Statements: break, continue. Looping Constructs: do-while statement, while statement, for statementUnit -3Arrays: Introduction, 1-DArrays, Character arrays and string representation, 2-D Arrays(Matrix), Multi-Dimensional Arrays. Strings: Working with Strings, String Handling Functions (both library and	
Total Number of Lecture Hours 50 Exam Hours Credits – 03 COURSE OBJECTIVES: The Objectives of Programming for problem solving are: • To learn about C programming language syntax, semantics, and the runtime envi • To be familiarized with general computer programming concepts like data types, conditional statements, loops and functions. • To befamiliarizedwithgeneralcodingtechniquesandprocedure-orientedprogramming. Unit -1 History & Hardware: Computer Hardware, Components, Types of Software, Memory Units. Introduction to Problem solving: Algorithm, Characteristics of Algorithms, Pseudo Code, Flowchart, Types of Languages, Relation between Data, Information, Input and Output. Basics of C: History and Features of C, Importance of C, Procedural Language, Compiler versus Interpreter, Structure of C Program, Program Development Steps, Programming Errors. Unit -2 Overview of C: Character Set, C-Tokens, Data Types, Variables, Constants, Operators, Operator Precedence and Associativity, Evaluation of C-Expressions, Input/output Functions. Conditional Branching: if statement, ifelse statement, Nested ifelse statement, If Loelse, if ladder, switch statement. Unconditional Branching: go to. Control flow Statements: break, continue. Looping Constructs: do-while statement, while statement for statement Unit -3 Arrays: Introduction, 1-DArrays, Character arrays and string representation, 2-D Arrays(Matrix),Multi-Dimensional Arrays. <t< th=""><th>30</th></t<>	30
Credits – 03 Credits – 03 COURSE OBJECTIVES: The Objectives of Programming for problem solving are: • To learn about C programming computer programming concepts like data types, conditional statements, loops and functions. • To befamiliarized with general codingtechniquesandprocedure-orientedprogramming. • Unit -1 History & Hardware: Computer Hardware, Components, Types of Software, Memory Units. Introduction to Problem solving: Algorithm, Characteristics of Algorithms, Pseudo Code, Flowchart, Types of Languages, Relation between Data, Information, Input and Output. Basics of C: History and Features of C, Importance of C, Procedural Language, Compiler versus Interpreter, Structure of C Program, Program Development Steps, Programming Errors. Unit -2 Overview of C: Character Set, C-Tokens, Data Types, Variables, Constants, Operators, Operator Precedence and Associativity, Evaluation of C-Expressions, Input/output Functions. Conditional Branching: if statement, ifelse statement, Nested ifelse statement, if adder, switch statement, Unconditional Branching: go to. Control flow Statements: break, continue. Looping Constructs: do-while statement, while statement, for statement Unit -3 Arrays (Matrix),Multi-Dimensional Arrays. Strings: Working with Strings, String Handling Functions (both library and	70
COURSE OBJECTIVES: The Objectives of Programming for problem solving are: • To learn about C programming language syntax, semantics, and the runtime envi • To be familiarized with general computer programming concepts like data types, conditional statements, loops and functions. • Tobefamiliarizedwithgeneralcodingtechniquesandprocedure-orientedprogramming. • Unit -1 H History & Hardware: Computer Hardware, Components, Types of Software, Memory Units. Introduction to Problem solving: Algorithm, Characteristics of Algorithms, Pseudo Code, Flowchart, Types of Languages, Relation between Data, Information, Input and Output. Basics of C: History and Features of C, Importance of C, Procedural Language, Compiler versus Interpreter, Structure of C Program, Program Development Steps, Programming Errors. Unit -2 Overview of C: Character Set, C-Tokens, Data Types, Variables, Constants, Operators, Operator Precedence and Associativity, Evaluation of C-Expressions, Input/output Functions. Conditional Branching: if statement, ifelse statement, Nested ifelse statement, flelseif ladder, switch statement. Unconditional Branching: go to. Control flow Statements: break, continue. Looping Constructs: do-while statement, while statement, for statement Unit -3 Arrays: Introduction, 1-DArrays, Character arrays and string representation, 2-D Arrays(Matrix), Multi-Dimensional Arrays. Strings: Working with Strings, String Handling Functions (both library and	03
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Memory Units. Introduction to Problem solving: Algorithm, Characteristics of Algorithms, Pseudo Code, Flowchart, Types of Languages, Relation between Data, Information, Input and Output. Basics of C: History and Features of C, Importance of C, Procedural Language, Compiler versus Interpreter, Structure of C Program, Program Development Steps, Programming Errors. Unit -2 Overview of C: Character Set, C-Tokens, Data Types, Variables, Constants, Operators, Operator Precedence and Associativity, Evaluation of C-Expressions, Input/output Functions. Conditional Branching: if statement, ifelse statement, Nested ifelse statement, Ifelseif ladder, switch statement. Unconditional Branching: go to. Control flow Statements: break, continue. Looping Constructs: do-while statement, while statement, for statement Unit -3 Arrays: Introduction, 1-DArrays, Character arrays and string representation, 2-D Arrays(Matrix),Multi-Dimensional Arrays. Strings: Working with Strings, String Handling Functions (both library and	lours
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Arrays: Introduction,1-DArrays,Character arrays and string representation, 2-D Arrays(Matrix),Multi-Dimensional Arrays. Strings: Working with Strings, String Handling Functions (both library and	10
2-D Arrays(Matrix),Multi-Dimensional Arrays. Strings : Working with Strings, String Handling Functions (both library and	
user defined). Functions: Basics, Necessity and Advantages, Types of Functions, Parameter Passing Mechanisms, Recursion, Storage Classes, Command Line Arguments, Conversion from Recursion to Iteration and Vice-Versa.	10
Unit -4	

Pointers: Understanding Pointers, Pointer Expressions, Pointer and Arrays, Pointers and Strings, Pointers to Functions. Dynamic Memory Allocation: Introduction to Dynamic Memory Alloca-tion- malloc(),calloc(),realloc(),free().Structures and Unions:Defining a Structure, type def, Advantage of Structure, Nested Structures, Arrays of Structures, Structures and Arrays, Structures and Functions, Structures andPointers,DefiningUnions,Self- ReferentialStructures,Bitfields,Enumerations.	10
Unit -5	
Preprocessing Directives: () acro Substitution, File Inclusion, Conditional Compilation and Other Directives. FileManagementInC:)IntroductiontoFileManagement,ModesandOperatio nsonFiles,TypesofFiles,ErrorHandlingduringI/O Operations.	10
 On completion of the course student will be able to Demonstrate computer components, algorithms, translate them into progra Choose the suitable control structures for the problem to be solved. Make use of arrays, pointers, structures, and unions effectively. Organize reusable code in a program into functions. Demonstration of file operations. 	ms.
 Question paper pattern: 1 Question paper consists of 10 questions. 2 Each full question carrying 14 marks. 3 Each full question will have sub question covering all topics under a unit 4 The student will have to answer 5 full questions selecting one full question. 	
TEXT BOOKS:	
1) Programming in C ,PradipDey, Manas Ghosh, OXFORD	
2) Programming in, C Reema Thareja, Second Edition, OXFORD	.
3) Programming for Problem BehrouzA.Forouzan,RichardF.Gilberg,CENGAGE.	Solving,
REFERENCE BOOKS:	
1) Computer Fundamentals and Programming, Sumithabha Das,McGrawHil	1.
2) Programming in C,AshokN.Kamthane, AmitKamthane, Pearson.	

Course Outcomes to Program Outcomes Mapping:

PO	PO	PO	PO	PO	PO	PO	PO	PO	PO	РО	РО	РО	PSO	PSO 2
CO	1	2	3	4	5	6	7	8	9	10	11	12	1	
1	2				3					2				3
2	2				3					2				3
3	2				3					2				3
4	2				3					2				3
5	2				3					2				3
Over all	2				3					2				3

COs VS POs MAPPING (DETAILED; HIGH:3; MEDIUM:2; LOW:1)

COMPUTER AIDED ENGINEERING GRAPHICS

(Common to AI&M, CSE, CST,ECE,ECT & IT) SEMESTER I

Subject Code	21ETMEL1050	IA Marks	30
Number of Lecture Hours/Week	1(L)+0(T)+4(P)	Exam Marks	70
Total Number of Lecture Hours	50	Exam Hours	3
i	Credits – 03		

COURSE OBJECTIVES: On successful completion of this course, Students should be able to

- 1. draw engineering objects with appropriate lettering and dimensioning using various commands of AutoCAD
- 2. draw geometric constructions, polygons, various types of curves and scales
- 3. construct multi views of points, lines and planes
- 4. construct multi views of solids by orthographic projection method
- 5. convert the orthographic views into isometric views and vice versa by 2D- Commands in AutoCAD

Unit -1: INTRODUCTION	Teaching Hours
Introduction to Engineering Graphics, sheet sizes & layouts (ISO), line types with application, scales, drawing sheet sizes, title block, sheet markings, dimensioning AutoCAD: Overview of Computer Graphics, starting with AutoCAD, templates, menu- bar, drawing area, option buttons (drawing settings), command line area, draw commands (point, line, polyline, circle, circular arc, ellipse, elliptical arc, spline fit, spline CV, rectangle &polygon), modify commands (move, rotate, trim/extend, erase, copy, mirror, chamfer/ fillet, explode, stretch, scale, array & offset), layers (layering, setting up and use of layers, layers to create drawings and create, edit and use customized layers) & annotation commands (applying dimensions/ annotations to drawings), drawing settings (grid, snap-mode, ortho, polar tracking, object snap, iso-draft), dimension settings (edit/ modify dimension style: text size & style, arrow size & style, line types & thickness and setting other parameters of dimension text, dimension lines & extension lines) Printing documents to paper and to PDF using plot command.	12
Unit -2: CONICS AND SCALES	
Geometrical constructions, polygons, conic sections – ellipse, parabola, hyperbola (Eccentricity method only); scales – plain, diagonal and vernier scales.	10
Unit - 3: ORTHOGRAPHIC PROJECTION OF POINTS, LINE AND PLAT	NES
Principles of Orthographic Projections, Projections of Points, projection of lines (inclined to HP & VP); Projections of planes (inclined to one reference plane).	10
Unit – 4: ORTHOGRAPHIC PROJECTION OF SOLIDS	
Projections of Regular Solids- Prisms, Pyramids, Cylinder & Cone (simple position	8

and inclined to one reference plane only).	
Unit-5: ISOMETRIC PROJECTIONS AND ORTHOGRAPHIC VIEWS	
Isometric Projections and orthographic views: Principles of isometric projection – isometric scale, isometric views, conventions; isometric views of lines, planes, simple solids, Conversion of Isometric Views to Orthographic Views and vice-versa	10
 COURSE OUTCOMES: On successful completion of this course, students will be a successful completion of this course, students will be a successful convention of engineering drawing with basic concepts engineering objects with appropriate lettering and dimensioning using variod AutoCAD construct polygons, various types of Curves and scales used engineering approach, buildings, bridges draw multi views of points, lines and planes by orthographic projection met draw multi views of solids by orthographic projection method convert the orthographic views into isometric views and vice versa by 2D- O AutoCAD 	& draw us commands of plication like hod
 Fext Books N.D. Bhatt & V.M. Panchal, Engineering Drawing, 48th edition, 2005, Chard House, Gujarat R.B.Choudary, Engineering Drawing with AutoCAD 2008, Anuradha Public 	_
Reference Books	nal Publishing

- 1. S. Trymbaka Murthy, Computer Aided Engineering Drawing, I.K. International Publishing House Pvt. Ltd., New Delhi, 3rd revised edition 2006.
- 2. K.R. Gopalkrishna, Engineering Graphics, 32nd edition, 2005 Subash Publishers, Bangalore

COs VS POs MAPPING (DETAILED; HIGH:3; MEDIUM:2; LOW:1):

PO	PO	PO	PO	РО	PO	PO	PO	РО	PO	РО	PO	PO	PSO	PSO 2
CO	1	2	3	4	5	6	7	8	9	10	11	12	1	
1	2				3					2				3
2	2				3					2				3
3	2				3					2				3
4	2				3					2				3
5	2				3					2				3
Over all	2				3					2				3

ENGINEERING PHYSICS LAB (Common for ECE &ECT)								
	SEMESTER I							
Subject Code	21ETPHL1060	IA Marks	15					
Number of Practice Hours/Week	03	Exam Marks	35					
Total Number of Practice Hours36Exam Hours03								
	Credits – 1.5							

COURSE OBJECTIVES:

The objectives of this course, help the students

- **To apply** the theoretical knowledge of Physics through hands on the experimental instruments
- **To improve** the experimental knowledge in the later studies
- **To understand** the basic need of experiments.
- **To know** how to measure the different physical quantities.
- **To acquire** ability to use instrumentation techniques.
- **To train** the students to develop techniques based on the principles related to various devices or components.

List of Experiments

- 1. Determination of the dielectric constant of the dielectric material in the given capacitor using a RC charging and discharging circuit.
- 2. Measuring of the magnetic field induction of circular coil-Stewart-Gee's experiment.
- 3. Determination of the horizontal component of earth magnetic field using Helmholtz coil galvanometer..
- 4. Study of the motion of charged particle in electric and magnetic fields and determine the value of e/m by magnetic focusing.
- 5. Determination of the frequency of the AC Source using Sonometer.
- 6. Determination of the electromotive force (emf) of an unknown cell using a stretched wire potentiometer.
- 7. Study of the particle behavior of EM waveand estimation of Planck's constant using photocell.
- 8. Determination of the frequency of electrical vibrator-Melde's experiment.
- 9. Determination of the wavelength and frequency of the electromagnetic wave using diffraction.
- 10. Verification of laws of transverse waves in a stretched string.

Demonstration experiments:

- 1. Estimation of Hall coefficient and estimate the concentration of charge carriers using Hall Effect.
- 2. Determination of the self inductance and resistance of a coil with air core.

COURSE OUTCOMES:

On completion of the course student will able to

- 1. Compare the theory and correlated with experiments
- 2. Design experiments
- 3. Analyze the experimental result
- 4. **Apply** appropriate techniques to perform the experiments
- 5. **Apply** the fundamental laws in electromagnetism to understand the behavior of electromagnetic fields.

6. Calculate the frequency and wavelength of EM Waves.

Question paper pattern:

Ten questions are given, and student should choose one question (blind option), which carries 50 marks in total.

- a. 15 marks are allotted for procedure including circuit diagrams and model graphs.
- b. 15 marks for conduction of the experiment.
- c. 10 marks for results and conclusions.
- d. 10 marks for viva voce.

TEXT BOOKS: *"Physics Laboratory Manual"* Prepared by Department of Physics, SITE.

REFERENCE BOOKS:

1. S. Balasubrahmanian, M.N. Srinivasan "A Text book of Practical Physics"- S. Chand Publishers, 2017.

2. Advanced Practical Physics Vol 1& 2 SP Singh & M.S Chauhan Pragati Prakashan, Meerut **WEB SOURCES:** http://vlab.amrita.edu/index.php -Virtual Labs, Amrita University.

СО	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	3	2	-	2	-	-	-	-	-	-	-	-
2	2	1	-	3	-	-	-	-	-	-	-	-
3	2	2	-	3	-	-	-	-	-	-	-	-
4	3	1	-	3	-	-	-	-	-	-	-	-
5	3	2	-	3	-	-	-	-	-	-	-	-
6	3	2	-	3	-	-	-	-	-	-	-	-
Course	3	2	-	3	-	-	-	-	-	-	-	-

ENGINEERING CHEMISTRY LABORATORY

(Common to All)

SEMESTER I

Subject Code	21CMCHL1070	IA Marks	15
Number of Practice Hr/Week	3	Exam Marks	35
Total Number of Practice Hr	36	Exam Hours	03

Credits - 1.5

List of Experiments

(Any 10 experiments must be conducted)

- 1. Determination of HCl using standard Na2CO3 solution
- 2. Determination of alkalinity of a sample containing Na2CO3 and NaOH
- 3. Determination of surface tension
- 4. Determination of viscosity of a liquid by Ostwald viscometer
- 5. Determination of chloride content of water
- 6. Determination total hardness of water by EDTA.
- 7. Determination of Mg^{+2} using standard oxalic acid solution.
- 8. Determination of Cu^{+2} using standard hypo solution.
- 9. Determination of the rate constant of first order reaction (Ester hydrolysis)
- 10. Determination of strength of strong acid using conductometeric titration.
- 11. Determination of strength of weak acid using conductometeric titration .
- 12. Determination of Ferrous iron using potentiometer.
- 13. Chemical oscillations- Iodine clock reaction
- 14. Estimation of Vitamin C.

Demonstration Experiments

1. Thin Layer Chromatography

2. Determination of $Fe^{+3}by$ a colorimetric method.

Question paper pattern:

Ten questions are given, and student should choose one question (blind option), which carries 50 marks in total.

- a. 15 marks are allotted for procedure including circuit diagrams and model graphs.
- b. 15 marks for conduction of the experiment.
- c. 10 marks for results and conclusions.
- d. 10 marks for viva voce.

PROGRAMM	ING FOR PROBLEMSOLV Common to All)	ING LAB	
	SEMESTER I		
Subject Code	21CMCSL1080	Internal Marks	15
Number of Lecture Hours/Week	3	External Marks	35
Total Number of Hours	36	Exam Hours	03
	Credits – 1.5		02
Course Objectives:			
This course will enable students to	0		
	s steps in Program developme	nt.	
	concepts in C Programming La		
	odular and readable C Program		
	ms (using structured programm		olve
problems.			
5. To introduce basic data	structures such as lists, stacks a	and queues.	
Exercise1(Familiarization with	programming environment)		
a) Familiarization of CODEBL		ompile, Execute, Test	t and
debugging C programs.			
b) Familiarization of RAPTOR T	ool to draw flow charts and un	derstand flow of contro	1.
Acquaintance with basic LINU	X commands.		
Exercise2(Simple computationa	l problems using arithmetic e	expressions)	
a) Write a C Program to display r	eal number with 2 decimal place	res	
b) Write a C Program to convert			
c) Write a C Program to calculate			
area = $\sqrt{(s(s-a)(s-b)(s-c))}$		omana	
d) Write a C program to find the l		ternary operator.	
	o numbers without using a ten		
Exercise3(Problems involving if			
		or odd using hitwise	
a) Write a C Program to check wh	-	or odd using bitwise	
operator, shif operator and arith	-		
b) Write C program to find the roc) Write a C Program to display g	1 I	ing if also if ladder	
c) Write a C Program to display gd) Write a C Program, which take		-	
performs the operation & then j	•	-	
the operators $+, -, *, /, \%$)	sints the result using switch et	Shiror statement. (Considered)	uci
Exercise4(Iterative problems)			
· · · · ·			
a) WriteaCProgramtocountnumbe		e	
b) WriteaCprogramtogenerateallt	-		
c) Write a C Program to print th	e multiplication table correspo	onding to number supp	lied a
input			
Exercise5(Iterative problems)			
a) Write a C Program to Find W	hether the Given Number is i).	Armstrong Number)	
Palindrome Number			
b) Writea C Program to print su	m of digits of a given number		
Exercise6(Series examples)			
a) Write a C Program to calculate	sum of following series		

Exercise7(1DArraymanipulation)

- a) Write a C program to interchange the largest and smallest numbers in the array.
- b) Write a C program to search an element in an array (linear search).
- c) Write a C Program to print the following pattern using a character array SA SASSASI

Exercise8(Matrix problems, String operations)

- a) Write a C program to add two matrices.
- b) Write a C program to multiply two matrices if they are compatible or print an error message **"Incompatible matrix sizes" otherwise.**
- c) Write a C program to check given matrix is symmetric or not.
- Implement the following string operations with and without library functions. i) copy ii) concatenate iii) length iv) compare

Exercise 9 (Simple functions)

- a) Write a C Program demonstrating the following function types
- b) With arguments and with return value.
- c) With arguments and without return alue
- d) Without arguments and without return value.
- e) Without arguments and with return value.
- f) Write a C Program illustrating call by reference

Exercise 10 (Recursive functions)

Write a C Program illustrating the following with Recursion without Recursion

a)Factorial b) GCD c) Power d) Fibonacci

Exercise 11(Pointers and structures)

a) Write a C program to find sum of n elements entered by user. To perform this program, allocate memory dynamically using malloc () function.

b) Write a C program to find sum of n elements entered by user. To perform this program, allocate memory dynamically using calloc () function. Note: Understand the difference between the above two programs.

c) Write a C Program to read and print student details using structures.

Exercise 12 (File operations)

- a) Write a C program to open a file and to print it contents on screen.
- b) Write a C program to copy files
- c) Write a C program merges two files onto a new file.
- d) Write a C program to delete a file.

Course outcomes:

Question paper pattern:

Ten questions are given, and student should choose one question (blind option), which carries 50 marks in total.

- a. 15 marks are allotted for procedure including circuit diagrams and model graphs.
- b. 15 marks for conduction of the experiment.
- c. 10 marks for results and conclusions.
- d. 10 marks for viva voce.

Text Books:

1. ComputerProgramingANSIC,EBalagurusamy,McGrawHillEducation(Private),Limited (TB1)

2. Programming in C,Reema Thareja, Second Edition, Oxford Higher Education(TB2) **Reference Books:**

1. Computer Basics and C Programming, V Raja Raman, Second Edition, PHI (RB1) Course Outcomes:

2. Attain knowledge on using CODE BLOCKS and RAPTOR tools in solving problems. Examine and analyze alternative solutions to a problem.

3. Design an algorithmic solution to a problem using problem decomposition and stepwise refinement.

4. Demonstrate conversion of iterative functions to recursive and vice-versa.

СО	PO	PO1	PO1	PO1	PSO	PSO								
	1	2	3	4	5	6	7	8	9	0	1	2	1	2
1	3	3	3										3	
2	3	3	3		2								3	
3	3	3	3		2								3	
4	3	3	3		2								3	
5	3	3	3		2								3	
Course	3	3	3		2								3	

Course Outcomes to Program Outcomes Mapping

CONSTITUTION OF INDIA, P			N RIGHTS
(Cor	nmon to all Branches)	
	SEMESTER I		20
Subject Code	21CMMSN1090	IA Marks	30
Number of Lecture Hr/week	03	Exam Marks	70
Total Number of Lecture Hr	50	Exam Hours	03
	Credits – 00		
COURSE OBJECTIVES:			
The objectives of this course help the st			
1. To provide basic information about I			
2. To identify individual role and ethica		rds society.	
3. To understand human rights and its i	mplications.		
Unit - I			Hours
Introduction to the Constitution of India	a, The Making of the	Constitution and	
Salient features of the Constitution.			10
Preamble to the Indian Constitution Fu	ndamental Rights & i	ts limitations.	
Unit - II			
Directive Principles of State Policy & I	Relevance of Directive	e Principles State	
Policy Fundamental Duties.			
Union Executives – President, Prime M	finister Parliament Su	preme Court of	10
India.		preme court of	
Unit – III			
State Executives – Governor, Chief Mi	nister State Legislatu	re High Court of	
State. Electoral Process in India, Amer	•	0	10
76th, 86th $\&$ 91 st Amendments.			10
Unit –IV			
Special Provision for SC & ST Special	Provision for Womer	Children &	
Backward Classes Emergency Provisio			
Human Rights – Meaning and Definitio		fic Themes in	
Human Rights- Working of National H			10
Powers and functions of Municipalities			10
Societies.	s, I anonyats and Co	Operative	
boelettes.			
Unit – V			
Scope & Aims of Engineering Ethics,	Responsibility of Eng	ineers	
Impediments to Responsibility.	Responsionity of Elig		
Risks, Safety and liability of Engineers	Honesty Integrity &	Reliability in	10
Engineering.	, monesty, megney a	c Kendonity in	
COURSE OUTCOMES:			
On completion of the course student wi	:11		
1. Have general knowledge and		thereby to take	un competitive
examinations.	i legal incluey and	thereby to take	up competitive
2. Understand state and central po	licies fundamental du	ities	
3. Understand Electoral Process, s			
4. Understand powers and function		ias Danchavata a	nd Co operativ
4. Understand powers and funct Societies, and	nons of municipalit	ics, ranchayats a	nu co-operative
	and rannaibilities of	FEngingers	
5. Understand Engineering Integri	-	Engineers	
6. Understand Engineering Integri	iy & Kellability		
Question paper pattern:			

- 1 Question paper consists of 10 questions.
- 2 Each full question carrying 14 marks.
- 3 Each full question will have sub question covering all topics under a unit.
- 4 The student will have to answer 5 full questions selecting one full question from each unit.

TEXT BOOKS:

1. Durga Das Basu: **"Introduction to the Constitution on India"**, (Students Edn.) Prentice – Hall EEE, 19th / 20th Edn., 2001

2. Charles E. Haries, Michael S Pritchard and Michael J. Robins **"Engineering Ethics"** Thompson Asia, 2003-08-05.

REFERENCE BOOKS:

1. M.V.Pylee, "An Introduction to Constitution of India", Vikas Publishing, 2002.

2. M.Govindarajan, Natarajan, V.S.Senthilkumar, **"Engineering Ethics"**, Prentice –Hall of India Pvt. Ltd. New Delhi, 2004

3. Brij Kishore Sharma," **Introduction to the Constitution of India**", PHI Learning Pvt. Ltd., New Delhi, 2011.

4. Latest Publications of Indian Institute of Human Rights, New Delhi

TE	CHNICAL ENGLIS	H					
	SEMESTER II						
Subject Code21CMEGT2010IA Marks							
Number of Lecture Hours/ Week 03 Exam Marks							
Total Number of Lecture Hours50Exams Hours							
	Credits -03						
Course Objectives:							
To enable the students to learn and Communication by focusing on: 1. Technical English Vocabu 2. Writing Skills 3. Common Errors in Writing 4. Nature and Style of Sensib 5. Writing Technical Reports Unit I Principles of Scientific Vocabula	lary g le Technical Writing and Letters	iples in Technical Engl	ish &				
 Principles of Scientific v substitutes for wordy phra hackneyed and stilted phra The role of roots in word b and expressions. 	ases- redundant words an ses, verbosity and incorre	d expressions-Avoid ct use of words	10 hours				
Unit II							
 Writing Skills Distinguishing between ac Use of clauses in technical Techniques of Sentence an Measuring the clarity of a Unit III 	phrases and sentences d paragraph writing		10 hours				
Common Errors in Writing							
 Subject-verb agreement an adjectives Common errors in the use Punctuation Technical Guidelines for C Avoiding the pitfalls 	of articles, prepositions, a	-	10 hours				

Unit IV	
Nature and Style of Sensible Technical Writing	
Academic Writing Process	10
 Describing, processes and products 	hours
• Defining, Classifying	
• Effective use of charts, graphs, and tables	
Unit V	
Report writing and Letter writing	10
• Writing Technical Reports, Précis writing ,Letter Writing &Essay writing	g Hours
COURSE OUTCOMES	
On Completion of the course student will acquire	
1. Ability to understand Scientific vocabulary and use them confidently	
2. Familiarity with the basic principles of writing clear sentences and parage	aphs
3. Ability to write error free simple technical passages	
4. Knowledge of writing different writing styles	
5. Confidence to write letters and technical reports clearly and coherently Question paper pattern:	
 Question paper consists of 10 questions. Each full question carrying 14 marks. Each full question will have sub question covering all topics under a will have to answer 5 full questions selecting one full que each unit. 	
Text Books	
1. Effective Technical Communication by Barun K Mitra, Oxford Univer Publication	ersity
Non-detailed Text	
1. Karmayogi: A Biography of E Sreedharan by M S Ashokan Reference Books	
1. Communication Skills by Sanjay Kumar & Pushpa Latha, OUP	
 Study Writing by Liz Hamp-Lyons and Ben Heasly, Cambridge Univ Press. 	ersity
3. Remedial English Grammar by F T Wood, Macmillian 2007	
4. Practical English Usage by Michael Swan Oxford University Press	
5. English Collocations in Use by Michael McCarthy & Felicity O'Dell	
 Effective Technical Communication by Arsahf Rizvi, Essential English Grammar by Raymond Murphy, CUP, 2017 	
Distance English Graninal by Raymona Harpity, CO1, 2017	

Unit	Title	Text books/Reference Books
I	Principles of Scientific Vocabulary	Text Book 1/Reference Book 5
II	Writing Skills	Text Book 1Reference Book 2 Reference Book 6
III	Common Errors in Writing	Text Book 1,Reference Book 3 Reference Book 4,Reference Book 7
IV	Nature and Style of Sensible Technical Writing	Text Book 1,Reference Book 1 Reference Book 2
V	Report writing and Letter writing	Text Book 1,Reference Book 1 Reference Book 2

СО	РО 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS O1	PS O2	PS O3
1	-	_	_	-	-	-	-	-	-	2	-	-	-	-	-
2	-	-	-	-	-	-	-	-	-	2	-	-	-	-	-
3	-	-	-	-	-	-	-	-	-	2	-	-	-	-	-
4	-	-	-	-	-	-	-	-	-	2	-	-	-	-	-
5	-	-	-	-	-	-	-	-	-	2	-	-	-	-	-
6	-	-	-	-	-	-	-	-	-	2	-	-	-	-	-

ENGINEERING MATHEMATICS-II

(Linear algebra, Laplace transforms & Numerical Methods)

Common to all the branches

SEMESTER II

Subject Code	21CMMAT2020	IA Marks	30
Number of Lecture Hours/Week	03	Exam Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
	Credita 02	•	•

Credits – 03

Course objectives:

To enable students to apply the knowledge of Mathematics in various engineering

fields by making them to learn the following'

- 1. To develop the use of matrix algebra techniques that is needed by engineers for practical applications and solve system of linear equations
- 2. To find the inverse and power of a matrix by Cayley-Hamilton theorem and reduce the Quadratic form
- 3. To solve initial value problems by using Laplace transforms
- 4. To find the solution of algebraic/ transcendental equations and also interpolate the functions.
- 5. To apply different algorithms for approximating the solutions of ordinary differential equations with initial conditions to its analytical computations.

Unit -1	Hours
Solving systems of linear equations: Rank of a matrix by echelon form and normal form – Solving system of homogeneous and non homogeneous linear equations – Gauss Elimination method- Jacobi and Gauss-Seidel methods for solving system of equations numerically.	10
Unit -2	
Eigen values and Eigen vectors, Cayley–Hamilton theorem and Quadratic forms: Eigen values and Eigen vectors and properties- Cayley-Hamilton theorem (without proof) Reduction to Diagonal form. Quadratic forms and nature of the	10

(without proof) – Reduction to Diagonal form – Quadratic forms and nature of the quadratic forms – Reduction of quadratic form to canonical forms by orthogonal transformation, Diagonalisation and Lagrange's reduction

Unit – 3

Laplace Transforms: Laplace transforms – Definition and Laplace transforms of	10
some certain functions- Shifting theorems - Transforms of derivatives and integrals	

 Unit step function –Dirac's delta function Periodic function – Inverse Laplace transforms– Convolution theorem (without proof). 	
Applications: Solving ordinary differential equations (initial value problems) using Laplace transforms.	
Unit – 4	
Numerical Methods: Introduction - Method of false position - Newton-Raphson method (One Variable) Introduction– Errors in polynomial interpolation – Finite differences– Forward differences– Backward differences –Central differences – Relations between operators – Newton's forward and backward formulae for interpolation – Interpolation with unequal intervals – Lagrange's interpolation formula.	10
Unit – 5	
Numerical integration, Solution of ordinary differential equations with initial conditions: Trapezoidal rule - Simpson's 1/3rd and 3/8th rule - Solution of initial value problems by Taylor's series– Picard's method of successive approximations– Euler's method – Runge -Kutta method (second and fourth order).	10
Course outcomes:	
On completion of this course, students are able to,	
 Develop the use of matrix algebra techniques that is needed by engineers for applications and solve system of linear equations (L6) Find the inverse and power of a matrix by Cayley-Hamilton theorem and Quadratic form (L3) Solve initial value problems by using Laplace transforms (L3) Find the solution of algebraic/ transcendental equations and also interpolate the functions(L3) Apply different algorithms for approximating the solutions of ordinary differentiat with initial conditions to its analytical computations (L3). 	reduce the
Question paper pattern:	
 Question paper consists of 10 questions. Each full question carrying 14 marks. Each full question will have sub question covering all topics under a unit. The student will have to answer 5 full questions selecting one full question from 	each unit.
Text Books:	
 B. S. Grewal," Higher Engineering Mathematics", Khanna publishers, 44th Editi Kreyszig, "Advanced Engineering Mathematics " - Wiley, 9th Edition, 2013. B.V.Ramana "Higher Engineering M athematics" Tata Mc Graw-Hill, 2006 	on, 2016.
Reference Books:	

- 1. Dr.K.V.Nageswara Reddy and Dr.B.Rama Bhupal Reddy, "Engineering Mathematics, Volume II" Scitech Publications, 2017.
- 2. Steven C. Chapra, Applied Numerical Methods with MATLAB for Engineering and Science, Tata McGraw Hill Education, 4th Edition, 2018
- 3. M. K. Jain, S.R.K. Iyengar and R.K. Jain, Numerical Methods for Scientific and Engineering Computation, New Age International Publications, 3rd Edition, 2020.
- 4. Lawrence Turyn, Advanced Engineering Mathematics, CRC Press, 1st Edition 2014.

СО	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	3	3	-	-	-	-	-	-	-	-	-	-
2	3	3	-	-	-	-	-	-	-	-	-	-
3	3	3	-	-	-	-	-	-	-	-	-	-
4	3	3	-	-	-	-	-	-	-	-	-	-
5	3	3	-	-	-	-	-	-	-	-	-	-
Course	3	3	-	-	-	-	-	-	-	-	-	-

BASIC ELECTRICAL ENGINEERING SEMESTER II (Common to All) Subject Code 21CMEET2030 IA Marks 30 Number of Lecture Hours/Week 3L + 1TExam Marks 70 Total Number of Lecture Hours 50 Exam Hours 03 Credits-03 **Course Objectives:** This course will enable student to 1. Understand basic electrical circuit operation. 2. Understand the concept of Alternating Voltage and Current. 3. Understand the operation of DC machines. 4. Understand the working of measuring instruments. 5. Understand the operation of different types of ac machines. 6. Understand the concept of Electrical Safety. Unit -1 Hours Basic Electrical Circuits: Basic definitions(Electric Charge, Current, Electro Magnet Force, Potential Difference; Electric Power and Energy) – types of network elements – Ohm's Law – Kirchhoff's Laws –series & parallel circuits - network 10 theorems (Super position, Thevinen's, Norton's, Maximum power transfer theorems) Unit -2 AC Fundamentals & Basic Electromagnetic Laws: Study of AC Voltage and Current, RMS and Average Values, Three phase Star-Delta connections, Alternating Voltage applied to Pure Resistance, Inductance, Capacitance and their combinations, Concept of Power and Power Factor in AC 10 Circuit. Concept of Magnetic Field, Magneto Motive Force (MMF), Permeability; Self and Mutual Induction, Basic Electromagnetic laws, Unit -3DC Machines: DC Machine -Principle of operation & construction - emf equation- torque equation - speed control methods – losses and efficiency – brake 10 test. Applications of DC motors. Unit – 4 AC Machines: Single Phase Transformers - Construction and Operation-Principles - Classification - Applications-OC & SC test of single phase 10 transformer-regulation & Efficiency. Three Phase Induction Motors: working principle- construction, speed- torque characteristics-losses and efficiency. Unit – 5 Electrical Safety: Electrical Shock and Precautions against it, Treatment of Electric Shock; Concept of Fuses and Their Classification, Selection and 10 Application; Concept of Earthing.

Course Outcomes: The student should be able to

- 1. Understand basic electrical circuit operation.
- 2. Understand the concept of Alternating Voltage and Current.
- 3. Understand the operation of DC machines.
- 4. Understand the working of measuring instruments.
- 5. Understand the operation of different types of ac machines.
- 6. Understand the concept of Electrical Safety.

Question paper pattern:

- 1. Question paper consists of 10 questions.
- 2. Each full question carrying 14 marks.
- 3. Each full question will have sub question covering all topics under a unit.
- 4. The student will have to answer 5 full questions selecting one full question from each unit.

Text Books:

i. Electrical Circuit Theory and Technology by John Bird, Routledge Taylor & Francis Group.

ii. Principles of Electrical Machines by V.K. Mehta & Rohit Mehta, S.Chand and Company Limited.

Reference Books:

- i. Theory and Performance of Electrical Machines by J.B. Gupta, S.K.Kataria & Sons.
- ii. A Textbook of Electrical Technology Volume II: AC & DC Machines by B.L.Theraja & A.K. Theraja, S.Chand and Company Limited.
- iii. Basic Electrical Engineering by Nagsarkar, Sukhija, Oxford Publications, 2nd edition.
- iv. Basic Electrical Engineering by M.S.Naidu and S.Kamakshiah, TMH Publications
- v. Fundamentals of Electrical Engineering by Rajendra Prasad, PHI Publications, 2nd edition.
- vi. Electrical Technology by Surinder Pal Bali, Pearson Publications.

COURSE-OUTCOMES-TO-PROGRAM-OUTCOMES-MAPPING:

COs / POs	P 01	P O2	P 03	P 04	P 05	P 06	Р О7	P 08	P 09	PO 10	PO 11	PO 12
CO1	2	2	1									
CO2	2	2	1									
CO3	2	2	1									
CO4	2	2	1									
CO5	2	2	1									
CO6	2	2	1									
Overall Course	2	2	1									

PYTI	HON PROGRAMMING						
	Common to All						
	SEMESTER II						
Subject Code	21CMCST2040	Internal Mark	s	30			
Number of Lecture Hours/Week	04	External Mar		70			
Total Number of Lecture Hours	48	Exam Hours		03			
Pre-requisite	Credits – 03						
The Objectives of Python Program	ming are	credits 05					
• To learn about Python programmenvironment	computer programming concepts						
conditional statements, loops an	d functions. coding techniques and object-orie		,				
Unit -1			Ног	irs			
Introduction:(TB1:22-30,TB2:1.	1-1.4.TB2:1.21-1.33)Introduction	Python	1101	- L U			
 Program Development Cycle, Inpu with the Print Function, Variables, Data Types, and Expression: (TI Numeric Data Types and Characte functions and Modules. Decision Structures and Boolean Statements, Nested Decision Structures 	it, Processing, and Output, Display Reading Input from the Keyboard B1:41-59) Strings Assignment, an r Sets, Type conversions, Express Logic:(TB1:77-85) if, if-else, if-	ying Output d, Operators. d Comment, ions, Using else if-else	08	3			
	tures, Comparing Strings, Logical	Operators,					
Boolean Variables.							
Unit -2							
Control Statement:(TB1:65-72,T Definite iteration for Loop Forma Statement Conditional Iteration, T Strings and Text Files:(TB1:10 Strings, Data Encryption, Strings Files.	atting Text for output, Selectionhe While Loop, Nested Loops.3-125) Accessing Character and	Substring in	1()			
Unit -3							
ListandDictionaries:(TB1:135-14	45.TR1:153-						
 158)Lists,Tuples,Sets,Dictionaries. Design with Function:(TB1:146-149, TB1:169-190)Functions as Abstraction Mechanisms, Problem Solving with Top Down Design, Design with Recursive Functions, Case Study Gathering Information from a File System. Modules:(TB2:8.1-8.5)Modules, Standard Modules, Packages. 							
Unit – 4							
 File Operations:(TB1:122-123)R in python, Understanding read Understanding write functions, wr Object Oriented Programming:(object and instances, Constructor, Design with Classes:(TB1:294-3) modeling Examples, Case Study and 	functions, read(), readline() and ite() and write lines(). TB2:5.1-5.20, TB2:6.1-6.17)Con class attributes and destructors, In 01, TB1:309-330) Objects and (readlines(), cept of class, heritance.	12	2			
Unit – 5							
Errors and Exceptions: (TB2:7.1 Exceptions, Raising Exceptions, U		0	8				

Actions, Redefined Clean-up Actions.	
Graphical User Interfaces:(TB1:245-288) The Behavior of Terminal	
Based Programs and GUI – Based Programs, Coding Simple GUI-Based	
Programs, Other Useful GUI Resources.	
Course outcomes:	
On completion of the course student will be able to	
• Able to learn the fundamental concepts in the Python language	
 Implementation of python iterative statements and strings 	
 Demonstrate python lists, dictionaries and functions 	
 Understand the concepts of modules and packages in python 	
• Complete coding challenges relating to object-oriented programming's essentia	l concepts and
techniques.	
Question paper pattern:	
1. Question paper consists of 10 questions.	
 2. Each full question carrying 14 marks. 	
 Each full question carrying 14 marks. Each full question will have sub question covering all topics under a unit. 	
 Lach full question with have sub-question covering an topics under a unit. The student will have to answer 5 full questions selecting one full question from the student will have to answer 5 full questions. 	om each unit
Text Books	
1. Fundamentals of Python First Programs, Kenneth.A.Lambert, Cengage.	
2. Python Programming: A Modern Approach, Vamsi Kurama, Pearson.	
ReferenceBooks:	
1)Introduction to Python Programming ,Gowrishankar.S,VeenaA,CRCPress.	
2)Introduction to Programming Using Python Y DanielLiang Pearson	

2)Introduction to Programming Using Python, Y. Daniel Liang, Pearson.

E-Resources:

https://www.tutorialspoint.com/python3/python_tutorial.pdf

CO	P01	P02	PO3	P04	P05	P06	P07	PO8	P09	P010	P011	P012	PSO1	PSO2	PSO3
1	3	3	-	-	-	-	-	-	-	-	-	-	-	-	3
2	3	3	-	-	-	-	-	-	-	-	-	-	-	-	3
3	2	2	3	-	2	-	-	-	-	-	-	-	-	-	3
4	3	2	3	-	3	-	-	-	-	-	-	-	_	-	2
5	3	3	3	-	2	-	-	-	-	Ι	-	-	-	-	2
6	3	2	3	-	3	-	-	-	-	-	-	-	-	-	3
Cou rse	3	3	2	-	2	-	-	-	-	-	-	-	-	-	3

Course Outcomes to Program Outcomes mapping:

	NETWORK ANALYSIS		
Subject Code	21ETETT2050	Internal Mark	s 30
Number of Lecture Hours/Week	03	External Mark	ks 70
Total Number of Lecture Hours	50	Exam Hours	03
Pre-requisite		Credits – 03	
COURSE OBJECTIVES:			
• To understand the basic co	ncepts on RLC circuits.		
• To know the behavior of the	ne steady states and transients state	es in RLC circui	ts.
• To know the basic Laplace	transforms techniques in periods	waveforms.	
• To understand the two port			
-	es of LC networks and filters.		
• To understand the property	es of Le networks and filters.		
Unit -1			Hours
Fundamentals and Network Top	ology: Definitions of branch, node	e, tree, planar,	
non-planar graph, incidence matrix		· · ·	
Definitions of terms associated w	with periodic functions: Time per	riod, Angular	
velocity and frequency, RMS value	e, Average value, Form factor and	d peak factor-	08
problem solving, Phase angle, Pha	-		
phasors, mathematical representat			
relevant theory, problem solving.	Principal of Duality with example	s.	
Unit -2	11 CO 1 NC 1 1 ' 1N	T 1 1 1 ·	
Electric Circuits: Review of Kirc		Nodal analysis	
problem solving including dependent Network Theorems: Thevin		Reciprocity,	10
Compensation, Substitution, Sup		1 .	
problem solving using dependent s	-	i, renegens	
Unit -3		I	
Steady State Analysis of A.C Ci	rcuits: Impedance concept, phase	angle, series	
R-L, R-C, R-L- C circuits probl			
notation for R-L, R-C, R-L-C pro-	oblem solving using mesh and ne	odal analysis,	
Star-Delta conversion, problem so	0		
Transients: First order differentia			12
circuit, R-C circuit with DC excit			
second order differential equatio		· •	
solving using R-L-C elements with		-	
related to s-plane rotation of roots. Unit – 4	Solutions using Laplace transform	m method.	
Resonance: Introduction, Definiti	on of O. Sarias resonance. Bandu	ridth of series	
resonance, Parallel resonance, Cor			
resonance, Bandwidth of parallel	1 ,		
both branches, anti resonance at al	-	F	12
Coupled Circuits: Coupled Ci	-	l inductance,	
Coefficient of coupling, analysis of			
coupled circuits, Conductively cou	pled equivalent circuits- problem	solving.	
Unit – 5			
Two-port Networks: Relationsh			8
parameters, Transmission line pa	rameters, h-parameters, Inverse	h-parameters,	0

Inverse Transmission line parameters, Relationship between parameter sets, Parallel connection of two port networks, Cascading of two port networks, series connection of two port networks, problem solving including dependent sources also.

Course outcomes:

On completion of the course student will be able to

- 1. Gain the knowledge on basic network elements.
- 2. Will analyze the RLC circuits' behavior in detailed.
- 3. Analyze the performance of periodic waveforms.
- 4. Gain the knowledge in characteristics of two port network parameters (Z,Y,ABCD,h&g).
- 5. Analyze the filter design concepts in real world applications.

Question paper pattern:

- 1. Question paper consists of 10 questions.
- 2. Each full question carrying 14 marks.
- 3. Each full question will have sub question covering all topics under a unit.
- 4. The student will have to answer 5 full questions selecting one full question from each unit.

Text Books:

- 1. Network Analysis ME Van Valkenburg, Prentice Hall of India, 3rdEdition,2000.
- 2. Network Analysis by K.Satya Prasad and S Sivanagaraju, CengageLearning
- 3. Electric Circuit Analysis by Hayt and Kimmarle, TMH

Reference Books:

- 1. Network lines and Fields by John. D. Ryder 2ndedition, Asiapublishinghouse.
- 2. Basic Circuit Analysis by DR Cunninghan, Jaico Publishers. 3.Network Analysis and Filter Design by Chadha,UmeshPublications.

Course Outcomes to Program Outcomes mapping:

CO	P01	P02	PO3	P04	PO5	PO6	P07	PO8	P09	P010	P011	P012	PSO1	PSO2	PSO3
1	3	3	-	-	-	-	-	-	-	-	-	-	-	-	3
2	3	3	-	-	-	-	-	-	-	-	-	-	-	-	3
3	2	2	3	-	2	-	-	-	-	-	-	-	-	-	3
4	3	2	3	-	3	-	-	-	-	-	-	-	-	-	2
5	3	3	3	-	2	-	-	-	-	-	-	-	-	-	2
6	3	2	3	-	3	-	-	-	-	-	-	-	-	-	3
Course	3	3	2	-	2	-	-	-	-	-	-	-	-	-	3

S.No.	Unit Name	Text Book/ Reference	Chapter No.
1.	Fundamentals and Network Topology	T2 &R1	1
2.	Electric Circuits and Network Theorems	T2&R1	2 &3
3.	Steady State Analysis of A.C Ckts & Transient	T2,T1,R2	4,5 &6
4.	Resonance and Coupled Circuits	T2,R2	6,7& 8
5.	Two-port Networks	T1	4 & 5

ENGLIS	H LANG	UAGE COMM SEMESTE	UNICATION SKIL	LS LAB
Subject Code		18CMEGL2060	IA Marks	15
Number of Practic	al			
Hr./week	ui	02	Exam Marks	35
Total Number of Pract	ical Hr	32	Exam Hours	03
		Credits – 01		
Objectives: To enable Speaking, Reading an			ication skills of Listenin	ıg,
	Comprehen			
 Pronunciat 	-	51011		
		formal and Informa	1 Situations	
	-	nication Skills	i bituutions	
 Interperson Presentation 		ineation Skins		
List of Experiments	ni okiiis			
UNIT I: Listening Co	mprehensi	on		
UNIT II: Pronunciat			ım	
			tions & Dialogues, Com	municatio
at Workplace	5 5		8	
-	nal Commu	inication Skills- Gro	up discussions and deba	ites
UNIT V:Formal Pres			1	
Outcomes:				
By the end of the cou English by practicing			acquire basic Proficiency	y in
• • • •	Comprehen	•		
 Pronunciat 	-	31011		
Dialogues	.1011			
•	nal Commu	nication Skills		
Presentation				
	is and Deba	tec		
Learning Resources				
8		Manual for Underg	graduate Students by Orio	ent
BlackSwar	-		raduate Students by Off	CIII
• Ted Talks,	Interviews	with Achievers and	select movies	
• Toastmast	er's speeche	es and table topics		
• Book Revi	ews and me	ovie reviews		
• Exercises	in Spoken F	English Parts: I-III, C	CIEFL, Hyderabad.	
• Oxford Gu	ide to Effe	ctive Writing and Sp	eaking by John Seely	
• https://ww	w.ted.com/	tal <u>k</u>		

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	-	-	-	-	-	-	-	-	-	2	-	-
2	-	-	-	-	-	-	-	-	-	3	-	-
3	-	-	-	-	-	-	-	-	-	3	-	-
4	-	-	-	-	-	-	-	-	-	2	-	-
5	-	-	-	-	-	-	-	-	-	3	-	-
6	-	-	-	-	-	-	-	-	-	2	-	-

Course Outcomes Vs Program Outcomes Mapping

DASIC ELECTRIC	CAL ENGINEERING LAB	UKATUKI	
	(Common to All)		
	SEMESTER II		
Subject Code	21CMEEL2070	IA Marks	15
Number of Lecture Hours/Week	3P	Exam Marks	35
Total Number of Lecture Hours	36	Exam Hours	03
	Credits-1.5		
Course Objectives:	1		
This course will enable the stu		•,	
	network theorems for a given circ	ult.	
2. Analyze the performance of l	-		
3. Control the speed of DC moto			
4. Predetermine the efficiency D	DC machine.		
5. Analyze performance of three	e phase induction motor.		
6. Determine the regulation of a	n alternators.		
-			
List of Experiments(Any ten experi	iments must be conducted)		
1. Verification of Kirchoff's la			
2. Verification of Thevenin's T			
3. Verification of Norton's The			
4. Verification of Superposition			
5. Verification of Maximum Po			
6. Speed control of D.C. shunt			
7. Brake test on DC shunt moto			
8. Calibration of wattmeter.			
9. OC & SC tests on single-pha	ase transformer.		
10. Brake test on 1-phase Induct			
11. Brake test on 3-phase Induct			
12. Study experiment on Ear thin			
COURSE OUTCOMES:	x		
On completion of the course student	t will be able to:		
1. Verify the Kirchoff's laws.			
2. Verify network theorems for a	a given circuit.		
3. Control the speed of DC mot	-		
4. Analyze performance of singl			
5. Analyze performance of three	•		
• •	-		
6. Identify different types of ear	thing's		
Question paper pattern:			
Ten questions are given, and student	should choose one question (blind	l option), which carr	ies 50
narks in total.	should encode one question (bline	² ^c r ^{uon} , "mon our	
	dure including circuit diagrams an	nd model graphs.	
b. 15 marks for conduction of the	• •		
c. 10 marks for results and conclu	-		

COs / POs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
CO1			2												
CO2			2												
CO3			2												
CO4			2												
CO5			2												
CO6			2												
Overall Course			2												

COURSE-OUTCOMES-TO-PROGRAM-OUTCOMES-MAPPING:

ENGINE	ERING WORKSHOP LA	В	
	SEMESTER II		1
Subject Code	21ETMEL2080	IA Marks	15
Number of Lecture Hours/Week	L(0)+T(0)+P(3)	Exam Marks	35
Total Number of Lecture Hours	36	Exam Hours	3
	Credits – 1.5		
Course objectives: On completion of	of the course students shoul	d be able to	
1. Learn basic use of hand tool	s along with the techniques	s and methods appli	cable to
the carpentry trade			
2. Learn basic use of hand tool	s along with the techniques	s and methods appli	cable to
the fitting trade			
3. Learn basic use of hand tool	s along with the techniques	s and methods appli	cable to
the forging trade			
4. Learn basic use of hand tool	s along with the techniques	s and methods appli	cable to
the casting trade			
5. Learn basic use of hand tool	s along with the techniques	s and methods appli	cable to
the welding trade			
EXPERIMENTS			
1. Preparation of T Lap joint us	ing carpentry.		
2. Preparation of Cross Lap join	nt using carpentry.		
3. Preparation of Square fit usir	ng mild steel specimen.		
4. Preparation of V fit using mi	1		
5. Conversion of round rod to s		ion.	
6. Preparation of <i>S</i> hooks by for			
7. Preparation of green sand mo			
8. Preparation of green sand mo	1 I I		
9. Preparation of a Butt joint us			
10. Preparation of a Lap joint usi	ing arc Welding		
ADDITIONAL EXPERIMENTS		/ 1 . 1	
1. Preparation of electrical wirin	ng connections using wiring	g (one lamp controlle	ed by
one switch) Course outcomes: On successful co	mplation of this course, the	students will be ab	la to
1. Perform the joinery work of w			
2. Perform the joinery work of m	1 0 1	ry.	
3. Produce the required shaped m	1 0 0	a smithy	
4. Make the green sand moulds u	1 0	Commy.	
5. Fabricate different component	•		
Question paper pattern:	s using wording.		
Ten questions are given, and student	should choose one question	n (blind option), wh	ich
carries 50 marks in total.		· · · · · · · · · · · · · · · · · · ·	
a. 15 marks are allotted for procedur	e including circuit diagram	s and model graphs.	
b. 15 marks for conduction of the ex			
c. 10 marks for results and conclusion	-		
d. 10 marks for viva voce.			

COs / POs	P01	P02	P03	P04	P05	904	707	PO8	P09	PO10	P011	P012	PS01	PSO2
CO1	2								2					
CO2	2								2				2	
CO3	2								2				2	
CO4	2								2				2	
CO5	2								2					
CO6	1								1				1	
Cours e	2								2				2	

COs vs POs MAPPING (HIGH: 3; MEDIUM: 2; LOW: 1)

ENVIRONMENTAL SCIENCE

SEMESTER II

	SEMESTER II		
Subject Code	21CMCHN2090	IA Marks	30
Number of Lecture Hours/Week	2	Exam Marks	70
Total Number of Lecture Hours	32	Exam Hours	03
	Credits – 00		
COURSE OBJECTIVES:			
 The objectives of this course, help the Acquire knowledge on global Learn different types of natura Create awareness on biodivers Gain scientific knowledge on a Acquire knowledge on water c Unit -1	environmental challenges l resources ity and ecology. environmental pollution conservation methods and	environmental leg	gislation Hours
MULTIDISCIPLINARY NATURE Environment - Definition, Introducenvironmental challenges, global warr layer depletion - Role of Information health.	uction - Scope and Imponing & climate change -	ortance - Global Acid rains, ozone	6
Unit -2			

Unit -2

NATURAL RESOURCES Renewable and non-renewable resources – Natural resources and associated problems – Forest resources – Use, deforestation - Timber extraction – Mining, dams and other effects on forest and tribal people Water resources – Floods, drought, , dams – benefits and problems Mineral resources: Use and exploitation, environmental effects of extracting and using mineral resources. Food resources: Effects of modern agriculture - fertilizer-pesticide problems, water logging, eutrophication, biological magnification and salinity. Energy resources: Renewable and non-renewable energy resources

Role of an individual in conservation of natural resources.	
Unit – 3	
ECOSYSTEM AND BIODIVERSITY	
Ecosystem - Concept of an ecosystem Structure and function of an ecosystem Producers, consumers and decomposers Energy flow in the ecosystem - Food chains, food webs and ecological pyramids Introduction, types, characteristic features, structure and function of the Forest and grassland ecosystem.	8
Biodiversity - Introduction - Definition: genetic, species and ecosystem diversity. – Value of biodiversity: consumptive use, productive use, social, ethical and optional values - Hot-spots of biodiversity - Threats to biodiversity: habitat loss - Endangered and endemic species of India – Conservation of biodiversity: In-situ and Ex-situ conservation of biodiversity.	
Unit – 4	
ENVIRONMENTAL POLLUTION	
Definition, Cause, effects and control measures of :	
a. Air pollution	
b. Water pollution	
c. Soil pollution	6
d. Noise pollution	
e. Nuclear hazards	
Solid waste Management: Causes, effects and control measures of urban and industrial wastes - Role of an individual in prevention of pollution.	
Unit – 5	
SOCIAL ISSUES AND THE ENVIRONMENT	
Urban problems related to energy -Water conservation, rain water harvesting, Resettlement and rehabilitation of people its problems and concerns. Environment Protection Act - Air (Prevention and Control of Pollution) Act. – Water (Prevention and control of Pollution) Act -Wildlife Protection Act -Forest Conservation Act .	6
COURSE OUTCOMES:	

On completion of the course student will be able to

- 1. Obtain knowledge on global warming & climate change Acid rains, ozone layer depletion.
- 2. Preserve several natural resources
- 3. Summarize the concept of ecosystem
- 4. Control different types of pollution
- 5. Understand social issues and environmental legislation

Question paper pattern:

- 1. Question paper consists of 10 questions.
- 2. Each full question carrying 14 marks.
- 3. Each full question will have sub question covering all topics under a unit.
- 4. The student will have to answer 5 full questions selecting one full question from each unit.

TEXT BOOKS:

- 1. E. Bharucha (2003), "Environmental Studies", University Publishing Company, New Delhi.
- 2. J.G. Henry and G.W. Heinke (2004), "Environmental Science and Engineering", Second Edition, Prentice Hall of India, New Delhi.
- 3. G.M. Masters (2004)" Introduction to Environmental Engineering and Science", Second Edition, Prentice Hall of India, New Delhi

REFERENCE BOOKS:

- 1. Text Book of Environmental Studies by Deeksha Dave & P. Udaya Bhaskar, Cengage Learning.
- 2. Environmental Studies by K.V.S.G. Murali Krishna, VGS Publishers, Vijayawada.
- 3. Environmental Studies, P.N. Palaniswamy, P. Manikandan, A. Geeta and K. Manjula Rani, Pearson Education, Chennai.

COURSE OUTCOMES TO PROGRAM OUTCOMES MAPPING:

CO	P01	P02	P03	P04	P05	P06	P07	PO8	P09	P010	P011	P012	PS01	PSO2	PSO3
1	-	-	-	-	-	-	3	-	-	-	-	-	-	-	-
2	-	3	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	-	-	-	-	-	-	-	-	-	-	-	-	-	-
4	-	-	3	-	-	-	-	-	-	-	-	-	-	-	-
5	-	3	-	-	-	-	-	-	-	-	-	-	-	-	-
Cou rse	2	3	2	-	-	-	2	-	-	-	-	-	-	-	-

MATHEMATICS-III

(Vector Calculus and Complex analysis)

(Syllabus for the Academic Year 2021 -2022)

Common to CE, EEE, ME, ECE and ECT

SEMESTER - II/I 21CMMAT3010/20 Subject Code IA Marks 30 70 Number of Lecture Hours/Week 3 Exam Marks 03 Total Number of Lecture Hours 48 Exam Hours Credits - 03 **Course Objectives:** To Interpret the physical meaning of different operators such as gradient, curl and divergence. • To Estimate the work done against a field, verify integral theorems. To apply Cauchy-Riemann equations to complex functions in order to determine whether a given continuous function is analytic To find the differentiation and integration of complex functions used in engineering problems. • To make use of the Cauchy residue theorem to evaluate certain integrals. Unit -1 **Vector Differentiation:** Gradient– Directional derivative – Divergence – Curl -Hours – 08 Scalar Potential. Unit -2 Vector Integration: Line integral - Work done - Area - Surface and volume Hours – 10 integrals - Vector integral theorems: Greens, Stokes and Gauss Divergence theorems (without proof) and problems on above theorems. Unit – 3 Function of a complex variable Introduction –continuity –differentiability- analyticity – properties – Cauchy – Hours – 10 Riemann equations in Cartesian and polar coordinates. Harmonic and conjugate harmonic functions - Milne - Thompson method. Unit – 4 Integration and series expansions Complex integration: Line integral – Cauchy's integral theorem, Cauchy's in Hours – 10 integral formula, generalized integral formula (all without proofs) Radius of convergence – expansion in Taylor's series, Maclaurin's series and Laurent series. Unit – 5 **Singularities and Residue Theorem** Zeros of an analytic function, Singularity, Isolated singularity, Removable singularity, Essential singularity, pole of order m, simple pole, Residues, Residue Hours – 10 theorem, Calculation of residues, Residue at a pole of order m, Evaluation of real definite integrals: Integration around the unit circle, Integration around semi circle. **Course outcomes:**

On completion of this course, students are able to

- 1. Interpret the physical meaning of different operators such as gradient , curl and divergence(L5)
- 2. Estimate the work done against a field, and verify integral theorems (L5)
- 3. apply Cauchy-Riemann equations to complex functions in order to determine whether a given continuous function is analytic (L3)
- 4. find the differentiation and integration of complex functions used in engineering problems(L3)
- 5. make use of the Cauchy residue theorem to evaluate certain integrals (L3)

Question paper pattern:

Question paper consists of 10 questions.

- 9. Each full question carrying 14 marks.
- 10. Each full question will have sub question covering all topics under a unit.
- 11. The student will have to answer 5 full questions selecting one full question from each unit.

Text Books:

1. B. S. Grewal, Higher Engineering Mathematics, 44th Edition, Khanna Publishers.

2. B. V. Ramana, Higher Engineering Mathematics, 2007 Edition, Tata Mc. Graw Hill Education.

Reference Books:

1. Erwin Kreyszig, Advanced Engineering Mathematics, 10th Edition, Wiley-India.

- 2. Srimantha Pal, S C Bhunia, Engineering Mathematics, Oxford University Press.
- 3. J. W. Brown and R. V. Churchill, Complex Variables and Applications, 9th edition,

4. N.P.Bali and Manish Goyal, "A text book of Engineering mathematics", Laxmi publications, 7th Edition.

5. H.K. Dass and Er. RajnishVerma, "Higher Engineerig Mathematics", S.Chand publishing, 1st edition, 2011.

Cour	rse Struc	cture for II B.7	Sech ECT Under the Regulation	ons of	f S	ITE	-21				
Semester -III											
S.No	Course Code	Subject Code	Course	L	Т	Р	С				
1	BS	21ECMAT3010	Engineering Mathematics - III	3	0	0	3				
2	BS	21ETETT3020	Probability Theory & Stochastic Processes	3	0	0	3				
3	PC	21ETETT3030	Semiconductor Devices	3	0	0	3				
4	PC	21ETETT3040	Digital System Design	3	0	0	3				
5	PC	21ETETT3050	Signals & Systems	3	0	0	3				
6	PC	21ETETL3060	Semiconductor Devices Lab	0	0	3	1.5				
7	PC	21ETETL3070	Digital System Design Lab	0	0	3	1.5				
8	PC	21ETETL3080	Electrical circuits Lab	0	0	3	1.5				
9	SOC	21ETETS3090	Skill Oriented Course –I (Data Science Using Python) OR (MATLAB FOR Engineers)	1	0	2	2				
	·	·	TOTAL	•		•	21.5				

Coui	rse Struc	cture for II B.T	ech ECT Under the Regulation	is of	S]	TE	-21				
Semester -IV											
S.No	Course Code	Subject Code	Course	L	Т	Р	С				
1	PC	21ETETT4010	Management Science	3	0	0	3				
2	PC	21ETETT4020	EM Waves & Transmission Lines	3	0	0	3				
3	PC	21ETETT4030	Principles of Communication Theory	3	0	0	3				
4	PC	21ETETT4040	Electronic Circuit Analysis	3	0	0	3				
5	HS	21CMMST4050	Control Systems	3	0	0	3				
6	PC	21ETETL4060	Principles of Communication Lab	0	0	3	1.5				
7	PC	21ETETL4070	Electronic Circuit Analysis Lab	0	0	3	1.5				
8	PC	21ETETL4080	Signals and systems Lab	0	0	3	1.5				
9	SOC	21ETETS4090	Skill Oriented Course -2 (Internet of Things (IOT)) OR (PCB Designing)	1	0	2	2				
10	MC	21ETETN40A0	Pulse and Digital Circuits	2	0	0	0				
	·	·	Total				21.5				
11		21ETETHXXX 21ETETMXXX	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also	4	0	0	4				

PROBABILIT	Y THEORY & STOCHASTIC P Common to ECE & ECT	ROCESSES	
	SEMESTER III	1	1
Subject Code	21ECECT3020, 21ETETT3020	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
Pre-requisite	Engineering Mathematics	Credit	$\frac{1}{8-03}$
 Course Objectives: This course will enable students To understand the conce To apply statistical opera To extend the concept of statistical operations and To characterize the rando To characterize the rando systems with random inp Unit -1 Review of Probability Theor Definition of a Random Varial Variable, Discrete, Continuous	pts of Probability Theory and Rando ations and transformations on one Ra one random variable to multiple ran transformations on multiple Rando om processes in the time domain. om processes in the frequency doma	om Variables. andom Variable. ndom variables a m Variables. in and analyze t oility Theory, be a Random stribution and	and Apply
Operation on One Random V Value of a Random Variable, Fu Origin, Central Moments, V Characteristic Function, Mome Random Variable: Monotonic	Yariable – Expectations: Introduction enction of a Random Variable, Mome Yariance and Skew, Chebychev's ent Generating Function, Transform Transformations for a Continu- nsformations of Continuous Random ndom Variable.	ents about the s Inequality, mations of a ous Random	10
Unit – 3			
Function, Properties of Joint Statistical Independence, Sum of Variables, Central Limit Theore Operations on Multiple Rand Joint Central Moments, Joint Cl Variables: Two Random Variab Transformations of Multiple Ran	Vector Random Variables, Joint Distribution, Marginal Distribution Two Random Variables, Sum of Sev m: Unequal Distribution, Equal Dist om Variables : Joint Moments about maracteristic Functions, Jointly Gauss oles case, N Random Variables case ndom Variables.	on Functions, veral Random tributions. ut the Origin, ssian Random	10
Unit – 4			
Classification of Processes, I Distribution and Density Fund	l Characteristics: The Random Pro Deterministic and Nondeterministic ctions, Concept of Stationarity and onary Processes, Second-order and	ic Processes, nd Statistical	10

Function and its Properties, Covariance Functions, Gaussian Random Processes,	
Poisson Random Process.	
Unit – 5	
Random Processes – Spectral Characteristics: The Power Density Spectrum: Properties, Relationship between Power Density Spectrum and Autocorrelation Function, The Cross-Power Density Spectrum, Properties, Relationship between Cross-Power Density Spectrum and Cross-Correlation Function. Linear Systems With Random Inputs: Random Signal Response of Linear Systems: SystemResponse – Convolution, Mean and Mean-squared Value of System Response, Autocorrelation Function of Response, Cross-Correlation Functions of Input and Output, Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectra of Input and Output.	10
Total	50
 On completion of the course student will be able to Understand the concepts of Probability Theory and Random Variables. Apply statistical operations and transformations on one Random Variable. Extend the concept of one random variable to multiple random variables and statistical operations and transformations on multiple Random Variables. Characterize the random processes in the time domain. Characterize the random processes in the frequency domain and analyze the with random inputs. Text Books: Peyton Z. Peebles, Probability, "Random Variables & Random Signal Principle Edition, TMH, 2001. Papoulis and S.Unnikrisha, "Probability, Random Variables and Stochastic ProEdition, PHI, 2002. 	LTI systems les", 4 th
 Reference Books: 1. Henry Stark and John W. Woods, "Probability and Random Processes with App Signal Processing", 3rd Edition, Pearson Education. 2. Gardener W.A, "Introduction to Random Processes with Applications to Signals Systems", 2nd Edition, McGraw-Hill. Web References: https://nptel.ac.in/courses/117105085/ https://ocw.mit.edu/courses/mathematics/18-440-probability-and-random-variab 2014/ 	s and

CO	P01	P02	PO3	P04	PO5	PO6	P07	PO8	P09	P010	P011	P012	PSO1	PSO2	PSO3
1	3	2	-	-	-	-	-	-	-	-	-	-	-	3	-
2	3	2	-	-	-	-	-	-	-	-	-	-	-	1	-
3	3	2	-	-	-	-	-	-	-	-	-	-	-	1	-
4	3	2	-	-	-	-	-	-	-	-	-	-	-	1	-
5	3	2	-	-	-	-	-	-	-	-	-	-	-	3	-
6	3	1	-	-	-	-	-	-	-	-	-	-	-	2	-
Course	3	2	-	-	-	-	-	-	-	-	-	-	-	2	-

S.No.	Unit Name	Text Book / Reference	Chapter No.
		T1	1 & 2
1	Review of Probability Theory & The	T2	1,2 & 4
1	Random Variable	R1	1 & 2
		R2	1 & 2
		T1	3
2	Operation on One Random Variable – Expectations	T2	5
		R1	3 & 4
		T1	4 & 5
3	Multiple Random Variables	T2	6
		R2	4
4	Random Processes – Temporal	T1	6
-	Characteristics	T2	9,12 & 14
5	Random Processes – Spectral Characteristics & Linear Systems With Random Inputs	T1	7 & 8

CONDCUTOR DEVICES			
SEMESTER III			
21ECECT3030, 21ETETT3030	Internal Ma	arks	30
03	External M	larks	70
50	Exam Hou	rs	03
Engineering Physics	Credits – 0	3	
conductor physics are to be reviewe	d.		
		d elec	trical
n of different semiconductor device	es.		
		cs with	n and
	and their cha	aracter	istics
a operation of anterent fransistors		ai de te i	150105
og and its significance is explained			
ig und its significance is explained.		Hoi	irs
hand diagram of Insulators Semi-	conductors	1100	41 5
	-	00	9
		0,	
· · · · · · · · · · · · · · · · · · ·			
Open circuited p-n junction. B	liased p-n		
1 0			-
		1()
.			
olar Junction transistor, transisto	or current		
-			
-		1	1
• • • •	-		
ar supply. Block diagram of regula			
er suppry. Dioek diagram of regula	ited power		
ve rectifier, bridge rectifier, rectifier ms, different parameters of rectifier	er circuits-	1	1
ve rectifier, bridge rectifier, rectifier ms, different parameters of rectifier	er circuits- s, Inductor	1	1
ve rectifier, bridge rectifier, rectifier ms, different parameters of rectifier lter, Π- section filter, Multiple L- s	er circuits- s, Inductor ection and	1	1
ve rectifier, bridge rectifier, rectifier ms, different parameters of rectifier	er circuits- s, Inductor ection and	1	1
	03 50 Engineering Physics conductor physics are to be reviewe ena such as conduction, transport mon diodes. n of different semiconductor devices rectifiers with their operation and . d operation of different Transistors ng and its significance is explained. band diagram of Insulators, Semi-conductors, electrons and trinsic semi-conductors, carrier obility and resistivity; charge denuity equations, law of junction, Fed extrinsic Semiconductors, Hall eff Open circuited p-n junction, B nt components in PN junction Dide perature dependence on V-I charastion and diffusion capacitance, end Breakdown mechanisms, Construer diode and Tunnel Diode, Zener of transistor configurations, commuter configurations, Ebers-Moll r olar Junction transistor, transistor transistor configurations, commuter of through, typical transistor, construer through, typical transistor, construer through, typical transistor, construer through, characteristics, construer through t	Common to ECE & ECT SEMESTER III 21ECECT3030, 21ETETT3030 Internal Ma 03 External M 50 Exam Hour Engineering Physics Credits – 0 conductor physics are to be reviewed. ena such as conduction, transport mechanism ar on diodes. n of different semiconductor devices. a rectifiers with their operation and characteristi	Common to ECE & ECT SEMESTER III 21ECECT3030, 21ETETT3030 Internal Marks 03 External Marks 50 Exam Hours Engineering Physics Credits – 03 onductor physics are to be reviewed. ena such as conduction, transport mechanism and elector on diodes. n of different semiconductor devices. erectifiers with their operation and characteristics with . . doperation of different Transistors and their character ag and its significance is explained. Hor band diagram of Insulators, Semi-conductors nsic semiconductors, Carrier transport: obility and resistivity; charge densities in nuity equations, law of junction, Fermi Dirac d extrinsic Semiconductors, Hall effect. 09 Open circuited p-n junction, Biased p-n nt components in PN junction Diode, diode perature dependence on V-I characteristics, ition and diffusion capacitance, energy band Breakdown mechanisms, Construction and r diode and Tunnel Diode, Zener diode as a 10 olar Junction transistor, transistor current transistor configurations, common Base, lector configurations, Ebers-Moll model of a through, typical transistor junction voltage n, operation, characteristics, Construction and 11

Transistor Biasing and Thermal Stabilization : Need for biasing, operating	
point, load line analysis, BJT biasing- methods, basic stability, fixed bias,	
collector to base bias, self-bias, Stabilization against variations in V_{BE} , I_{co} , and β , 09	
Stability factors, (S, S', S''), Compensation Techniques, Thermal runaway,	
Thermal stability. FET Biasing- methods and stabilization.	
Course outcomes:	
On completion of the course student will be able to	
1. Understand the basic concepts of semiconductor physics.	
2. Understand the different diodes and its behavior.	
3. Understand the construction, principle of operation of Bipolar junction Transistor and	ł
their V-I characteristics in different configurations.	
4. Understand the construction, principle of operation and characteristics of FET, UJT and	ł
SCR.	
5. Know the construction, working principle of rectifiers with and without filters with	ı
relevant expressions and necessary comparisons.	
6. Know the need of transistor biasing, various biasing techniques for BJT and FET and	I
stabilization concepts with necessary expressions.	
Text Books:	
1. Jacob Millman, C. Halkies, C.D.Parikh, "Integrated Electronics", Tata Mc-Graw Hill	•
2009.	·
2. G. Streetman and S. K. Banerjee, "Solid State Electronic Devices", 2 nd edition, Pearson	
2014.	<i>,</i>
Reference Books:	
1. Robert L Boyelstad, LovisNashelsky, "Electronic Devices & Theory", 10th edition	
2. David A Bell, "Electronic Devices and Circuits", 5 th Edition, Oxford Publications	
3. J. Millman, C. Halkias, "Electronic Devices and Circuits", 3rdEdition, Tata Mc-Graw	/
Hill.	
4. Salivahanan, Kumar, Vallavaraj, "Electronic Devices and Circuits", 2 nd Edition, Tata	ì

Mc-Graw Hill.

Course Outcomes to Program Outcomes mapping:

CO	PO1	P02	PO3	PO4	PO5	904	PO7	PO8	60d	P010	P011	P012	PS01	PSO2	PSO3
1	3	3	3	-	-	-	-	-	-	-	-	-	-	-	-
2	3	2	-	-	-	-	-	-	-	-	-	-	2	-	2
3	3	2	-	-	-	-	-	-	-	-	-	-	2	-	2
4	3	2	-	-	-	-	-	-	-	-	-	-	2	-	2
5	2	1	1	-	-	-	-	-	-	-	-	-	1	-	-
6	3	3	2	-	-	-	-	-	-	-	-	-	-	-	-
Course	3	2	1	-	-	-	-	-	-	-	-	-	1	-	1

C N-	These Names	Text Book/	Chapter
S.No.	Unit Name	Reference	No.
		T1	2
1.	Semi-Conductor Physics	T2	3
		R4	4
2.	Junction Diode Characteristics	T1	3
		T1	5,10,18
3.	Transistor Characteristics	T2	6,7
		R3	7,12
4.	Building blocks of regulated power supply	T1	4
4.	Building blocks of regulated power suppry	R1	2
5.	Transistor Dissing and Thormal Stabilization	T1	9
5.	Transistor Biasing and Thermal Stabilization	R2	5

DIGITAL SYSTEM DESIGN

Common to ECE & ECT SEMESTER III

Subject Code	21ECECT3040, 21ETETT3040	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
Pre-requisite		Credits – 03	

Course Objectives:

This course will enable students to

- Learn the basic concepts of number systems and their conversions
- Learn the Boolean algebra and digital logic minimization techniques
- Understand the VHDLprogramming for the design and implementation of combinational logic circuits
- Design and analyze Sequential logic circuits
- Design various digital systems with the help of FSM using HDL

- Design various argitar systems with the help of 1 biv using TDL	
Unit -1	Hours
Digital Fundamentals : Analog Vs Digital; Merits of Digital System; Number	
systems; Base conversions; Number representations: Binary, Integer and Floating	
point; Complements of numbers; Weighted and Unweighted codes; Boolean	
algebra; Logic gates; Canonical and Standard forms; Minimization and realization	08
of switching functions using Boolean theorems, NAND – NAND, NOR-NOR	
Implementations.	
Unit -2	
Combinational Logic Design-I: K-Map (up to 5 variables), Design of Half	
adder, full adder, half subtractor, full subtractor, applications of full adders; 4-bit	
adder-subtractor circuit, BCD adder circuit, Excess 3 adder circuit and carry look-	
ahead adder circuit, Design code converts using Karnaugh method and draw the	10
complete circuit diagrams.	
VHDL Coding styles: Lexical Conventions, Basic Architecture, Operators, Gate	
Level Modelling, Data Flow Modelling and Behavioral level Modelling of Adders	
and code converters	
Unit -3	
Combinational Logic Design - II: Design of encoder, decoder, multiplexer and	
de-multiplexers, Implementation of higher order circuits using lower order	
circuits. Realization of Boolean functions using decoders and multiplexers.	
Design of Priority encoder, 4-bit digital comparator and Seven segment decoder.	12
INTRODUCTION OF PLD's : PROM, PAL, PLA -Basics structures,	12
realization of Boolean functions, Programming table Gate Level Modelling, Data	
Flow Modeling and Behavioral level Modelling of decoders, encoders,	
multiplexers and de-multiplexers using VHDL	
Unit – 4	
Sequential Logic Design: Operation of NAND & NOR Latches and flip-flops;	
truth tables and excitation tables of RS flip-flop, JK flipflop, T flip-flop, D flip-	10
flop with reset and clear terminals. Conversion of flip- flops. Design of ripple	10
counters, design of synchronous counters, Johnson counter, ring counter. Design	
of registers - shift register, universal shift, register.	

Gate Level Modeling and Behavioral level Modeling of counters and registers	
using VHDL	
Unit – 5	
Finite State Machines, Logic Families: State diagram, state assignment, state	
minimization, Design of Mealy and Moore FSM - Sequence Detection.	
Behavioral level Modeling of Mealy and Moore FSM using VHDL	10
Logic Families:	
Characteristic parameters, Transistor-Transistor logic, TTL subfamilies, CMOS	
logic family, Implementation of Boolean function using CMOS logic	
Total	50
Course outcomes:	
On completion of the course student will be able to	
1. Understand the basic number systems and conversions.	
2. Apply the Boolean algebra to optimize the logic functions using K-maps an	d to
understand the basic concepts of VHDL.	
3. To design and analyze combinational logic circuits, PLDs	
4. To design and analyze sequential logic circuits.	
5. To design combinational and sequential logic circuits using mealy and more	machines
Text Books:	
1. Morris Mano, Michael D Ciletti, "Digital Design", 4 th Edition, PEA	
2. John F. Wakerly, "Digital Design Principles & Practices", 3rdEdition PHL	/ Pearson
Education Asia, 2005.	
3. C.H. Roth Jr and L.L. Kinney, "Fundamentals of Logic Design", 7 th edition,	Cengage
Learning, 2014.	
Reference Books:	
1. W R.P. Jain, "Modern Digital Electronics", Tata McGraw-Hill, 4 th edition, 2008	
2. C.H. Roth Jr, "Digital System Design using VHDL", Indian Edition, Thomso	on Books,
2006.	
3. Stephen Brown, ZvonkocVranesic, "Fundamentals of Digital Logic with VHDL	Design",
TMH, 2nd Edition., IEEE Press, 2004.	
Simulation Books	• •.•
1. R.S.Sandige, M.L.Sandige, "Fundamentals of Digital and Computer Des	sign with
VHDL", TMH, First edition, 2012.	
2. J Baskar, "VHDL Primer", Prentice Hall, 3 rd edition, 2002.	

	SIGNALS & SYSTEMS Common to ECE & ECT						
	SEMESTER III						
Subject Code21ECECT3050, 21ETETT3050Internal Marks							
Number of Lecture03External MarksHours/Week03External Marks							
Total Number of Lecture Hours	48	Exam Hours	03				
Pre-requisite	Engineering Mathematics-III	Credits – 03					
Course Objectives: This course will enable stude • Know the concepts of • Analyze frequency do		nd systems using tra	•				
Unit -1			Hours				
related functions-Exponentia Classification of Signals, Cla Analogy between vectors approximation using Orthog complete set of Orthogonal fu Unit -2 Fourier Series: Fourier Se	o Signals and Systems, Singula l and Sinusoidal signals. Oper ssification of Systems. and signals, Orthogonal sign gonal functions, Mean square unctions, Orthogonality in comp erries representation of continu- ns, Trigonometric Fourier serie	ations on Signals, nal space, Signal error, Closed or lex functions.	10				
Fourier series Representation Trigonometric and Exponenti Fourier Transform: Compl from Fourier series, Fourier t transforms Fourier transfor Transform.	ns, Properties of Fourier series,	Relation between Fourier transform operties of Fourier	12				
linear system, Linear Time In time domain and frequency Transfer function of a LTI sy Correlation: Cross-correlation correlation function, Energy density spectrum, Relation be spectral density function.	s: Introduction, Impulse respon totariant (LTI) systems. Concept domain, Graphical representation stem. on and Auto-correlation of funct y density spectrum, Parseval's etween auto correlation function Relation between Convolution in the presence of noise by Corr	tions, Properties of s theorem, Power and energy/power and Correlation.	10				
Sampling Theorem: Represe The Sampling theorem, Im- Reconstruction of signal from Introduction to Band Pass sam	entation of Band limited CT sig pulse sampling, Natural and I n its samples, effect of under sa npling. rms, Properties of Laplace Tra	Flat-top Sampling, ampling –Aliasing,	8				

hatman IT and FT of a signal		
between LT and FT of a signal. Unit – 5		
Z-Transforms: Discrete time Complex Exponential Periodicity properties of discrete time Complex Export Z-Transform of a discrete time sequence. Region of co Transform and Properties of ROC for various classes of Transforms. Inverse Z-Transform. Distinction between Transforms.	ential signal. Concept of onvergence (ROC) of Z- signals. Properties of Z-	8
Total		48
 Course outcomes: On completion of the course student will be able to Illustrate various signals and systems and their propriets Make use of Fourier analysis for frequency domains Solve the response of LTI system through Convolous Construct Sampling theorem for signal conversions Apply Z-Transform for the analysis of discrete-time Text Books: A.V. Oppenheim, A.S. Willsky and S.H. Nawab, PHI, 2009. B.P. Lathi, "Signal Processing & Linear Systems", 2006. A. Anand Kumar, "Signals and Systems", 3rd Editional conversions. 	n representation of signals ution and Correlation. n. <u>ne signals.</u> "Signals and Systems", 2 ⁿ 1 st Edition, Oxford Univers	sity Press,
Reference Books		
 Simon Haykin and Van Veen, "Signals & Syste 2011. M. J. Roberts, "Analysis using Transform method 2005. 		
Web References:		
 https://ocw.mit.edu/courses/electrical-engineering signals-and-systems-fall-2011/lecture-videos/ https://ocw.mit.edu/resources/res-6-007-signals-and- lectures/ https://ocw.mit.edu/resources/res-6-007-signals-and- notas/ 	nd-systems-spring-2011/vio	deo-
<u>notes/</u> <u>A https://pptol.ac.in/courses/117104074/</u>		

4. https://nptel.ac.in/courses/117104074/

SEMI	CONDUCTOR DEVICES LAB			
	Common to ECE & ECT			
	SEMESTER III			
Subject Code	21ECECL3060, 21ETETL3060	Internal Mar	ks	15
Number of Lecture Hours/Week	03	External Man	rks	35
Total Number of Hours	36	Exam Hours		03
		(Credits	- 1.5
Course Objectives:				
This course will enable students to				
	various passive components and ac		1	
• Study the operation of m CRO.	ultimeter, function generator, reg	ulated power	supply	y and
• Introduce the operation of	diodes and transistors.			
	soldering of different components	and wires.	r	
Unit -1 List of Experiments:			Ho	urs
 supply and Cathode Ray O 3. PN junction diode characteristic 4. Zener Diode Characteristic 5. Half-wave Rectifier with a 6. Full-wave Rectifier with a 7. Common Emitter configura 8. Common source configura 9. Transistor Biasing. 10. Soldering Practice. 	ultimeter, function generator, regu Descilloscope. eristics. cs. and without filter. nd without filter. ration: Input and Output characteri ation: Drain and Transfer character	stics. istics.	3	6
CRO.	ents. ultimeter, function generator, reg s of Semiconductor devices and cir	_	supply	y and

DIGI	TAL SYSTEM DESIGN LAB Common to ECE & ECT SEMESTER III						
Subject Code	21ECECL3070, 21ETETL3070	Internal Mark	S	15			
Number of Lecture Hours/Week	03	External Mar		35			
Total Number of Hours36Exam Hours							
	Credits – 1.5						
• Design various sequential	onal circuits using logic gates. circuits using logic gates.						
	DL in Digital systems design.						
List of Experiments:			Ho	urs			
 2. Design of code converters 3. Adders 4. Subtractors 5. Multiplexers. 6. Encoders 7. Decoders 8. D Flip-Flop. 9. Synchronous and Asynchronou 10. Shift registers. 11SRAM 12 Sequence Detector 13. ALU Design 	s counters		3	6			
 Course outcomes: On completion of the course stude 1. Design of Logic Gates and co 2. Design and analysis of basic a 3. Design and analysis of combination 4. Design and analysis of Sequenation 5. Design of memory elements 6. Design of complex logic circum 	de converters rithmetic logic circuits. national logic circuits						

ELF	CTRICAL CIRCUITS LAB Common to ECE & ECT SEMESTER III			
Subject Code		Internal Mark	7.0	15
Subject Code Number of Lecture Hours/Week	,	External Mar		35
Total Number of Hours			KS	03
Total Number of Hours		Exam Hours		05
Course Objectives:	creatts – 1.5			
This course will enable students to	2			
	f design and analysis of Electrical c	ircuits		
-	uits using various circuit analysis te			
 Anaryze the electrical circle Determine the transient rest 		chiliques		
	-			
	s and determine filter characteristics		IIa	urs
-	sign the electrical circuits to verify time response of AC circuits are		HO	urs
	time response of AC circuits ar Experimental results should be ve			
theoretical values.	Experimental results should be ve	anned with		
List of Experiments:				
-	ort network parameters and tran	sionts		
	ameters – Z-Y Parameters and			
verification.	$z_{1} = z_{1}$ randleters and	anaryticar		
	ters – Hybrid & ABCD parameters,	Analytical		
verification.	iers Tryona & Theed parameters,	7 mary ticar		
	& RC Networks for DC and AC Inp	uts		
	C Circuit for DC and AC inputs.	uts		
Part-B: Simulation of electrica				
	and verification of Kirchhoff's law	s for basic		
electrical networks.			3	6
	cal circuits and verification using	Kirchhoff's		
laws	1	77. 11. 60		
	cal circuits and verification using	Kirchnoff's		
laws	a's and Negton's assignation in			
	n's and Norton's equivalent circ	cuits using		
PSPICE. Verification on I				
9. Verification of Thevenii PSPICE. Verification on A	n's and Norton's equivalent circ	Luits using		
	C Circuits for DC and AC Inputs			
1				
11. Determination of Two por	1			
11. Determination of Two por 12. Low pass and High Pass F	1			
11. Determination of Two por 12. Low pass and High Pass F Course outcomes:	Tilter characteristics			
 Determination of Two por 12. Low pass and High Pass F Course outcomes: On completion of the course stude 	Tilter characteristics			
11. Determination of Two por	Filter characteristics ent will be able to AC linear circuits.			

		ILL ORIENTED COUR							
	Data Science using Python SEMESTER III								
Subie	ct Code	21ETETS3090	Internal Marks	0					
	ber of Lecture Hours/Week	02	External Marks	50					
	Number of Practical	32	Exam Hours	03					
	s/Week	52	Linum Hours	05					
	equisite	Technical English	Credits – 02						
	ourse Objectives:	Teeninear English	creates 02						
	he main objective of the cours	e is to inculcate the basic	understanding of Dat	a Science and					
	's practical implementation us								
	se Outcomes:								
	e completion of this laborator	v course, the students wil	l be able to						
	successful completion of the	-		Teaching					
•	Perform various operations			Hours					
•	Importing data from differen		as	32					
•	Draw different types of char	• •	u						
-	List of Experiments :	tis using mulpiotilo.							
	Creating a NumPy Array								
	a. Basic ndarray								
	b. Array of zeros								
1	c. Array of ones								
-	d. Random numbers	in ndarrav							
	e. An array of your c								
	f. Imatrix in NumPy								
	g. Evenly spaced nd	arrav							
	The Shape and Reshaping or								
	a. Dimensions of Nu								
	b. Shape of NumPy a								
2	c. Size of NumPy and								
	d. Reshaping a Num	-							
	e. Flattening a Num								
	f. Transpose of a Nu	mPy array							
	Expanding and Squeezing a								
3	a. Expanding a Num								
3	b. Squeezing a Num	Py array							
	c. Sorting in NumPy								
	Indexing and Slicing of Nur								
	a. Slicing 1-D NumF								
4	b. Slicing 2-D NumF								
	c. Slicing 3-D NumPy arrays								
	d. Negative slicing o	f NumPy arrays							
	Stacking and Concatenating	g Numpy Arrays							
5	a. Stacking ndarrays								
5	b. Concatenating nda	-							
	c. Broadcasting in N	umpy Array							
	Perform following operation	ns using pandas							
6	a. Creating datafram	e							
U	b. concat()								
	c. Setting conditions								

	d. Adding a new column
	Perform following operations using pandas
7	a. Filling NaN with string
,	b. Sorting based on column values
	c. groupby()
	Read the following file formats using pandas
	a. Text files
8	b. CSV files
	c. Excel files
	d. JSON files
	Read the following file formats
0	a. Pickle files
9	b. Image files using PIL
	c. Multiple files using Glob
	d. Importing data from database
10	Demonstrate web scraping using python
	Perform following preprocessing techniques on loan prediction dataset
	a. Feature Scaling
11	b. Feature Standardization
	c. Label Encoding
	d. One Hot Encoding
	Perform following visualizations using matplotlib
	a. Bar Graph
	b. Pie Chart
12	c. Box Plot
	d. Histogram
	e. Line Chart and Subplots
	f. Scatter Plot
	References:
	ps://www.analyticsvidhya.com/blog/2020/04/the-ultimate-numpy-tutorial-for-data-science-
begin	
	<u>os://www.analyticsvidhya.com/blog/2021/07/data-science-with-pandas-2-minutes-guide-</u>
	<u>y-concepts/</u>
	ps://www.analyticsvidhya.com/blog/2020/04/how-to-read-common-file-formats-python/
	os://www.analyticsvidhya.com/blog/2016/07/practical-guide-data-preprocessing-python-
<u>SCIKI</u>	-learn/

		ILL ORIENTED COURS							
	MATLAB FOR Engineers								
Subio	at Cada	SEMESTER III 18ETETS3090	Internal Marks	0					
2	ct Code per of Lecture Hours/Week	02	External Marks	50					
	Number of Practical	32	External Warks Exam Hours	03					
	s/Week	52	Lxam mours	05					
Pre-requisite Programming Skills Credits – 02									
	Course Objectives:								
	-	MATLAB as a programm	ing tool.						
		ching model that will help	-	ming skills					
		ve mathematical problems		C					
	• To understand MA	TLAB graphic feature and	its applications.						
	• To use MATLAB a	s a simulation tool.							
Cours	se Outcomes:								
	e completion of this laborator	•	be able to						
	derstand MATLAB environm	•							
	derstand MATLAB Functions		l built-in functions	Teaching					
	e graphics, 2D, 3D Plotting a			Hours					
	ogramme using MATLAB – c	onditional statements, prog	gramming and	32					
	bugging, applications.			-					
5. Un	derstand mathematical compu	iting with MATLAB							
	List	of experiments							
	Introduction to MATLAB								
1	a. The MATLAB Envi								
1		Variables, Numbers, Oper	ators, Expressions, I	nput and					
	output c. Vectors, Arrays – N	Antrinon							
	c. vectors, Arrays – N	Taurces							
	MATLAB Functions								
2	a. Built-in Functions								
	b. User defined Function	ons							
	Graphics with MATLAB								
2	a. Files and File Manag	gement – Import/Export							
3	b. Basic 2D, 3D plots								
	c. Graphic handling								
	Programming with MATL	AB							
4	a. Conditional Stateme	· •							
-	b. MATLAB Programs	- Programming and Debu	ıgging.						
	c. Applications of MA	FLAB Programming							
	Mathematical Computing	with MATLAR							
_	a. Algebraic equations								
5	•	culus and Differential equa	tions						
	c. Numerical Techniqu	-							
	1								

Reference Books:

- 1. "A Guide to MATLAB for Beginners and Experienced Users", 2nd Ed., Brian R. Hunt, Ronald L.
- Lipsman, Jonathan M. Rosenberg, Cambridge University Press, (2006).
- 2. "Essentials of MATLAB Programming", 2nd Ed., Stephen J. Chapman, Cengage Learning, (2009).
- 3. "MATLAB Demystified", David McMahon, The McGraw-Hill Companies, (2007).
- 4. "MATLAB® for Engineers", 3rd Ed., Holly Moore, Pearson Education, Inc., (2012).
- 5. "Engineering computation with MATLAB", 2nd Ed., David M. Smith, Pearson Education, Inc., (2010).

	AGEMENT SCIENCE to ECE,CSE,ECT,IT& EEI	()	
(Common)	SEMESTER IV	_)	
Subject Code	21CMMST4010	Internal Marks	30
Number of Lecture Hours/Week	03	External Mark	
Total Number of Lecture Hours	50	Exam Hours	03
Total Number of Lecture Hours	Credits – 03	LAdin Hours	05
Course objectives:			
This course will enable the students	to		
 Understand the concepts of 		importance Ma	nageme
theories and organization pri	-	importance, ivie	inageme
 Analyze the Work study, SQ 	±	nd its techniques	,
 Learn various concepts like 		-	
management.	FERT, CFWI and Floject cla	sining and recen	t trends i
Unit -I			Hours
Introduction to Management:	Concent noture and is	nnorton of	nours
Management – Functions of Mar			
thought- Theories of Motivation			10
organization structure- Principles			10
structure.	of organization - Types of	organization	
Unit –II			
Operations Management : Princip	las and Types of Layouts	Work study	
Statistical Quality Control- Control			
Simple problems- Material Manage		,	10
ABC analysis (simple problems) a	•		10
VED, and FSN analysis).	nd Types of ABC analysis	(IIIVIL, SDL,	
Unit-III			
Functional Management & Strate	gic Managamant:		
Functional Management: Concept	0 0	Functions of	
HRM - Marketing Management- Fur			
based on product Life Cycle, Channel		ing strategies	12
Strategic Management: Vision, 1		Elements of	12
Corporate Planning Process – Enviro			
in Strategy Formulation and Implem	6	• 1	
Unit –IV	ientation, Generie Strategy a	tornati v es	
Project Management: (PERT/CPM	Development of Networ	k – Difference	
between PERT and CPM Identif	· •		10
Crashing (Simple Problems).		ing inget	Ĩ
Unit-V			
Contemporary Management Pra	ctices: Basic concepts of I	MIS MRP	
Justin- Time (JIT) system, Total Q	1		
Supply Chain Management, Enterp		-	08
	less process Re-engineering		00
Process outsourcing (BPU) Busin			
_	F		
Marking, Balanced Score Card.			
Marking, Balanced Score Card. Course outcomes:			
Marking, Balanced Score Card. Course outcomes: On completion of the course student	t will be able to:	anagement & I	eadershi
Marking, Balanced Score Card. Course outcomes: On completion of the course student 1. Execute the functions of Ma	t will be able to:	anagement & L	Leadershi
Marking, Balanced Score Card. Course outcomes: On completion of the course student	t will be able to: anagement, Principles of M	C	

- 3. Predict the Customer Behaviour and Employees Contribution towards success of Organization.
- 4. Identify different Strategies for the Development of the Organization.
- 5. Analyze Project Management Techniques like CPM, PERT and Crashing.
- 6. Apply various contemporary issues in Management Practices like TQM, Business Process Reengineering and BPO etc.

Question paper pattern:

Section A:

- 1. This section contains ten one or two line answer question carrying 1 mark each.
- 2. Two questions from each unit should present.

Section B:

- 1. This Section will have 10 questions, 2 from each unit
- 2. Each full question carry 12 marks.
- 3. Each full question will have sub question covering all topics under a unit.
- 4. The student will have to answer 5 full questions selecting one full question from each unit

Text Books:

- 1. Dr. A. R. Aryasri Management Science, TMH 2011.
- 2. Dr. P.G.Ramanujam, Dr. B.V.R.Naidu and Prof. P.V.Rama Sastry: Management Science, Himalaya Publishing House 2013.

Reference Books:

- 1. Koontz & Weihrich: 'Essentials of Management' TMH 2011.
- 2. Seth & Rastogi: Global Management Systems, Cengage Learning, Delhi, 2011.
- 3. Robbins: Organizational Behaviors, Pearson Publications, 2011

Web References:

- 1. https://www.managementstudyguide.com/management_principles.htm
- 2. https://businessjargons.com/strategic-management.html

СО	PO1	P02	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	P011	P012	PS01	PSO2	PSO3
1	-	-	-	-	-	-	-	-	3	-	-	-	-	-	-
2	-	-	-	-	1	-	-	-	-	-	-	-	-	-	-
3	-	-	-	-	-	-	-	-	1	-	-	-	-	-	-
4	-	-	-	-	-	1	-	-	-	-	-	-	-	-	-
5	-	-	-	-	-	-	-	-	-	-	2	-	-	-	-
6	-	-	-	-	1	-	-	-	-	-	-	-	-	-	-
Course	-	-	-	-	1	1	-	-	3	-	2	-	-	-	-

S.No.	Unit Name	Text Book Reference	Chapter No.
1	Introduction to Management	T1	1&3
1		T2	1
2	Operations Management	T1	7,8,10&15
2	Operations Management	T2	2
3	Eurotional Management & Strategia Management	T1	11,12 &14
5	Functional Management & Strategic Management	T2	3 & 5
4	Design Managements (DEDT/CDM)	T1	16 & 17
4	Project Management: (PERT/CPM)	T2	4
		T1	20
5	Contemporary Management Practices	T2	8

ELECTROMAGNETIC WAVES AND TRANSMISSION LINES Common to ECE & ECT SEMESTER IV

Subject Code	21ECECT4020, 21ETETT4020	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
Pre-requisite	Engineering Physics	Credits – 03	

Course Objectives:

This course will enable students to

- To gain conceptual and basic mathematical understanding of electrical and magnetic fields in free space and in materials with the help of Maxwell equations
- To understand wave propagation in lossless and in lossy media
- To introduce the various types of transmission lines and to discuss the losses associated
- To provide thorough understanding about impedance transformation and matching.
- To give insight about the usage of Smith chart in problem solving

• To give insight about the usage of shiftin chart in problem solving	
Unit -1	Hours
TIME VARYING FIELDS AND MAXWELL'S EQUATIONS: Review of	
vector analysis and coordinate systems, Faraday's Law – Transformer and Motional	
Electromotive Forces – Displacement current – Generalized forms of Maxwell's	9
equation in final forms, Electromagnetic boundary conditions.	
Unit -2	
PROPAGATION OF UNIFORM PLANE WAVES: The wave equation-	
uniform plane waves, Plane waves in lossless media, Plane waves in lossy media	9
(low-loss dielectrics and good conductors), Group velocity, Electromagnetic power	
flow and Poynting vector	
Unit -3	
REFLECTION AND REFRACTION OF PLANE WAVES: Reflection and	
refraction of plane waves at plane boundaries under normal and oblique incidence	
on the surface of perfect dielectric, perfect conductor, Wave impedance.	
TRANSMISSION LINE THEORY : Transmission Line Model- Line of Cascaded	14
T sections, General theory of Transmission lines, Transmission line equations at	
radio frequencies, Primary and secondary constants, The infinite line - Input and	
transfer impedance, Waveform distortion, Distortion-less lines, methods of loading	
Unit – 4	
HIGH FREQUENCY TRANSMISSION LINES: Input impedance, Open and	
short circuited lines, wavelength, velocity of propagation, Reflection coefficient -	9
calculation of current, voltage and power delivered, Standing Wave Ratio,	
Reflection losses on unmatched line.	
Unit – 5	
IMPEDANCE MATCHING IN HIGH FREQUENCY LINES: Impedance	
matching: Quarter-wave line and applications, Smith chart – Smith circle equations,	
Determination of Load impedance, input impedance, Reflection coefficient, VSWR,	9
V _{min} and V _{max} using Smith chart Half-wave line, Impedance matching by stubs -	
Single stub and double stub matching, Single stub matching using Smith chart.	

Course outcomes:

On completion of the course student will be able to

- 1. Demonstrate knowledge and understanding of fundamental electromagnetic laws and concepts
- 2. Display an understanding of the effect of materials on electric and magnetic fields
- 3. Understand the EM wave propagation in a medium and through boundaries
- 4. Analyze the various types of transmission lines and to discuss the losses associated.
- 5. Comprehend the working of transmission line at radio frequencies
- 6. Analyze the problems in RF line and stub matching using Smith chart

Text Books:

- 1. D.K. Cheng, "Field and Wave Electro Magnetics", Pearson (India), 2 nd Edition, 1989.
- 2. John D Ryder, "Networks lines and fields", Prentice Hall of India, 2005

Reference Books:

- 1. W.H. Hayt and J.A. Buck, "Engineering electro magnetics", McGraw-Hill (India), 7 th Edition, 2006
- 2. E.C.Jordan and K.G. Balmain, "Electromagnetic Waves and Radiating Systems", PHI, 2011.
- 3. Mathew.N.O.Sadiku, "Elements of Electromagnetics", Oxford University Press, 6 th Edition, 2015.
- 4. Kraus, Fleisch, "Electromagnetics with Applications", McGraw-Hill, 5 th Edition, 2010.
- 5. R. K. Shevgaonkar, "Electromagnetic Waves", Tata McGraw Hill Publications, 2006
- 6. UmeshSinha, Transmission Lines and Networks: Networks, Filters & Transmission Lines, SathyaPrakash, 2010.

Course Outcomes to Program Outcomes mapping:

co	PO1	PO2	PO3	P04	PO5	P06	PO7	PO8	P09	P010	P011	P012	PS01	PSO2	PSO3
1	3	2			2										
2		2	1	2											
3		3	3	3									1		
4	2	1	1	1		1					2	2	2		
5	2	3	2			1					1	1	2		
6	3	3	3	3		1					1	1	2		
Course	2	3	2	2	1	1					1	1	1		

S.No.	Unit Name	Text Book/	Chapter No.	
		Reference		
1.	Time varying fields and Maxwell's equations	T1	5	
		R2	4	
2.	Propagation of uniform plane waves	T1	6	
		R2	5	
3.	Reflection and Refraction of plane waves	T1	6	
		R2	6	
4.	Transmission line theory	T2	3	
		R6	6	
5.	High frequency transmission lines	T2	9	
		R6	7	
		T2	9	
6.	Impedance matching in high frequency lines	R3	6	
		R4	5	

PRINCIPLES	OF COMMUNICATION THEORY		
	Common to ECE & ECT		
	IV SEMESTER		
Subject Code	21ECECT4030, 21ETETT4030	Internal	30
Number of Lecture Hours/Week	03	External	70
Total Number of Lecture Hours	50	Exam	03
Pre-requisite	Signals & Systems	Credits –	03
 Course Objectives: The student will be able to Understand the concept of modulation techniques. Understand Modulation & Understand Modulation & To acquire knowledge to an techniques. To understand the pulse modulation. Unit -1 Amplitude Modulation: Introduct modulation, Amplitude Modulation, single waves, Generation of AM waves	ion to communication system, Need for	'SB	- -
Ring Modulator, Coherent detection Loop, and Phase discrimination generating AM SSB Modulated wave	n of DSBSC Waves, Balanced Modulators, of DSB-SC Modulated waves, COSTAS method Frequency discrimination for es, Demodulation of SSB Waves, Vestigial nonstrate the use of digital formatting in ansmission.	1	0
Unit – 3			
Angle Modulation: Introduction, Modulation index for FM and PM FM, Narrow band and wide band H FM Waves: Balanced Frequence Comparison of AM, FM and PM. NOISE: Review of noise and nois communication Systems, Noise in Systems, Threshold effect in Angle emphasis	Mathematical analysis of FM and PM, , Frequency spectrum and bandwidth of FM, Direct, FM generation, Detection of cy discriminator, Phase locked loop, e sources, noise figure, Noise in Analog AM System, Noise in Angle Modulation Modulation System, Pre-emphasis & de-	10	0
Unit – 4			
The Low Pass Sampling Process Pul Modulation, Generation of PPM	N: Introduction, Digitize Analog Sources se Amplitude Modulation, Pulse-Position Waves, Detection of PPM Waves. nultiplexing, Time division multiplexing, d comparison.		0

Unit – 5PULSE DIGITAL MODULATION : The Quantization Random Process,
Quantization Noise, Pulse-Code Modulation: Sampling, Quantization,
Encoding, Regeneration, Decoding, Filtering, Differential PCM, Application
examples-, Video + MPEG, Vocoders.8

Course outcomes:

After going through this course the student will be able to

- 1. Understand the concept of modulation and amplitude modulation.
- 2. Differentiate various schemes of amplitude modulation and demodulation techniques.
- 3. Understand the fundamentals of angle modulation and demodulation techniques.
- 4. Analyze noise characteristics of various analog modulation methods.
- 5. Analyze the concept of pulse modulation schemes.
- 6. Design /Demonstrate the use of digital formatting in Multiplexers, Vocoders and Video transmission.

Text Books:

- 1. Simon Haykin, **"Principles of Communication Systems"**, 2nd Ed, John Wiley.
- Modern Digital and Analog Communication Systems –B.P. Lathi, Zhi Ding, Hari Mohan Gupta, Oxford University Press, 4th Edition, 2017

References Books:

- 1. B.P. Lathi, "Communication Systems", BS Publication, 2006.
- 2. Proakis J. G. and Salehi M., "Communication Systems Engineering", Pearson Education, 2002.
- 3. H. Taub and D. Schilling, "Principles of Communication Systems", TMH, 2003

Web References:

- 1. https://ocw.mit.edu/courses/electrical-engineering-and-computer-science/6-02introduction-to-eecs-ii-digital-communication-systems-fall-2012/lecture-videos/
- 2. https://nptel.ac.in/courses/117102059/
- 3. https://www.youtube.com/watch?v=TPm0XSPxld8

Course Outcomes to Program Outcomes mapping:

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
2	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
3	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
4	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
5	3	3	-	-	-	_	-	-	-	-	-	-	-	3	-
6	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
Course	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-

S.No	Unit Name	Text Book / Reference	Chapter No.
		T1	3
1	Amplitude Modulation	R1	3
		R2	3
		T1	3
2	DSB & SSB Modulation	R1	3
		R2	3
	Angle Modulation	T1	4
3	Angle Modulation	R1	4
5		T1	5
	Noise	R3	9
	Dulsa Analog Modulation	T1	6
4	Pulse Analog Modulation	R1	5
5	Dulse Digital Medulation	T1	7
3	Pulse Digital Modulation	R1	6

ELEC	FRONIC CIRCUIT ANALYSIS			
	Common to ECE & ECT			
	SEMESTER IV			
Subject Code	21ECECT4040, 21ETETT4040	Internal Ma	arks	30
Number of Lecture	03	External M	arks	70
Hours/Week				
Total Number of Lecture	50	Exam Hour	rs	03
Hours				
Pre-requisite	Electronic Devices	Credits – ()3	
Course Objectives:				
This course will enable the stude	ents to:			
• Understand analysis of s	nall signal BJT and FET amplifier	circuits		
•	nal high frequency amplifiers and the		Cascad	ing
on single stage amplifiers				U
• Understand the concept of	of feedback on amplifiers,			
• Understand the principle	L 1			
	lifferent Power amplifiers			
• Understand the concept of	of tuned amplifiers			
Unit -1			Ho	urs
Small Signal Low Frequency	Fransistor Amplifier Models:			
	stor hybrid model,h-parameters, co	nversion of		
1	ysis of transistor amplifier mode)
	and CC amplifiers, Comparison o	0	8)
amplifiers.				
FET: Small signal model of	a MOSFET, Analysis of CG, C	S and CD		
amplifiers, comparison of FET a	mplifiers.			
Unit -2				
Small Signal High Frequency	Fransistor Amplifier models:			
BJT: Transistor at high frequence	cies: Hybrid- π CE transistor mode	l, Hybrid π		
• •	ances, validity of hybrid π model			
	with resistive load, cut-off frequen	cies, single	1	2
• •	ponse and gain bandwidth product.			
	ce and common drain Amplifier cir	cuits at		
high frequencies				
Unit – 3 Feadback Amelifiance Classic	Continue of Association To 11 1			
	fication of Amplifiers, Feedback			
Method of analysis of feedback	naracteristics of negative feedback	ampimers,	2	0
•	cillations, RC-phase shift and W	ion bridge	4	
	is, General form of oscillator circu	-		
and Colpitts oscillators with BJT		in, marticy		
Unit-4	und undry 515.			
	er coupled Class A power Amplif	fier and its		
-				
efficiency. Class B amplituer	and its efficiency. Class AD			~
efficiency, Class B amplifier Complementary symmetry pus	•	-	1	0
•	h pull amplifier, Class-C power	-	1	0

capaci single amplif	Amplifiers : Introduction, Q-Factor, small signal tuned amplifier, tance single tuned amplifier, double tuned amplifiers, effect of cascading tuned amplifiers on band width, effect of cascading double tuned fiers on band width, staggered tuned amplifiers, stability of tuned fiers, wideband amplifiers	10
Cours	e outcomes:	
On con	mpletion of the course student will be able to:	
1.	Perform the analysis of small signal amplifier circuits using BJT and FE	Т
2.	Design small signal high frequency amplifiers and estimate the effects o	f cascading
3.	Design different types of feedback amplifier circuits	-
4.	Design a reliable amplifier and oscillator circuits	
5.	Design a power amplifier with the required efficiency	
6.	Design the tuned amplifiers and the effect of cascading	
Text I	Books:	
1.	Microelectronic Circuits - A.S. Sedra and K.C. Smith, 5 th edition	
2.	Integrated Electronics- Jacob Millman, C. Halkies, Tata McGraw Hill E	lectronic
Refer	ence Books:	
1.	Electronic Devices and Circuits -David A. Bell, 5th Edition Oxford Univ	ersity press
2.	Electronic Devices and Circuits Theory - Robert L. Boylestad and Louis	S
	Nashelsky, Pearson/Prentice Hall, Tenth Edition.	

Course Outcomes to Program Outcomes mapping:

co	P01	P02	P03	P04	P05	P06	P07	80d	P09	P010	P011	P012	PSO1	PSO2	PSO3
1	3	2	-	-	-	-	-	-	-	-	-	-	-	-	-
2	3	2	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	2	-	-	-	-	_	-	-	-	-	-	-	-	-
4	2	2	2	-	-	-	-	-	-	-	-	-	-	-	-
5	2	2	2	-	-	-	-	-	-	-	-	-	-	-	-
Cou rse	3	2	1	-	-	-	-	-	-	-	-	-	-	-	-

S.No.	Unit Name	Text Book Reference	Chapter No.
1	Small Signal Low Frequency Transistor Amplifier Models	T1	3
2	Small Signal High Frequency Transistor Amplifier models	T1	3 & 4
3	Feedback Amplifiers & Oscillators	T1	7 & 11
4	Power Amplifiers	T1	12
5	Tuned Amplifiers	T1	10

	CONTROL SYSTEMS		
	Common to ECE & ECT		
	SEMESTER IV		
Subject Code	21ECECT4050,21ETETT4050	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
Pre-requisite	Mathematics-III	Credits – 03	
Course Objectives:	1		
This course will enable the studen	its to:		
• Understand concepts of the	ne mathematical modeling, feedba	ack control and	stability
analysis in Time and Freq			2
	tate variable analysis techniques.		
Unit -1	· · · · ·		Hours
Introduction: System, Control S	ystem, Open Loop Control System	n, Closed loop	
2	Examples, Effects of Feedba	· 1	
Characteristics and its advantages	▲ ·	,	
Mathematical models of Physic		ons of physical	12
systems, Transfer functions of El			
systems. Block diagram Algebra,			
Unit -2			
Controller Components: DC	Servomotor (Armature Control	led and Field	
Controlled) with necessary deriva			
transfer function, AC Tachometer			
Time Response Analysis: Standa	· · · · · · · · · · · · · · · · · · ·	irst and second	10
order systems, steady state error			
second order systems, controllers			
Unit – 3			
Concepts of Stability and Al	gebraic Criteria: The concept	t of Stability,	
Necessary Conditions for Stabili	-	•	
stability analysis	5	,	10
The Root Locus Technique: Intro	oduction, The Root Locus concept	s, Construction	
of Root Loci, Effect of adding pol	les and zeros to a system		
Unit – 4	· · ·		
Frequency response analysis:	Introduction Correlation betw	een time and	
frequency response, Polar Plo			10
Performance specifications in free	• •	nty cincilon,	
_	lucitey domain.		
<u>Unit – 5</u>			
State Variable Analysis and I			0
Variables and State models, Sta		•	8
Solution of state equations and Co	oncepts of Controllability and Obs	ervability	
Course outcomes:			
On completion of the course stude			
1. Characterise a control syst			
2. Develop mathematical mo			
	vsis on first and second order syste		
	ty using Routh Hurwitz and Root		5
• •	ty using frequency response analy		
	sis to continuous time systems an		tionship
between state variable rep	resentation and transfer functions.		

Text Books:

- 1. I.J.Nagarath and M.Gopal, "Control Systems", New Age International Publishers, 5th Edition, 2014
- 2. Katsuhiko Ogata, "Modern Control Engineering", Pearson, 4th Edition, 2012 **Reference Books**:
 - Ambikapathy, "Control Systems", Khanna Book Publishing Co. (P) Ltd., Delhi
 Anand Kumar, "Control Systems", 2nd Edition, PHI learning PVT. Ltd,2014

Web References:

- 1. https://nptel.ac.in/courses/108101037/
- 2. http://www.ee.surrey.ac.uk/Projects/CAL/control/index.htm

Course Outcomes to Program Outcomes mapping:

CO	P01	P02	PO3	P04	PO5	PO6	P07	PO8	P09	P010	P011	P012	PS01	PSO2	PSO3
1	3	3	2	-	-	-	-	-	-	-	-	-	-	3	-
2	3	2	-	-	-	-	-	-	-	-	-	-	-	2	-
3	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
4	3	3	-	-	-	-	-	-	-	-	-	-	-	3	-
5	3	2	2	-	-	-	-	-	-	-	-	-	-	2	-
6	3	2	2	-	-	-	-	-	-	-	-	-	-	2	-
Course	3	2	1	-	-	-	-	-	-	-	-	-	-	2	-

Text Book /Reference:

S.No.	Unit Name	Text Book /Reference	Chapter No.
		T1	1,2 & 3
1	Introduction and Mathematical models	T2	1 & 3
	of Physical Systems	R1	1
		R2	1,2 & 3
		T1	4 & 5
2	Controller Components and Time	T2	5 & 7
2	Response Analysis	R1	2
		R2	4
3	Concepts of Stability and Algebraic	T1	6&7
5	Criteria and The Root Locus Technique	T2	6

		R1	3
		T1	8 & 9
4	Frequency response analysis	T2	8
		R2	7 & 8
		T1	12
5	State Variable Analysis and Design	T2	11
		R2	10

PRINCIPI	LES OF COMMUNICATION L	AB					
Common to ECE & ECT							
	SEMESTER IV						
Subject Code21ECECL4060, 21ETETL4060Internal Mar							
Number of Lecture Hours/Week	03	External Marks	35				
Total Number of Hours	36	Exam Hours	03				
Course Objectives	Credits – 1.5						
Course Objectives: This course will enable students to							
	ave modulation and demodulation	techniques					
 Verify Sampling Theorem 		teeninques					
Simulate modulation Tech							
	1						
List of Experiments:		I	Iours				
1. Amplitude Modulation - N	Iodulation & Demodulation.						
2. AM – DSBSC - Modulatio	on & Demodulation.						
3. Frequency Modulation & amp; Demodulation.							
4. Diode Detector.							
5. Pre-emphasis & De-emph	asis						
6. AGC Circuits.							
7. Verification of Sampling	Гheorem		36				
8. Pulse Amplitude Modulat	ion & Demodulation						
9. PWM, PPM–Modulation	& Demodulation						
10. PLLIC-565 as FM demod	ulator						
11. Pulse Code Modulation ar	d Demodulation						
12. Communication link simu	lation						
Course outcomes:		I					
On completion of the course stud	dent will be able to						
1. Infer the modulation and c	lemodulation techniques for contin	uous wave.					
2. Apply the sampling theore	em.						
3. Simulate modulation Tech	iniques.						

ELECT	RONIC CIRCUIT ANAL	YSIS LAB					
	Common to ECE & ECT	Г					
	SEMESTER IV		1				
Subject Code	21ECECL4070, 21ETETL4070	Internal Marks	15				
Number of Lecture Hours/Week	03	External Marks	35				
Total Number of Lecture Hours	36	Exam Hours	03				
	Credits – 1.5	·					
Course objectives:							
The objective of the course is Amplifiers, Oscillators, Feedbac		erstand the concepts of	f Basic				
For the following amplifier of oscillations needs to be executed	1 1 1	1 1	Hours				
1. BJT CE Amplifier							
2. Emitter follower-CC Ampl	ifier						
3. FET CS Amplifier							
-							
4. Two Stage RC Coupled An							
5. Voltage-Series Feedback A	-						
6. Current-Shunt Feedback Amplifier							
7. RC Phase Shift Oscillator							
8. Wien Bridge Oscillator							
9. Hartley and Colpitts Oscill	ator						
10. Class A Series-fed Power	Amplifier						
11. Complementary Symmetry	Class B Push-Pull Power	Amplifier					
12. Single Tuned Voltage Amp	olifier						
Course outcomes:							
After completing this course, stu	idents will be able to:						
1. Design CE amplifier and		se at low, mid and high					
frequencies	J 1 J 1	ý C					
2. Design two stage amplif	ier and analyze frequency r	response at low, mid and	l				
high frequencies							
3. Design feedback amplifi	er and analyze its frequenc	y response					
4. Design different oscillate							
5. Design different Power a	1	•					
	nd evaluate the resonant fr	requency					
Hardware/Software Require	ments:						
Equipment required							
1. Regulated Power supplie							
2. Analog/Digital Storage (_						
 Analog/Digital Function Digital Multimeters 	Generators						
5. Decade Résistance Boxe	s/Rheostats						
6. Decade Capacitance Box							
7. Ammeters (Analog or D							
8. Voltmeters (Analog or D	-						
9. Active & Passive Electro							
	*						

Software:

- Multisim/ Equivalent Industrial Standard Licensed simulation software tool.
 Computer Systems with required specifications.

	VALS AND SYSTEMS LAB Common to ECE & ECT					
	SEMESTER IV					
Subject Code	21ECECL4080, 21ETETL4080	Internal Mark	ks 1			
Number of Lecture Hours/Week						
Total Number of Hours	36	Exam Hours	ks 3			
	Credits – 1.5	Lixun Hours				
 Analyze the continuous tim Extend the properties of sy. Verify the sampling theorem Generation and standard op The programs shall be implemented perform at least TEN Experiments Introduction to MATLAB at Generation of Continuous Table Perform standard operation Check the properties of System Exponential Fourier series signal. 	tinuous time and discrete time sine signals using Fourier Series ar stems to LTI Systems. m. <u>berations on Random signals.</u> red in MATLAB software and structure of a MATLAB pro Fime and Discrete Time signals. Is on signals. stems. representation of a periodic Fully porm and Inverse Fourier Transf	nd Fourier Trans student has to gram. wave rectified	sform. Hours			
 9. Verification of the Samplin 10. Generate a uniformly districompute it's Mean and Var 11. Generate a discrete time distributed random numbe autocorrelation of the seque 12. Calculate Probability District Random variable. Course outcomes: Experiment with Generatio Analyze the fundamental si Inspect the system properties Construct the Sampling the 	buted random sequence in the ra- riance. Also plot the Histogram. sequence of length N with i. rs in the interval (-0.5,-0.5) and ence. ibution and Probability Density n of fundamental signals. ignals in frequency domain. es for a LTI system	inge (0,1) and i.d uniformly l compute the functions of a	om signal			

SKI	LL ORIENTED COURS PCB Designing	SE-II						
	SEMESTER IV		0					
Subject Code 21ETETS4090 Internal Marks Number of Leasture House (Week 02 External Marks								
Number of Lecture Hours/Week02External Marks								
Total Number of Practical32Exam Hours								
Hours/Week								
Pre-requisite	Technical English	Credits – 02						
Course Outcomes:		:						
1. Determine appropriate comp								
2. Interpret test results and me		. ,	— 11					
3. Analyze the fabrication proc	-		Teaching					
4. Apply the software and hard			Hours					
5. Evaluate an electronic prin		pecific application	36					
using industry standard software.(L5)								
	Course Contents							
List of experiments:								
1. Study on types of PCB layer								
2. Schematic Creation and sim		rcuit						
3. Mapping Components of an								
4. Set Parameters for PCB Des	sign.							
5. Laying Tracks on PCB.								
6. Create PCB Layout of an El								
7. Create Device Model and si								
8. Create PCB layout of an am								
9. Create PCB layout of an As	e							
10. Create PCB layout of a Volt	0 0							
11. Create PCB layout of a Galv	vanic isolation circuit.							
12. Printing on PCB.								
13. Etching and Drilling of PCE	3.							
14. Soldering PCB.								
15. Testing of an electronic Circ								
16. Testing of an electronic Circ								
Note: Any TWEL	VE of the experiments a	re to be conducted						

	LL ORIENTED COURS					
IN	TERNET OF THINGS (IO	T)				
	SEMESTER IV	-				
Subject Code	21ETETS4090	Internal Marks	0			
Number of Lecture Hours/Week	02	External Marks	50			
Total Number of Practical	32	Exam Hours	03			
Hours/Week						
Pre-requisite	 Computational 	Credits – 02				
	Thinking with'C'					
	Digital Logic Design					
Laboratory objectives						
• To develop Embedded C la	nguage program skills.					
• Providing the basic knowle	dge of interfacing various p	eripherals to Ardui	no Uno.			
• To Develop Real Time Sma	all Scale Embedded Applica	ations.				
Laboratory Outcomes						
1. Understand the concepts	of ArduinoUnoand differ	ent types of I/O				
Devices.			Teaching			
2. Construct interfacing circuits for different Applications						
3. Develop Embedded C codes for different applications using Arduino 36						
4. Develop Real time applicat	ions using Arduino.					
	Course Contents					
	(Minimum Any 6 Experi					
1. Write an Embedded C Program	n to interface the following	with Arduino Uno				
a. LED						
b. RGB LED						
2. Write an Embedded C Program						
3. Write an Embedded C Program						
4. Write an Embedded C Program	•					
5. Write an Embedded C Program			Arduino Uno			
6. Write an Embedded C Program						
7. Write an Embedded C Program	n to interface the fallowing	with Arduino Uno				
a. IR Sensor						
b. Temperature Sensor						
c. Ultrasonic Sensor						
	(Minimum Any 4 Experi	menus)				
1. Study and Implement RFID us	0	duing and transmi	t the masses			
2. Develop an Application to "WELCOME TO SASI" to the		Junio and transmi	i me messag			
3. Develop an Application to Inte	-	nd Identify Latitude	and Longitue			
of SITE.		2	C			
4. Design an Application to deve	lop touchless doorbell using	g Arduino				
5. Design an Application to monicoloud using Bluetooth and Arc	-	ty of a city and store	e in Thingspea			
6. Design an Application to deter		othole and hump to	aid drivers ar			

6. Design an Application to detect depth and height of the pothole and hump to aid drivers and store in Thing speak cloud using Bluetooth and Arduino.

PUL	SE & DIGITAL CIRCUITS							
	Common to ECE & ECT							
	SEMESTER IV							
Subject Code21ECECN40A0, 21ETETN40A0Internal Marks								
Number of Lecture Hours/Week	03	External Marl	ks 70					
Total Number of Lecture Hours	50	Exam Hours	03					
Pre-requisite	Semiconductor Devices	Credits 0						
Course Objectives: This course will enable students t • Understand Wave shapin • Analyze switching charac • Design multivibrators and Unit -1 Linear Wave Shaping: High p	g circuits. cteristics of electronic devices. d time base generators.	response for	Hours					
sinusoidal, step, pulse, square,	ramp and exponential inputs. Renutions , its applications in CRO	C network as	10					
Clamping circuit theorem, pr		upled clipper; ferent inputs, ect of diode	12					
characteristics, Design and analys consideration of transistor, satura with temperature, Design of trans Multivibrator: Analysis And De Vibrator, Collector Catching Di	vices: Diode as a switch, piecewis is of Transistor as a switch, Break tion parameters of Transistor and istor switch, transistor switching t sign of Fixed Bias, Self Bias E odes, Commutating Capacitors, Bistable Multivibrator (Schmitt T	down voltage their variation imes. Bistable Bistable Multi Triggering of	12					
Collector Coupled Astable Multiv a Voltage to Frequency Converte	ering of Monostable Multivibrator stable Multivibrator: Analysis a vibrator, Application of Astable M	, Applications nd Design of	9					
Unit – 5								
of generating time base wave	General features of a time base signarm Exponential Sweep Circunciples in Miller and Bootstra	its, Negative	7					

genera	tors, Transistor Miller time base generator, Transistor Bootstrap time base						
genera							
Cours	e outcomes:						
On con	mpletion of the course, student will be able to						
1.	Analyze linear wave shaping circuits with different inputs.						
	2. Design Non linear wave shaping circuits.						
	Design switching circuits.						
	Analyze different Multivibrators						
	Design different multivibrators						
6.	Understand different types of time base generators						
Text I	Books:						
1.	A. Anand Kumar, "Pulse and Digital Circuits", PHI, 2005						
Refer	ence Books:						
2.	J. Millman and H. Taub, Mothiki S Prakash Rao, "Pulse, Digital and Switching						
	Waveforms", McGraw-Hill, Second Edition, 2007.						
3.	Venkata Rao,K,Ramasudha K, Manmadha Rao,G, "Pulse & Digital Circuits",						
	Pearson,2010						
4.	J. Millman and H. Taub, Pulse, "Digital and Switching Waveforms", McGrawHill						

Web References:

- 1. http://www.iitg.ac.in/apvajpeyi/ph218/Lec-18.pdf
- 2. http://www.nptelvideos.in/2012/12/digital-circuits-and-systems.html
- 3. http://www.allaboutcircuits.com/video-lectures/

Course Outcomes to Program Outcomes mapping:

CO	PO1	P02	PO3	P04	PO5	P06	PO7	PO8	P09	PO10	P011	P012	PSO1	PSO2	PSO3
1	3	3	2	-	-	-	-	-	-	-	-	-	-	-	1
2	3	3	3	-	-	-	-	-	-	-	-	-	-	-	1
3	3	3	3	-	-	-	-	-	-	-	-	-	-	-	2
4	3	3	2	-	-	-	-	-	-	-	-	-	-	-	2
5	3	3	3	-	-	-	-	-	-	-	-	-	-	-	2
6	3	3	1	-	-	-	-	-	-	-	-	-	-	-	-
Cours e	3	3	3	-	-	-	-	-	-	-	-	-	-	-	2

Text Book/Reference:

S.No	Unit Name	Text Book/Reference	Chapter No
1	Linear Wave Shaping	T1	1
1	Linear wave Snaping	R1	2

2	Non-Linear Wave Shaping	T1	2
2	Non-Linear wave Shaping	R1	5,6
3	Switching Characteristics of	T1	3,4
5	Devices, Bistable Multivibrator	R2	6
4	Monostable Multivibrator & Astable	T1	4
4	Multivibrator	R2	7,8
5	Voltage Time Base Generators	T1	5
5	voltage Time Base Generators	R3	14,15

Course Structure for III B.Tech ECT Under the Regulations of SITE-21

	Semester -V							
S.No	Subject Code	Name of the subject		т	Ρ	С		
1	21ETETT5010	Digital Modulation and Coding	3	0	0	3		
2	21ETETT5020	Microelectronics & VLSI Design	3	0	0	3		
3	21ETETT5030	Linear IC Applications	3	0	0	3		
4	21ETETP504X	Professional Elective-1	3	0	0	3		
5	21ETXXO505X	Open Elective - 1	3	0	0	3		
6	21ETETL5060	Digital Modulation and Coding Lab	0	0	3	1.5		
7	21ETETL5070	Linear IC Applications Lab	0	0	3	1.5		
8	21CMAHS5080	Skill advanced course/ soft skill course-3* (Soft Skills & Aptitude Builder-1).	1	0	2	2		
10	21CMMSN5090	Biology for Engineers	2	0	0	0		
11	21ETETR70A0	Summer Internship - 2 Months (Mandatory) after second year (to be evaluated during V semester		0	3	1.5		
	Total Serr			r Cre	dits	21.5		
12		Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)	4	0	0	4		

Professional Elective-1

S.No	Subject Code	Name of the subject	L	Т	Р	С
1	21ETETP504A	Computer Organization and Microprocessors	3	0	0	3
2	21ETETP504B	Radiation Systems	3	0	0	3
3	21ETETP504C	System Design through Verilog	3	0	0	3

Open Elective-I

Candidate should select the subject from list of subjects offered by other departments

	Course Structure for III B.Tech ECT Under the Regulations of SITE-21							
	Semester -VI							
S.No	Subject Code	Name of the subject	L	т	Ρ	С		
1	21ETETT6010	Digital Signal Processing	3	0	0	3		
2	21ETETT6020	Data Communications and Networking	3	0	0	3		
3	21ETETT6030	Microcontrollers and Applications	3	0	0	3		
4	21ETETP604X	Professional Elective-II	3	0	0	3		
5	21ETXXO605X	Open Elective - II	3	0	0	3		
6	21ETETL6060	Digital Signal Processing Lab	0	0	3	1.5		
7	21ETETL6070	VLSI Design Lab	0	0	3	1.5		
8	21ETETL6080	Microcontroller and Applications Lab	0	0	3	1.5		
9	21ETETS6090	Skill advanced course/ soft skill course-4* Soft Skills and Aptitude Builder-2	1	0	2	2		
10	21ETETN60A0	Essential of Indian Traditional Knowledge	2	0	0	0		
		Total Sem	este	r Cre	dits	21.5		
11	H/M	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)	4	0	0	4		

Professional Elective-2

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP604A	Spread Spectrum Techniques	3	0	0	3
2	21ETETP604B	Advanced VLSI Design	3	0	0	3
3	21ETETP604C	Microwave Engineering	3	0	0	3

Open Elective-II

Candidate should select the subject from list of subjects offered by other departments

Course Structure for IV B.Tech ECT Under the Regulations of SITE-21

	Semester -VII						
S.No	Subject Code	Name of the subject	L	т	Ρ	С	
1	21ETETP701X	Professional Elective -III	3	0	0	3	
2	21ETETP702X	Professional Elective -IV	3	0	0	3	
3	21ETETP703X	Professional Elective -V	3	0	0	3	
4	21ETXXO704X	Open Elective - III	3	0	0	3	
5	21ETXXO705X	Open Elective - IV	3	0	0	3	
6	21ETXXO706X	Open Elective - V	3	0	0	3	
7	21CMAHS7070	Skill advanced course/ soft skill course	1	0	2	2	
8	21ETETR7080	Summer Internship 2 months after Third year (To be evaluated during VII sem)	3	0	0	3	
	Total Semester Credits				23		
9		Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)	4	0	0	4	

Professional Elective-III

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP701A	Digital Image Processing	3	0	0	3
2	21ETETP701B	Telecommunications and Switching Networks	3	0	0	3
3	21ETETP701C	CAD Tools for VLSI	3	0	0	3

Professional Elective-IV

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP702A	Bio-Medical Signal Processing	3	0	0	3
2	21ETETP702B	Global Position Systems	3	0	0	3
3	21ETETP702C	Optical Communication	3	0	0	3

Professional Elective-V

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP703A	RF System Design	3	0	0	3
2	21ETETP703B	Internet of Things	3	0	0	3
3	21ETETP703C	Radar and Satellite Communications	3	0	0	3

Open Elective-III

Candidate should select the subject from list of subjects offered by other departments

Open Elective-IV

Candidate should select the subject from list of subjects offered by other departments

Open Elective-V

Candidate should select the subject from list of subjects offered by other departments

	Course Structure for IV B.Tech ECT Under the Regulations of SITE-21								
		Semester -VIII							
S.No	S.No Subject Code Name of the subject L T P C								
1	xxxxxx	Project work, seminar and internship in industry	0	0	24	12			
	Total Semester Credits 12								

DIGITAL	MODULATION AND COI SEMESTER V	DING	
Subject Code	21ETETT5010	Internal Marks	30
Subject Code Number of Lecture Hours/Week			
	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
Pre-requisite	Principles of Communication Theory	Credits – 03	
 Course Objectives: This course will enable students to Interpret various digital modu their performance in terms of f Illustrate the concepts of infor Explain Block codes, cyclic co Unit -1 DIGITAL MODULATION communication: Elements of digit communication systems, Digital r QPSK, M-ary PSK, M-ary ASK, F Unit -2 DATA TRANSMISSION : Bas optimum filter, matched filter, pr reception, non-coherent detection 	lation techniques and able to probability of error. mation theory and need for so odes and convolution codes. TECHNIQUES : Introdu al communication systems, a nodulation techniques: ASK, M-ary FSK. e band signal receiver, prob obability of error using mato	ction to digital dvantages of digital FSK, PSK, DPSK, ability of error, the ched filter, coherent	ems for Hours 10
BPSK. Unit – 3 INFORMATION THEORY: information and its properties. A Information rate SOURCE CODING: Introducti Fano coding, Huffman coding, eff and analog Channels, capacity of	verage information, Entropy ons, Advantages, Shannon's iciency calculations, channel	and its properties. theorem, Shanon- capacity of discrete	12
Unit – 4 LINEAR BLOCK CODES: In codes, Error detection and error Hamming codes, Binary cyclic co Unit – 5	correction capabilities of I		9
CONVOLUTION CODES: In approach, transform domain appr diagram, decoding using Viterbi a	oach. Graphical approach: s		9
Total			50
Course outcomes: On completion of the course stude 1. Demonstrate various Digit 2. Solve the probability of er 3. Illustrate various source co 4. Interpret the Linear Block	al Modulation Techniques. ror in the data transmission. oding techniques.		

5. Demonstrate the Convolution Codes.

Text Books:

- 1. Digital communications Simon Haykin, John Wiley, 2005
- 2. Digital and Analog Communication Systems Sam Shanmugam, John Wiley, 2005

Reference Books:

- 1. Principles of Communication Systems H. Taub and D. Schilling, TMH, 2003
- 2. Digital Communications John Proakis, TMH, 1983. Communication Systems Analog&Digital Singh &Sapre, TMH,2004.
- 3. Modern Digital and Analog Communication Systems B.P.Lathi, Zhi Ding, Hari Mohan Gupta, Oxford University Press,4 th Edition,2017
- 4. Analog & Digital Communication Dr. Sanjay Sharma, S.K. Kataria& Sons, 3rd Edition.

Web References:

- 1. https://nptel.ac.in/courses/117101051
- 2. https://archive.nptel.ac.in/courses/108/101/108101113/
- 3. https://archive.nptel.ac.in/courses/117/105/117105144/
 - 4. https://archive.nptel.ac.in/courses/108/102/108102120/

MICROELE	CTRONICS AND VLSI D	ESIGN	
Subject Code	SEMESTER V	Internal Mar	Jra 20
Subject Code	21ETETT5020	Internal Mar	
Number of Lecture Hours/Week	03	External Ma	
Total Number of Lecture Hours	48	Exam Hours	
Pre-requisite	Digital System Design	Credits – 03	
Course Objectives:			
This course will enable students to			
1. Know about IC technology ar		S.	
2. Demonstrate IC design proces			
3. Estimate parametric of CMOS			
4. Understand gate level design	and physical design.		
5. Know subsystem design.			TT
Unit -1			Hours
Introduction: Introduction: Basic	1		
& BiCMOS, Basic Electrical Prope		Ũ	
of MOS transistors – Threshold			10
relationships, Threshold Voltage,			10
gds, Figure of merit $\omega 0$; Pass trans		nverter analysis	
and design, Various pull ups loads,	B1-CMOS Inverters.		
Unit -2			
VLSI Circuit Design Processes	ũ là chiến the second sec		0
Diagrams, Design Rules and Layou			9
CMOS Inverters and Gates, 2 um		MOS/BiCMOS	
Rules, 1.2 um Double Metal, Singl	e Poly. CMOS Rules.		
Unit -3			
Basic Circuit Concepts: Sheet res			
and inverters, Resistance estimatio			
capacitance, Capacitance estimatio			10
delays, Driving large capacitance le		g Capacitances.	
Scaling of MOS circuits, Limitatio	ns of Scaling.		
Unit – 4			
Gate level Design: Introduction to		0 0	
switches, Working polar transistor		gates and other	10
complex gates, Switch logic, Altern	•		10
Physical Design: Floor-Planning,	Placement, Routing, Power de	lay estimation,	
Clock and Power routing.			
<u>Unit – 5</u>			
Subsystem Design: Shifters, Ac	lders, ALUs, Multipliers, Par	ity generators,	
Comparators and Counters.			9
VLSI Design Styles: Full-custom		FPGAs, CPLDs	
and Design Approach for Full-cust	om and Semi-custom devices.		
Total			48
Course outcomes:			
On completion of the course studen			
-	properties of MOS and BiCMO	OS circuits.	
2 Illustrate the intricacies of V	VLSI Circuit design processes.		
3. Develop the parametric for			
	ethodologies.		

LINEAR IC APPLICATIONS SEMESTER VSubject Code21ETETT5030IA Marks30Number of Lecture Hours/ Week03Exam Marks70Total Number of Lecture Hours48Exams Hours03Pre-requisiteSemiconductor DevicesCreditsCourse Objectives:This course will enable the students to6. Understand the basic operation and performance parameters of differential amplifier7. Understand the measuring techniques and performance parameters of op-amp.8. Learn linear and non-linear applications of operational amplifiers.9. Understand and design active filters using op-amps and applications of IC555 timer PLL .10. Learn the internal structure and operation of various DACs and ADCs.Unit IDIFFERENTIAL AMPLIFIERS:	ers.		
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PLL . 10. Learn the internal structure and operation of various DACs and ADCs. Unit I DIFFERENTIAL AMPLIFIERS:	and		
10. Learn the internal structure and operation of various DACs and ADCs. Unit I DIFFERENTIAL AMPLIFIERS:			
Unit I DIFFERENTIAL AMPLIFIERS:			
DIFFERENTIAL AMPLIFIERS:			
Differential Amplifier-DC and AC Analysis of Dual Input Balanced Output			
Configuration, Properties of Other Differential Amplifier Configuration: Dual	9		
Input Unbalanced Output, Single Ended Input Balanced and Unbalanced Output,			
DC Coupling and Cascaded Differential Amplifier Stages, Level Translator.			
Unit II			
CHARACTERISTICS OF OP-AMPS:			
Integrated Circuits Types, Classification, Package Types and Temperature Ranges,			
Power Supplies, Op-amp Block Diagram, Ideal and Practical Op-amp	.0		
Specifications, 741 Op-amp & its Features, Op-Amp Parameters and			
Measurement, DC and AC Characteristics: Input and Output Offset Voltages and			
Currents, Slew Rate, CMRR, PSRR, Drift, Frequency Compensation Techniques.			
LINEAR APPLICATIONS OF OP-AMPS:			
Inverting and Non-inverting Amplifier, Integrator and Differentiator, Difference	0		
	.0		
NON-LINEARAPPLICATIONS OF OP-AMPS:			
Comparators, Multivibrators, Function Generators: Triangular and Square Wave			
Generators, Log and Anti-log Amplifiers, Precision Rectifiers. Unit IV			
ACTIVE FILTERS:			
Design and Analysis of Butterworth Active filters: 1 st order, 2 nd order LPF, HPF,			
Band Pass, Band Reject and All Pass Filters, Sample & Hold Circuits.			
TIMERS AND PHASE LOCKED LOOPS:	.0		
IC 555 Timer Functional Diagram, Monostable and Astable Operations and			
Applications, Schmitt Trigger, Phase Locked Loop (PLL), 565 PLL, Frequency			
Multiplication and Frequency Translation using PLL, Applications of VCO(566).			
Unit V			
DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS			
Basic DAC Techniques, Weighted Resistor DAC, R-2R Ladder DAC, Inverted R-2R	9		
DAC, IC1408 DAC, Parallel Comparator type ADC, Counter type ADC, Successive			

r	1			
Approxir	nation and Dual Slope ADC, DAC and ADC Specifications, Specifications			
of AD574	4 (12 bit ADC).			
	Total	48		
Course C	Dutcomes			
On Com	pletion of the course, student will be able to			
6. II	lustrate basic operation and performance parameters of differential amp	lifiers.		
7. D	Demonstrate the performance parameters of operational amplifier.			
8. D	8. Develop linear and non-linear applications of operational amplifier.			
9. B	uild different active filters, timer and PLL applications.			
10. C	Construct various DAC and ADC circuits.			
Text Boo	oks			
2. L	inear Integrated Circuits by D. Roy Choudhury, NewAgeInternational(p) Ltd,4 th		
E	dition, 2015.			
3. C	p-Amps and Linear ICs by Ramakanth A.Gayakwad, PHI, 1987.			
Reference	ce Books			
8	. Operational Amplifiers and Linear Integrated Circuits by Sanjay Sha	rma, S K		
	Kataria & Sons, 2 nd Edition, 2010.			
9	. Operational Amplifiers and Linear ICs by David A Bell, Oxford Ur	i. Press,		
	3rdEdition.	,		

Professional Elective-1

S.No	Subject Code	Name of the subject		Т	Р	С
1	21ETETP504A	Computer Organization and Microprocessors	3	0	0	3
2	21ETETP504B	Radiation Systems		0	0	3
3	21ETETP504C	System Design through Verilog	3	0	0	3

	ANIZATION & MICROPROC (Professional Elective – I) SEMESTER V	ESSORS		
Subject Code	21ETETP504A	Internal Marks	30	
Number of Lecture Hours /Week	3	External Mark	s 70	
Total Number of Lecture Hours	48	Exam Hours	03	
Pre-requisite	Digital System Design	Credits 03		
and memory and I/O devices. • Understand the concepts of p • Understand architectures com • Apply programming concepts • Apply interfacing of Micro pr Unit -1 Basic Structure of Computers Operational Concepts, Bus, Struct and Multi Computers, Data Rep Micro Operations: Register Tra Memory Transfers, Arithmetic M Micro Operations, Arithmetic L Registers Computer Instructions Instructions, I/p-O/p and Interrup Addressing Modes, DATA Tra Reduced Instruction Set Computer	basic structure of Computers, machine arallel processing and pipelining. cepts of 8086 and advanced Micro process of 8086 Micro processors. rocessors with memory and other periph s: Computer Types, Functional UNIT etures, Software, Performance, Multipro presentation. Register Transfer Language insfer Language, Register Transfer E icro Operations, Logic Micro Operation ogic Shift Unit, Instruction Codes, Co s - Instruction Cycle. Memory - Re ot, STACK Organization, Instruction F ansfer and Manipulation, Program (essors. herals. T, Basic Decessors age and Bus and hs, Shift omputer eference Formats,		
I/p-O/p Processor (IOP), Serial	des, Priority Interrupt, Direct Memory Communication; Introduction to Pe CI) Bus, Introduction to Standard	ripheral	9	

The Memory System : Basic Concepts of Semiconductor RAM Memories, Read- Only Memories, Cache Memories Performance Considerations, Virtual Memories secondary Storage, Introduction to RAID.	
Unit – 3	I
 Processing Unit: Fundamental Concepts: Register Transfers, Performing An Arithmetic Or Logic Operation, Fetching A Word From Memory, Execution of Complete Instruction, Hardwired Control. Micro programmed Control: Microinstructions, Micro program Sequencing, Wide Branch Addressing Microinstructions with next –Address Field. Concept of parallel processing, Pipelining, Forms of parallel processing, interconnect network. 	8
Unit – 4	
Micro processors Architecture: Introduction to 8-bit Processors, Features, Pin Description, 8086 Microprocessor Family, 8086 Internal Architecture, Interrupts, Minimum Mode and Maximum Mode Configuration of 8086. Advanced processor Architectures- 286, 386,486.	8
Unit – 5	I
8086 Programming: Instruction set, Addressing Modes, Assembler Directives, Writing Simple Programs with an Assembler, Assembly Language Program Development Tools.	8
Total	48
Course outcomes: On completion of the course student will be able to: 1. Demonstrate the fundamental organisation of a computer system. 2. Summarize memory and I/O devices 3. Interpret parallel processing, Pipelining and parallel processing concepts 4. Learn architectures concepts of 8086 and advanced Micro processors. 5. Analyze the programming concepts of 8086 Microprocessor. Text Books:	, 5th Edition
 Carl Hamacher, Zones Vranesic, Safea Zaky, "Computer Organization" McGraw Hill. John P. Hayes, "Computer Architecture and Organization", 3rd Editi 3. A. K. Ray, K. M. Bhurchandi, "Advanced Microprocessors and Peri 	· · · · · · · · · · · · · · · · · · ·
McGraw Hill. 2. John P. Hayes, "Computer Architecture and Organization", 3rd Editi	· ·

RADIA	TION SYSTEMS		
(Profes	ssional Elective – I)		
S	EMESTER V		
Subject Code	21ETETP504B	Internal	30
		Marks	
Number of Lecture Hours/Week	03	Externa	70
		l Marks	
Total Number of Lecture Hours	48	Exam	03
		Hours	
i		Credit	s – 03

Course Objectives:

This course will enable students to:

- To give insight into the radiation phenomena.
- To give a thorough understanding of the radiation characteristics of different types of antennas
- To create awareness about the different types of propagation of radio waves at different frequencies

Unit -1	Hours
FUNDAMENTALS OF RADIATION- Antenna parameters -	
Radiation pattern, Gain, Directivity, Effective aperture, Main lobe and	10
side lobes, Antenna Beam width, Beam efficiency, Bandwidth, Antenna	10
height, Friss transmission formula, Impedance matching: BALUNS,	
Polarization mismatch, Antenna temperature, half wave dipole and	
folded dipole, Antenna polarization.	
Unit -2	
ANTENNA ARRAYS - Two element array, N-element linear array,	
Pattern multiplication, Broadside and end fire array, Array synthesis:	8
Binomial array, Adolph-Tschebyscheff array, Phased array antenna,	
Yagi-Uda array.	
Unit – 3	
APERTURE ANTENNAS - Huygens' principle, Babinet's principle,	
Types of Horn antennas, radiation from rectangular aperture, design	11
considerations, Radiation from sectoral and pyramidal horns, parabolic	
reflector antennas and feeding techniques, microstrip patch antenna.	
Unit – 4	
MODERN ANTENNAS - Phased array antennas, Smart antennas -	
switched beam and adaptive arrays, UWB antennas, RFID Antennas,	10
Wearable antennas, Reconfigurable antennas, Dielectric resonator	10
antennas, bandwidth enhancement techniques, gain enhancement	
techniques.	
Unit – 5	

ANTENNA MEASURMENTS - Required equipment in antenna	
measurement, Antenna measurement range, Measurements: Gain	9
measurement, Directivity measurement, Measurement of phase of an	
antenna, Measurement of Antenna efficiency and polarization.	

Course outcomes:

On completion of this course, students are able to

- 1. Comprehend and appreciate the significance and role of this course in the present contemporary world.
- 2. Understand the fundamentals of the antenna by gaining technical knowledge regarding antenna parameters.
- 3. Have insight into the radiation phenomena.
- 4. Have a thorough understanding of the radiation characteristics of different types of Antennas.
- 5. Identify the different types of propagation of radio waves at various frequencies.

Text Books:

- 1. John D Kraus, "Antennas for all Applications", Mc Graw Hill, 5 th Edition, 2005.
- 2. R.E.Collin, "Antennas and Radio wave propagation", Mc Graw Hill, 1985.

References:

- 1. Constantine.A.Balanis, "Antenna Theory Analysis and Design", Wiley student edition, 3rd Edition, 2009.
- 2. Edward C.Jordan and Keith G.Balmain, "Electromagnetic Waves and Radiating Systems", Prentice Hall of India, 2006.
- 3. S. Drabowitch, "Modern Antennas", Springer Publications, 2 nd Edition, 2007.
- 4. Robert S.Elliott, "Antenna theory and Design", Wiley student edition, 2010.
- 5. H.Sizun, "Radio Wave Propagation for Telecommunication Applications", First Indian Reprint, Springer Publications, 2007.

SYSTEM DESIGN THROUGH VERILOG (Professional Elective – I)

SEMESTER V

Number of Lecture Hours/Week04External Marks70Total Number of Lecture Hours50Exam Hours03Pre requisiteDigital System DesignCredits – 03Course Objectives: Enable the students to 1. Understand basic language constructs of Verilog HDL 2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions.HoursUnit -1HoursIntroduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.10		SEMESTER V			
Total Number of Lecture Hours50Exam Hours03Pre requisiteDigital System DesignCredits - 03Course Objectives: Enable the students to 1. Understand basic language constructs of Verilog HDL 2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions.HoursUnit -1HoursIntroduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.10Unit -2Gate Level Modeling: Introduction, Gate Primitives, Dilay switch Level Modeling - CMOS Switches.10Unit -3Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators.10Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignment to Vectors, Operators.10Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct.9Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Intit - 5Implementation of Sequential Circuits: - Verilog implement	Subject Code	21ECECT504C	Internal Marks	30	
Pre requisite Digital System Design Credits – 03 Course Objectives: Enable the students to 1. Understand basic language constructs of Verilog HDL 2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions. Unit -1 Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification 10 Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches. 10 Modeling – CMOS Switches. 10 Unit - 3 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Ramples, Assignment to Vectors, Operators. 10 Blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Inplementation of Combinational Circuits: Verilog implementation of combinational logic circuits - Full Adders, Full Subtractors, encoders, decoders, multiplexers and	Number of Lecture Hours/Week	04	External Marks	70	
Course Objectives: Enable the students to 1. Understand basic language constructs of Verilog HDL 2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions. Unit -1 Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling - CMOS Switches. Unit - 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non-blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 <	Total Number of Lecture Hours	50	Exam Hours	03	
Enable the students to 1. Understand basic language constructs of Verilog HDL 2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions. Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification 10 Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit - 5 Implementation of Sequential Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Intiplexers and magnitude comparators. 9 Course outcomes: 50 <	Pre requisite	Digital System Design	Credits – 03		
1. Understand basic language constructs of Verilog HDL 2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions. Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling - CMOS Switches. 10 Unit -3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit - 4 Implementation of Combinational Circuits : Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Implementation of Sequential Circuits : - Verilog implementation of sequential logic circuits-	Course Objectives:				
2. Design the digital circuits using Verilog HDL 3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions. Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Hours Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling - CMOS Switches. 10 Unit -3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis	Enable the students to				
3. Verifying and synthesizing the digital circuits using CAD tools 4. Design simple digital systems based on digital abstractions. Unit -1 Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification 10 Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches. 10 Unit - 3 Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Bata flow Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non-blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit - 4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits - Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequent	1. Understand basic language	constructs of Verilog HDL			
4. Design simple digital systems based on digital abstractions. Hours Unit -1 Hours Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification 10 Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling - CMOS Switches. 10 Modeling - CMOS switches. 10 Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit -4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequ					
Unit -1HoursIntroduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.10Unit -2Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri- State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit - 3Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. Unit - 410Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit - 5Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: A the end of the course, students will be able to:Total50	, e , e	e e	ols		
Introduction to Verilog HDL: Verilog as HDL, HDL Design flow, Levels of Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.10Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit -3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct.10Unit -4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit - 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: At the end of the course, students will be able to:50	4. Design simple digital system	ms based on digital abstractions.			
Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.10Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit - 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. Unit - 410Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit - 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: A the end of the course, students will be able to:50	Unit -1			Hours	
Design Description, Simulation and Synthesis, Functional Verification Language Constructs: Introduction, Module, Keywords, Identifiers, White Space, Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.10Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit - 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. Unit - 410Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit - 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: A the end of the course, students will be able to:50	Introduction to Verilog HDL:	Verilog as HDL, HDL Design	flow, Levels of		
Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators. Int -2 Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches. 10 Unit - 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit - 4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Unit - 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector. 9 Course outcomes: Total 50					
Scalars and Vectors, Parameters, Operators. 10 Unit -2 Gate Level Modeling: Introduction, Gate Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches. 10 Unit - 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit - 4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Unit - 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits - Sequence detector. 9 Course outcomes: 50 At the end of the course, students will be able to: 50	Language Constructs: Introducti	on, Module, Keywords, Identifier	rs, White Space,	10	
Unit -2Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri- State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit - 3Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct.10Unit - 4Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit - 5Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: At the end of the course, students will be able to:50	Characters, Comments, Numbers	, Strings, Logic Values, Strength	ns, Data Types,		
Gate Level Modeling: Introduction, Gate Primitives, Illustrative Examples, Tri- State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit – 3Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct.10Unit – 4Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit – 5Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Total50Course outcomes: At the end of the course, students will be able to:	Scalars and Vectors, Parameters,	Operators.			
State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level Modeling – CMOS Switches.10Unit - 3Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct.10Unit - 4Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit - 5Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: At the end of the course, students will be able to:50	Unit -2				
Primitives, Delays, Strengths and Contention Resolution, Net Types, Switch Level 10 Modeling – CMOS Switches. Unit – 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit – 4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Unit – 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector. 9 Course outcomes: At the end of the course, students will be able to: 50					
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Unit – 3 Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. Image: Construct, Examples, Assignment to Vectors, Operators. Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit – 4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Unit – 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector. 9 Course outcomes: Total 50		Contention Resolution, Net Type	s, Switch Level	10	
Data flow Modeling: Introduction, Continuous Assignment Structures, Delays and Continuous Assignments, Assignment to Vectors, Operators. 10 Behavioral Modeling: Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Nonblocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct. 10 Unit – 4 Implementation of Combinational Circuits: Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators. 9 Unit – 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector. 9 Course outcomes: At the end of the course, students will be able to: 50					
Continuous Assignments, Assignment to Vectors, Operators.Behavioral Modeling:Introduction, Types of Assignments, Initial Construct, Always Construct, Examples, Assignments with Delays, Blocking and Non- blocking Assignments, The case statement, if and if-else constructs, for loop, while loop, forever loop, wait construct.10Unit – 4Implementation of Combinational Circuits:Verilog implementation of combinational logic circuits- Full Adders, Full Subtractors, encoders, decoders, multiplexers and magnitude comparators.9Unit – 5Implementation of Sequential Circuits:- Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Course outcomes: At the end of the course, students will be able to:50					
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multiplexers and magnitude comparators. Unit – 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector. Total 50 Course outcomes: At the end of the course, students will be able to:	A	0 1		9	
Unit – 5 Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector. Total 50 Course outcomes: At the end of the course, students will be able to:	•		uers, uecouers,		
Implementation of Sequential Circuits: - Verilog implementation of sequential logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Total50Course outcomes: At the end of the course, students will be able to:10					
logic circuits- latches, Flip-flops, Shift registers, Synchronous counters, Design and analysis of clocked sequential circuits- Sequence detector.9Total50Course outcomes: At the end of the course, students will be able to:10		Sirguits: Varilog implementation	n of sequential		
analysis of clocked sequential circuits- Sequence detector.Total50Course outcomes:At the end of the course, students will be able to:		0 1	-	0	
Total50Course outcomes:At the end of the course, students will be able to:			iers, Design and	9	
Course outcomes: At the end of the course, students will be able to:	analysis of clocked sequential end	suits bequeilee detector.	Total	50	
At the end of the course, students will be able to:	Course outcomes:		TUtal	30	
		will be able to:			
1. Understand Verilog HDL fundamentals.					
	1. Understand Verilog HDL funda	amentals.			

- 2. Construct various syntaxes in Gate level modeling.
- 3. Construct various syntaxes in data flow and behavioral modeling.
- 4. Examine various combinational circuits.
- 5. Examine various sequential circuits.

Text Books:

1. Design through Verilog HDL – T.R. Padmanabhan and B. Bala Tripura Sundari, WSE, IEEE Press, 2004.

2. Verilog HDL - Digital Design and Modelling-Joseph. Cavanagh, CRS Press, 2007.

Reference Books:

1. A Verilog Primier – J. Bhasker, BSP, 2003.

2. HDL Design by Palnitkar.

DIGITAL MO	DDULATION AND CODIN	G LAB		
Subject Code	SEMESTER V	Internal Marks		15
Subject Code Number of Lecture Hours/Week		External Mark		15 35
Total Number of Hours		External Mark Exam Hours	8	03
	30		redits	
Course Objectives: This course will enable students to • Know Multiplexing Schem • Know the Digital Modulati • Know the Analog to Digital List of Experiments:	on Schemes			
Students have to perform a m programming or MATLAB Simulia 13. Time Division Multiplexin 14. Differential Pulse Code Mo 15. Amplitude Shift Keying 16. Frequency Shift Keying 17. Phase Shift Keying 18. Differential Phase Sift Key 19. Quadrature Phase Shift Key 20. Implementation of Source 6 21. Implementation of Source 6 22. Linear Block Code – Encod 23. Binary Cyclic Code – Encod	g odulation ying (QPSK) Coding Techniques – Huffman Coding Coding Techniques – Shannon – Fano der and Decoder oder and Decoder nd correction using Hamming code	<u>z</u> ,	3	6
Course outcomes: On completion of the course studer 1. Illustrate Multiplexing schemes. 2. Analyze different Digital Modul				
3. Evaluate various Source & Chan				
4. Demonstrate the Analog to Digit	*			
5. Make an effective report based o	on experiments.			

LINE	AR IC APPLICATIO	NS LAB	
	SEMESTER V		
Subject Code	21ETETL5070	IA Marks	15
Number of Lecture Hours/ Week	03	Exam Marks	35
Total Number of Lecture Hours	36	Exams Hours	03
		Cr	edits -1.5
Course Objectives:			
This lab will enable the students t	0		
1. Study basic parameters and sp	ecifications of variou	s ICs related to Linear ICs.	
2. Analyze basic application of IC	741.		
3. Understand various filters and	timer.		
4. Understand the operation of P	LL and VCO.		
5. Learn the operation of DAC.			T
List of Experiments:			Hours
Conduct any ten experiments usir	ng Multisim software		
 Adder, Subtractor and Cor Integrator and Differentiat Function Generator using Low Pass and High Pass Fil Monostable Multivibrator Astable Multivibrator using Schmitt Trigger Circuits us IC 565 PLL Applications. IC 566 VCO Applications. A-bit DAC using Op-amps. Voltage Regulator using IC 	tor using IC 741. IC 741. ters (first order) usin using IC 555. g IC 555. ing IC 741 and IC 555	g IC 741.	36
Course Outcomes: On Completion of the lab, student 1. Understand specifications 2. Construct various applicat 3. Design various filters and t 4. Construct various applicati 5. Make an effective report bas	of Linear ICs. ions using IC 741. timer based applications of PLL and VCO.	ons.	

Course Structure for III B.Tech ECT Under the Regulations of SITE-21						
		Semester -VI				
S.No	Subject Code	Name of the subject	L	т	Ρ	С
1	21ETETT6010	Digital Signal Processing	3	0	0	3
2	21ETETT6020	Data Communications and Networking	3	0	0	3
3	21ETETT6030	Microcontrollers and Applications	3	0	0	3
4	21ETETP604X	Professional Elective-II		0	0	3
5	21ETXXO605X	Open Elective - II		0	0	3
6	21ETETL6060	Digital Signal Processing Lab	0	0	3	1.5
7	21ETETL6070	VLSI Design Lab		0	3	1.5
8	21ETETL6080	Microcontroller and Applications Lab	0	0	3	1.5
9	21ETETS6090	Skill advanced course/ soft skill course-4* Soft Skills and Aptitude Builder-2	1	0	2	2
10	21ETETN60A0	A0 Essential of Indian Traditional Knowledge		0	0	0
		Total Seme	ester	Cree	dits	21.5
11	H/M	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)	4	0	0	4

Professional Elective-II

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP604A	Spread Spectrum Techniques	3	0	0	3
2	21ETETP604B	Advanced VLSI Design	3	0	0	3
3	21ETETP604C	Microwave Engineering	3	0	0	3

Open Elective-II

Candidate should select the subject from list of subjects offered by other departments

DIGITA	AL SIGNAL PROCESSING SEMESTER VI			
Subject Code	21ETETT6010	Internal N	larks	30
Number of Lecture Hours/Week	03	External N	/larks	70
Total Number of Lecture Hours	50	Exam Ho		03
Pre-requisite	Signals and Systems		ts - 03	
Course Objectives:				
This course will enable students to				
1. Analyze the Discrete time signal	s.			
2. Compute DFT of a signal using				
3. Learn the IIR and FIR filter desi				
4. Understand the need of Multirate				
5. Understand the basics of DSP Pr				
Unit -1			Hours	5
Introduction: Introduction to Digi	tal Signal Processing: Discrete-tin	ne signals,		
Classification of Discrete-time syster	<u> </u>	•		
systems to arbitrary inputs. Solution of				
Frequency domain representation of d	liscrete-time signals and systems. Re	view of Z-	10	0
transforms, Solution of difference equ	ations using Z-transforms, System fu	nction.		
Unit -2				
Discrete Fourier Series & Fourier	r Transforms: Discrete Fourier Se	eries: DFS		
representation of periodic sequences l	Properties of DFS. Discrete Fourier t	ransforms:	1	0
Properties of DFT, Linear filtering m	ethods based on DFT, Fast Fourier	transforms	10	0
(FFT) - Radix-2 decimation in time	and decimation in frequency FFT A	Algorithms,		
Inverse FFT.				
Unit -3				
Design of IIR Digital Filters& Real	izations: Analog filter approximation	ns – Butter		
worth and Chebyshev, Design of I	IR Digital filters from analog filte	rs, Design		
Examples, Analog and Digital frequ	ency transformations. Basic structu	res of IIR		
systems, Transposed forms.			12	2
Design of FIR Digital Filters & Rea			1.	4
Frequency response. Design of FIR				
Frequency Sampling technique, Comp	parison of IIR & FIR filters, Basic st	ructures of		
FIR systems, Lattice structures.				
Unit – 4				
Multirate Digital Signal Processin	•			
Interpolation by a factor I, Sampli	•		10	0
Implementation of sampling rate of		rate signal		
processing: Sub-band Coding of Speed	ch Signals.			
Unit – 5				
DSP Processors: Introduction to pr			8	8
Accumulator, Modified bus structur				
Multiple Access Memory, Multi-po		Pipelining,		
Special addressing modes, On-Chip P			ļ	
	Total		5	0

Course outcomes:

On completion of the course, student will be able to

- 1. Illustrate the Discrete-time signals and systems.
- 2. Apply the FFT algorithm for solving the DFT of a given signal.
- 3. Construct a Digital IIR and FIR filter for the given specifications.
- 4. Apply Multirate signal Processing concepts in various applications.
- 5. Apply the signal processing concepts on DSP Processor.

Text Books:

1. John G. Proakis, Dimitris G. Manolakis, Digital Signal Processing, Principles, Algorithms, and Applications, Pearson Education, 2007.

2. A.V. Oppenheim and R.W. Schaffer, Discrete Time Signal Processing, PHI, 3rd Edition, 2010.

3. Venkataraman, Bhaskar, Digital Signal Processors, Architecture, Programming and

Applications, TATA McGraw Hill, 2002.

- 1. A Anand Kumar, Digital Signal Processing, PHI.
- 2. Robert J. Schilling, Sandra L. Harris, Fundamentals of Digital Signal Processing using MATLAB, Thomson, 2007.

DATA COMM	UNICATION AND NETWORKI SEMESTER VI	NG		
Subject Code		Internal Mark	s	30
Number of Lecture Hours/Week		External Marl		70
Total Number of Lecture Hours		Exam Hours		03
	1	Cre	dits -	- 03
Course Objectives:				
This course will enable students to				
• Understand the concept of data	a communications and network con	nection.		
• Design and analyze the operat	ion of data link layer.			
• Understand the routing protoc	ols in the Network Layer.			
• Understand the operation of tr	ansport layer and IP.			
• The application layer and Prin	ciples of Networking Applications.			
Unit -1	· · · · ·		Hou	rs
Introduction to Data Communicat	ions: Components, Data Representa	ation, Data		
Flow, types of networks, Network	Topologies, Network Models, Cat	egories of		
Networks Interconnection of Netw	orks, Network Models, Layered T	asks, OSI		
model, Layers in OSI model, TCP/II	P Protocol Suite, Wireless Links and	d Network	10)
Characteristics, WiFi: 802.11 Wirele	ess LANs -The 802.11 Architecture	•		
Unit -2				
Data Link Layer: Links, Access N Layer, The Services Provided by th Parity Checks, Check summing M Framing, Flow Control and Error O Noisy Channels, HDLC, Multiple A Controlled access.	he Link Layer, Types of errors, Re lethods, Cyclic Redundancy Check Control protocols, Noisy less Cha	dundancy, (CRC), annels and	10)
Unit -3		·		
The Network Layer: Introduction Models, Virtual Circuit and Dat Datagram Networks, Origins of VO Input Processing, Switching, Output Plane, Internet protocols:IPv4 and II	agram Networks-Virtual-Circuit C and Datagram Networks, Inside It Processing, Queuing, The Routir	Networks, a Router-	10)
Unit – 4				
Transport Layer: Introduction a Between Transport and Network La Internet, Multiplexing and De-mu Connection oriented Transport: TCF	yers, Overview of the Transport La ltiplexing, Connectionless Transp	ayer in the	10)
Unit – 5				
Application Layer: Principles of Protocol, Electronic Mail, The web	U 11	Transfer	8	
		Total	48	8

Course Outcomes:

- 1. Able to understand the knowledge of data communications and its networking.
- 2. Able to design and analyze various error detection techniques.
- 3. Able to demonstrate the mechanism of routing the data in network layer.
- 4. Able to design the transport layer protocol.
- 5. Able to understand the application layer.

Text Books:

- 1. Computer Networking A Top-Down Approach Kurose James F, Keith W, 6thEdition , Pearson, 2017.
- 2. Data Communications and Networking Behrouz A.Forouzan4th Edition McGraw Hill Education, 2017.

- 1. Data communication and Networks Bhusan Trivedi, Oxford university press, 2016.
- 2. Computer Networks -- Andrew S Tanenbaum, 4th Edition, Pearson Education, 2003.
- 3. Understanding Communications and Networks, 3 rd Edition, W.A.Shay, Cengage Learning, 2003.

MICROCONTROLI	L ERS AND APPLIC MESTER VI	ATIONS	
Subject Code	21ETETT6030	Internal Marks	30
Number of Lecture Hours/Week	3	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
		Cre	edits – 03
 Course Objective: This course will enable students to: Understand internal architecture and Learn the programming models of 80 Interpret the concept of 8051 microcourse I/O ports interfacing. Discuss the operational aspects of additional spects of a	051 Microcontroller u ontroller internal arch	sing embedded C. tecture like Timer/	
Unit -1			Hours
Intel 8051 Microcontroller Architect (8051): Microprocessors & Microcontr Microcontroller, Internal Block Diagr Memory Organization, Internal RAM interfacing. Unit -2	ollers Comparison, C am of 8051, Pin D	Overview of 8051 iagram of 8051,	8
Intel 8051 Interfacing and Programm	6		
8051 Interfacing- Input/output ports a input/output, Interrupts. Instruction syr 8051 Programming Concepts- Assee Instructions and basic 8051 Assembly L and arithmetic Instructions	ntax, addressing modern bler directives,	es with examples Classification of	10
and arithmetic Instructions. Unit – 3			
8051 I/O Interfacing: Introduction interfacing, keypad interfacing, Seven DAC interfacing, 2x16 LCD interfacing interfacing, high power devices. En interfacings.	Segment Display into g stepper motor inter	erfacing, ADC & facing, serial port	10
Unit – 4			
Architecture, Pin functions, Arduino I uploading sketches, Arduino Shield Interfacing- LED's, Switches, Seven se POT, Introduction to sensors and actua other basic electronic components programming.	s and Libraries. Ge gment display, Relay ators, Digital and Ana	compiling and eneral Hardware s, LCD, Buzzer, log Sensors, and	10
Unit – 5			
 Introduction to On board Computing board, Board preparation and installation updating of OS. Basic Commands in Doperation. Raspberry Pi GPIO operations: Compine of the Raspberry Pi, RPi. GPIO lipins, General purpose IO pins, Protocol Doperations. 	n procedures of OS, C Raspberry Pi OS, Ba mmunication with de brary, Python functio	Configuration and sh command line vices through the ns, setting up the	10

like LED, Buzzer, Switch, Display, matric keyboard and seven segment display	
etc.	
Total	48
Course outcomes:	
On completion of the course student will be able to	
1. Understand the internal operations of 8051microcontroller	
2. Apply the programming model of 8051 Microcontroller using embedded C.	
3. Apply the interfacing concepts of 8051 with I/O ports and other peripherals.	
4. Demonstrate the real world solutions with 8051 Microcontroller.	
5. Discuss the operational aspects of advanced Processors.	
6. Interpret the operational view of on board computers.	
Text Books:	
1. The 8051 Microcontroller and Embedded Systems Using Assembly and	ł C by
Muhammad Ali Mazidi, Rolinmckinlay Janice GillispieMazidi, Pearson, Second Ed	lition.
2. Programming and Interfacing with Arduino by Yogesh Misra, CRC Press,	ISBN:
9781000431681, 1000431681.	
3. Exploring Raspberry Pi-Interfacing to the Real World with Embedded LinuxB	y Derek
Molloy · 2016, ISBN: 9781119188681, 1119188687.	
Reference Books:	
1.Kenneth J. Ayala, The 8051 Microcontroller, Penram International Publishing	, 1996.
2.Raspberry PI 3: a Comprehensive Beginner's Guide From a to Z Simple S	teps by
Darryl Barton · 2016, ISBN: 9781540328199, 1540328198.	
3. Programming Arduino Getting Started with Sketches by Simon Monk	· 2011,
McGraw-Hill Education, ISBN:9780071784238, 0071784233.	

Professional Elective-II

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP604A	Spread Spectrum Techniques	3	0	0	3
2	21ETETP604B	Advanced VLSI Design	3	0	0	3
3	21ETETP604C	Microwave Engineering	3	0	0	3

SPREAD SPE	CTRUM TECHNIQ	UES	
(Profe	ssional Elective-II)	-	
Subject Code	EMESTER VI 21ETETP604A	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	External Warks	03
	40 Credits -03	Exam Hours	03
Course Objectives:	cicuits – 05		
This course will enable students to:			
 Understand the concept of Spread S sequences and their generation. 	Spectrum and study va	arious types of Spr	read spectrum
• Understand the principles of Code spectrum concept in CDMA.	Division Multiple Acc	cess (CDMA) and	use of Spread
• Understand various Code tracing	loops for optimum tra	acking of wideban	d signals viz
Spread spectrum signals.	n ale an air a chianna a' C	·····	a tha C 1
• Understand the procedure for system signal.	nchronization of rece	eiver for receiving	g the Spread
 Study the performance of spread st 	spectrum systems in	Iamming environn	nent systems
with Forward Error Correction and			
Unit -1			Hours
Introduction to Spread Spectrum Syst	ems: Fundamental Co	oncepts of Spread	
Spectrum Systems, Pseudo Noise Sequen			
Frequency Hop Spread Spectrum, Hyb			8
Spread Spectrum, Code Division Mu		_	
Sequences for Spread Spectrum S Mathematical Background and Sequence	•		
Length Sequences, Gold Codes.			
Unit -2			
Code Tracking Loops: Introduction, Op	timum Tracking of W	ideband Signals,	8
Base Band Delay-Lock Tracking Loop,			0
Loop, Double Dither Non-Coherent Trac	king Loop.		
Unit – 3			
Initial Synchronization of the Recei			
1	num Synchronizer,		11
Synchronization Techniques, Synchron	-	Matched Filter,	
Synchronization by Estimated the Receiv	eu spreading Code.		
Unit – 4			
Cellular Code Division Multiple Acce	· · · ·	,	
Wide Band Mobile Channel, The Cellular	•		
in a Multi User Channel, CDMA Syste CDMA Cellular Radio: Optimal Mult	1 ·		11
Detectors, Interference Combat Detection		-	
Techniques.			
Unit – 5			

Performance of Spread Spectrum Systems in Jamming Environments:	
Spread Spectrum Communication System Model, Performance of Spread	
Spectrum Systems without Coding. Performance of Spread Spectrum Systems	10
with Forward Error Correction: Elementary Block Coding Concepts, Optimum	
Decoding Rule, Calculation of Error Probability, Elementary Convolution	
Coding Concepts, Viterbi Algorithm, Decoding and Bit-Error Rate.	
Total	48
Course outcomes:	
On completion of this course, students are able to	
1.Understand Spread spectrum techniques and various codes used in SST.	
2.Explain code tracking loops and significance.	
3.Explain the concept of Synchronization of the receiver Spreading Code.	
4.Explain the Synchronization of Received Spreading Code.	
5.Understand the Interference Combat Detection Schemes, Interference	Cancellation
Techniques.	
6.Analyze the performance of Spread spectrum systems in Jamming envir	ronment and
systems with Forward Error Correction.	
Text Books:	
1.Rodger E Ziemer, Roger L. Peterson and David E Borth - "Introduction	on to Spread
Spectrum Communication- Pearson, 1st Edition, 1995.	1
2.Mosa Ali Abu-Rgheff – "Introduction to CDMA Wireless Communication	ns." Elsevier
Publications, 2008.	
References:	
1.George R. Cooper, Clare D. Mc Gillem - "Modern Communication and Sprea	dSpectrum."
McGraw Hill, 1986.	
2. Andrew j. Viterbi - "CDMA: Principles of spread spectrum communication	on." Pearson
Education, 1st Edition, 1995.	,

AI	OVANCED VLSI DESIGN (Professional Elective-II) SEMESTER VI			
Subject Code	21ETETP604B	Interna	l Marks	30
Number of Lecture Hours/Week	03		al Marks	70
Total Number of Lecture Hours	48	Exam Hours		03
Pre-requisite	Microelectronics and VLSI Design		edits – 03	
Course Objectives:				
This course will enable students to				
•To study the fundamental conce	epts in low power CMOS VLSI design.			
•To learn about different FPGA	designs and implementation.			
•Calculate yield and test vectors	for IC design.			
•To study the fundamental conce	epts of VLSI Interconnects.			
Unit -1			Hours	
Low-Power CMOS VLSI Design	: Sources of power dissipation, static	power		
- · · · ·	, designing of low power, circuit technic	ques for	8	
leakage power reduction.				
Unit -2				
FPGA families- Altera Flex 8000F	Basic FPGA architecture, FPGA Techno PGA, Altera Flex 10FPGA, Xilinx X	KC4000	10	
FPGA.	PGA, Xilinx Spartan II FPGAs, Xilinx	vertex		
Unit -3				
e .	roduction, Process Variations, Basic C	-		
	nents and Performance Modelling, Par	ametric	10	
Yield Estimation and Yield Maximiz	zation, Worst-Case Analysis.			
Unit – 4				
e .	h, Fault Types and Models, Controllabi	•	10	
	gn Techniques, Scan-Based Techniques	s, Built-		
In Self-Test (BIST) Techniques, Cur	rent Monitoring IDDQ Test.			
Unit – 5				
	ts: Distributed RC interconnect model,			
	cts, Elmore delay in RC tree and bu			
	of RC interconnect, Scaling Effects,	•		
0	C interconnect simulation session, In	aucuve	10	
effects in interconnects.		Total	48	
Course outcomes:		I Utal	40	
On completion of the course student	will be able to			
-	ots in low power CMOS VLSI design.			
2. Examine commercial architectu				
3. Understand design for Manufac				
4. Understand design for Testabili	•			
5. Understand the concepts of VLS	-			

5. Understand the concepts of VLSI Interconnects.

Text Books:

1. Kamran Eshraghian, Douglas and A. Pucknell and Sholeh Eshraghian, Essentials of VLSI Circuits and Systems, Prentice-Hall of India Private Limited, 2005.

2. Sung-Mo Kang, Yusuf Leblebic, CMOS Digital Integrated Circuits Analysis & Design McGraw-Hill Higher Education, 2002.

- 1. Introduction to VLSI Design by Eugene D. Fabricius, McGraw Hill International Editions, 1990.
- 2. Modern VLSI Design System on chip by Wayne Wolf, Pearson Education, 2002.

MICH	ROWAVE ENGINEERING			
	Professional Elective-II)			
	SEMESTER-VI			
Subject Code	21ETETP604C	Internal N	larks	30
Number of Lecture Hours/Week	03	External N		70
Total Number of Lecture Hours	50	Exam Ho	ours	03
Pre-requisite	EM Waves and Transmission	Credi	ts - 03	
_	Lines			
Course Objectives:				
This course will enable students to				
-	Rectangular waveguides, micro st	trip lines,	and car	vity
resonators				
2. Analyze the passive compone of these components	nts for microwave systems and obt	ain the cha	racteris	ucs
3. Analyze microwave O-type va	cuum tubes			
	amplification of the microwave s	ignals and	obtain	the
characteristics of O & M Type	Tubes.	-Shaib and	Jouin	
5. Understand the microwave me				
Unit -1	•		Hours	5
Introduction to microwaves:	History, Microwave Spectrum an	d Bands,		
Applications of Microwaves.				
Rectangular Waveguides: Introd				
Degenerate Modes, Sketches of TE a			12	2
Characteristics – Phase and Grou				
Relations, Power Transmission and Pov TEM mode. Related Problems.	ver Losses in Rectangular Guide, Impo	ossibility of		
Microstrip Lines– Introduction, 2	Zo Relations Effective Dielectric	Constant		
Losses, Q factor.		Constant,		
Unit -2				
Microwave Passive Component	s: Coupling Mechanisms – Prol	be, Loop,		
Aperture types. Waveguide Attenu				
Waveguide Phase Shifters – Diele			1(
Significance, Formulation and Pro	perties, S-Matrix Calculations for	-2 port	10)
Junction, E-plane and H-plane T	ees, Magic Tee, Hybrid Ring; D	Directional		
Couplers – 2Hole, Bethe Hole type	-			
Matrix Calculations for Gyrator, Iso	plator, Circulator, Related Problems			
Unit -3				
Microwave Tubes: Limitations an				
frequencies, Re-entrant Cavities,	• •	• -		
classifications, O-type tubes :2 Cavi				
Process and Applegate Diagram, B	unching Process and Small Signal	Theory –,	10)
Applications. Reflex Klystrons – Structure, Ap	plegate Diagram and Principle of	f working		
Electronic Admittance; Oscillating		-		
and Mechanical Tuning, Applicatio	-			
Unit – 4	,			
Helix TWTs: Significance, Types	and Characteristics of Slow Wave S	Structures:		
Structure of TWT and Suppression			8	
	s-field effects, Magnetrons - Different	ent Types,		
		• • • /		

Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p	
characteristics	
Unit – 5	
 Microwave Solid State Devices: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diode – Principle, RWH Theory, Characteristics, Basic Modes of Operation, Oscillation Modes. Microwave Measurements: Description of Microwave Bench – Different 	10
Blocks and their Features, Precautions; Microwave Power Measurement –	
Bolometer Method. Measurement of Attenuation, Frequency, Q- factor, Phase	
shift, VSWR, Impedance Measurement	
Total	50
 Course outcomes: On completion of the course, student will be able to 1. To understand microwave transmission lines 2. To analyze various microwave passive components with their working 3. To analyze various microwave O-type tubes 4. To analyze various M Type microwave vacuum tubes 5. To study the importance of microwave measurements 	
Text Books: 1. Samuel Y. Liao, Microwave Devices and Circuits, Pearson, 1990	
 Samuel T. Elao, Wierowave Devices and Circuits, Tearson, 1990 M. Kulkarni, Microwave and Radar Engineering, Umesh Publications, 2009 	
Reference Books:	
 Annapurna Das and Sisir K. Das, "Microwave Engineering", 3rd Edition, Tata Hill Education, 2000 	
2. G S N Raju, Microwave Engineering, I K International Publishing House Pvt.	Ltd, 2013

DIGITAL S	SIGNAL PROCES SEMESTER VI	SING LAB	
Subject Code	21ETETL6060	Internal Marks	15
Number of Lecture Hours/Week	03	External Marks	35
Total Number of Hours	36	Exam Hours	03
	Credits – 1.5		
Course Objectives:			
This course will enable students to			
1. Generate the fundamental discr	ete-time signals.		
2. Perform Convolution and DFT	-		
3. Design Infinite Impulse Respon			
4. Design Finite Impulse Respons			
5. Understand the concept of Nois			T
List of Experiments: students has to	• •	•	Hours
1. Generation of discrete-time sig		-	
2. To verify the Linear Convolutio	n for discrete signa	ls	
a. Using MATLAB			
b. Using Code Composer Studio			
3. To verify the Circular Convolution	ion for discrete sigr	nals	
a. Using MATLAB			
b. Using Code Composer Studio	o (CCS)		
4. To verify the autocorrelation b	etween two discret	te signals	
5. To verify Discrete Fourier Tra	nsform (DFT) and	Inverse Discrete Fourier	
Transform (IDFT)			
a. Using MATLAB			36
b. Using Code Composer Studio	o (CCS)		
6. Determination of the power sp	ectrum of a discret	e signal	
7. Frequency Response of IIR low	pass Butterworth F	Filter	
8. Frequency Response of IIR High	n pass Butterworth	Filter	
9. Frequency Response of IIR Low	v pass Chebyshev Fi	lter	
10. Frequency Response of IIR high	n pass Chebyshev Fi	ilter	
11. Frequency Response of FIR low	pass Filter using R	ectangle Window	
12. Frequency Response of FIR hig	h pass Filter using F	Rectangle Window	
13. Implementation of the Decima	tion Process		
14. Implementation of Interpolation	on Process		
Course outcomes:			
On completion of the course, studer	nts will be able to		
1. Illustrate the fundamental disc	rete-time signals		
2. Experiment with the properties	s of an LTI system		
3. Construct a Digital IIR filter for	the given specificat	tions.	
4. Construct a Digital FIR filter for	the given specifica	tions.	
5. Apply basic building blocks of N	Aulti-rate signal pro	ocessing.	

	VLSI Design LAB		
	SEMESTER VI		
Subject Code	21ETETL6070	IA Marks	15
Number of Lecture Hours/ Week	03	Exam Marks	35
Total Number of Lecture Hours	36	Exams Hours	03
			Credits -1.5

Course Objectives:

This lab will enable the students to

- 6. Design CMOS logic circuits.
- 7. Simulate combinational and sequential CMOS circuits.
- 8. Analyze layouts for combinational CMOS circuits.
- 9. Analyze of layouts for sequential CMOS circuits.
- 10. Perform DRC and LVS for CMOS circuits.

List of Experiments:	Hours
Conduct any ten experiments using Mentor Graphics/Cadence/Synopsis	
software.	
13. Design and Implementation of an Inverter.	
14. Design and Implementation of a NAND Gate.	
15. Design and Implementation of an NOR Gate.	
16. Design and Implementation of Full Adder.	
17. Design and Implementation of 4-bit Ripple Carry Adder.	36
18. Design and Implementation of Multiplexer using Transmission Gate.	50
19. Design and Implementation of Decoder.	
20. Design and Implementation of D Flip-flop.	
21. Design and Implementation 4-bit Register.	
22. Design and Implementation asynchronous counter.	
23. Design and Implementation of static RAM cell.	
24. Design and Implementation of Sequence Detector.	
Course Outcomes:	
On Completion of the lab, student will be able to	
6. Design CMOS logic circuits	
7. Design and simulate Combinational and Sequential CMOS circuits.	
8. Generate and verify layouts for combinational CMOS circuits.	
9. Generate and verify layouts for sequential CMOS circuits.	
10 Design and analyze DBC and LVS for CMOS	

10. Design and analyze DRC and LVS for CMOS.

Microc	ontroller and Applications L SEMESTER VI	ab	
Subject Code	18ETETL6080	Internal Mark	is 15
Number of Lecture Hours/Week	03	External Mar	ks 35
Total Number of Hours	36	Exam Hours	03
			Credits -1.5
 Course Objectives: This course will enable students to 1. To learn the basic input and 2. Understand the concepts of I/O Devices. 3. To develop Embedded C la 4. To develop Python languag 5. Providing the basic knowled List of Experiments: Part-A (Minimum of 3 Experiment 1. Write a Embedded C program LEDs Interfacing with 8051 2. Write a Embedded C program display interface with 8051 3. Stepper motor interfacing with 4. External ADC and Temperature 	l output interfacing concepts w Arduino Uno, Raspberry Pi Be nguage program skills. e program skills. dge of interfacing various perij s has to be performed) and verify different timer mod for displaying digits in 2 digi 8051 for clockwise and anticlo	oard and differen pherals to Raspbe le operations for t seven segment ockwise rotation.	t types of
5. Implement Serial Communication Part-B (Perform all Experiments)			
 Introduction and history of A Arduino Desktop IDE, Install Arduino programming. Introduction to Raspberry Pi Bo required, download and installa in the memory card and booting Part-C (Perform any 3 Experiment 	ling Libraries, functions and bard, identification of compone ation procedures of necessary s g of Raspberry Pi board.	components of nts and software	-
 Write an Embedded C program Uno Board. Write an Embedded C program interfacing with Arduino Uno F Write an Embedded C Program motor with Arduino Uno Write an Embedded C Program with Arduino Uno. Write an Embedded C Program modules with Arduino Uno and 	to display "Hello World" mess Board. m to control speed and direction to control speed and direction um to implement real time cl 1 12C LCD.	age on I2C LCD ion of a stepper n of a DC motor	
Part- D (Minimum of 2 Experimen1.Write a Python program Raspbery Pi Board.2.Write a Python code to inter	- ·		36

- 3. Write a Python code to read following sensor data and display the data in TFT screen. a) DHT11/22, b). Light Sensor (TEMT6000).
- 4. Write a Python code to read soil moisture and DS18B20sensor interfacing with raspberry Pi board and display in TFT screen.

Course outcomes:

On completion of the course student will be able to

- 1. Illustrate the interfacing concepts of various components with 8051 microcontroller board.
- 2. Understand the concepts of Arduino Uno and different types of I/O Devices.
- 3. Develop Embedded C programs for different applications using Arduino Uno
- 4. Construct interfacing circuits for different Applications using Raspberry Pi
- 5. Compile, design and test various hardware components with raspberry Pi using Python Language

	Course Structure for IV B.Tech ECT Under the Regulations of SITE-21						
	Semester -VII						
S.No	Subject Code	Name of the subject	L	т	Ρ	С	
1	21ETETP701X	Professional Elective -III	3	0	0	3	
2	21ETETP702X	Professional Elective -IV	3	0	0	3	
3	21ETETP703X	Professional Elective -V	3	0	0	3	
4	21ETXXO704X	Open Elective - III	3	0	0	3	
5	21ETXXO705X	Open Elective - IV	3	0	0	3	
6	21ETXXO706X	Open Elective - V	3	0	0	3	
7	21CMAHS7070	Skill advanced course/ soft skill course	1	0	2	2	
8	21ETETR7080	Summer Internship 2 months after Third year (To be evaluated during VII sem)			0	3	
	Total Semester Credits					23	
9		Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)	4	0	0	4	

Professional Elective-III

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP701A	Digital Image Processing	3	0	0	3
2	21ETETP701B	Telecommunications and Switching Networks	3	0	0	3
3	21ETETP701C	CAD Tools for VLSI	3	0	0	3

Professional Elective-IV

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP702A	Bio-Medical Signal Processing	3	0	0	3
2	21ETETP702B	Global Position Systems	3	0	0	3
3	21ETETP702C	Optical Communication	3	0	0	3

Professional Elective-V

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP703A	RF System Design	3	0	0	3
2	21ETETP703B	Internet of Things	3	0	0	3
3	21ETETP703C	Radar and Satellite Communications	3	0	0	3

Open Elective-III

Candidate should select the subject from list of subjects offered by other departments

Open Elective-IV

Candidate should select the subject from list of subjects offered by other departments

Open Elective-V

Candidate should select the subject from list of subjects offered by other departments

DIGIT	AL IMAGE PROCES	SING	
(Pr	ofessional Elective-	lll)	
,	SEMESTER VII		
Subject Code	21ETETP701A	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Hours	50	Exam Hours	03
Prerequisite	SS & DSP		Credits – 3
Course Objectives:			
This course will enable students to			
1. Familiarize with basic conce	pts of digital image	processing and image tra	ansforms.
2. Make use of filtering in spati			
3. Inference the images using w	1 .		models.
4. Outline the color models and			
on grayscale images.	1 1	0 0 1	0 1
5. Choose various segmentation	n algorithms on digi	ital images	
Unit-1			Hours
Introduction: The origins of Digit	tal Image Processir	ng, Fundamental steps in	1
Digital Image Processing, Compon			
sensing and acquisition, Image			
relationships between pixels.	1 0 1		12
Image Transforms: Need for image	e transforms, 2-D D	Discrete Fourier transform	1
(DFT) and its properties, Walsh tran			
Discrete cosine transform, PCA and			,
Unit-2			
Intensity Transformations and S	patial Filtering:	Background, some basic	c
intensity transformation functions, H	• 0	0	
filtering, smoothing spatial filters, S	harpening spatial fil	lters.	10
Filtering in the Frequency Doma			y 10
domain, Image smoothing using fi			
using frequency domain filters, Sele			
Unit-3			
Wavelets and Multiresolution Pro	cessing: Image py	ramids, Sub-band coding	
Multiresolution expansions, Wave			
dimensions, Wavelet coding.			10
Image Compression: Fundamenta	als, Basic compres	sion methods: Huffman	1 0
coding, Arithmetic coding, LZW c	oding, Run-length	coding, Block transform	ı
coding, Predictive coding.			
Unit-4			
Color Image Processing: Color 1	fundamentals, Colo	or models, Pseudo colo	r
Image Processing.			
Morphological Image Processing:	Preliminaries, Erosi	ion and Dilation, Opening	g 8
and Closing, Basic morphological al			
Unit-5			
Image segmentation: Fundamer	ntals, Point, Line	and Edge detection	, 10
Thresholding, Region-based Segmen		-	' 10

Case studies on digital image processing: Feature Detection, Face Recognition,	
Image Cryptography.	
Total	50
Course outcomes:	
On completion of the course, students will be able to	
 Interpret the fundamentals of digital image processing and apply various tr on digital images. 	ansforms
2.Apply filtering concepts in spatial and frequency domains	
3. Analyze digital images using compression algorithms	
4. Classify the color models and interpret the Morphological image processing	concepts
to grayscale images.	
5. Apply various segmentation algorithms on digital images	
Text Books:	
 R. C. Gonzalez and R. E. Woods, "Digital Image Processing", 3rdedition, Hall, 2008 	, Prentice
2. Jayaraman, S. Esakkirajan, and T. Veerakumar," Digital Image Processin McGraw-HillEducation, 2011.	ng", Tata
Reference Books:	
1. Anil K.Jain, "Fundamentals of Digital Image Processing", Prentice Hall	of India,
7 th Edition, Indian Reprint, 1989	

7thEdition, Indian Reprint, 1989
2. B.Chanda, D.Dutta Majumder, "Digital Image Processing and Analysis", PHI, 2009

Telecommunications and Switching Networks					
(Professional Elective –III) SEMESTER VII					
Subject Code	21ETETP701B	Internal Marks	30		
Number of Lecture Hours/Week	03	External Marks	70		
Total Number of Lecture Hours	48	Exam Hours	03		
			Credits - 03		

Course Objectives:

This course will enable the students to:

- To provide students with a balanced blend of theoretical and practical aspects regarding Telecommunication Switching System.
- To expose through the evolution of switching systems from manual and Electro mechanical systems to stored-program-controlled digital systems.
- To provide knowledge to the students regarding design and performance analysis of various switching systems.
- To train the students about basic Telephone Networks structures and traffic Engineering concepts.
- To inculcate students on various internet concepts like OSI reference model, LAN, WAN, WAN, Repeaters, bridges, routers ,gateways ,data communication networks and ISDN.

wary, Repeaters, bruges, routers, gateways, data communication networks an	u ISDN.
Unit -1	Hours
Telecommunication Switching Systems: Introduction, Elements of switching systems, switching network configuration, Rotary switches, Uniselector, Two motion selector, Trunking principle ,principles of cross bar switching, Crossbar Switch Configuration, Cross point Technology, Crossbar Exchange Organization.	9
Unit -2	
Electronic Space Division Switching: Stored Program Control, Centralized SPC, Distributed SPC, Software Architecture, Application Software, Enhanced services, Two Stage Networks, Three-Stage Networks, n-Stage Networks. Time Division Switching: Basic Time Division Space Switching, Basic Time Division Time Switching, Time Multiplexed Space Switching, Time Multiplexed Time Switching, Combination Switching, Three Stage Combination Switching.	10
Unit – 3	
Telecommunications Traffic: Introduction, The Unit of Traffic, Congestion; Traffic Measurement, A Mathematical Model, Lost-Call Systems-Theory, Traffic Performance, Loss Systems in Tandem, Use of Traffic Tables. Queuing Systems: The Second Erlang Distribution, Probability of Delay, Finite Queue Capacity, Some Other Useful Results, Systems with a Single Server, Queues in Tandem, Delay Tables, Applications of Delay Formulae.	10
Unit – 4	

Telephone Networks: Subscriber loop systems, switching hierarchy and routing, transmission plan, transmission systems, numbering plan, charging plan, Signaling techniques: In channel signaling, common channel signaling, Cellular mobile	10
telephony.	10
Data Networks: Data transmission in PSTNs, Switching techniques for data	
transmission, data communication architecture, link to link layers, end to end layers,	
satellite based data networks, LAN, MAN, Internetworking.	
Unit – 5	
Integrated Services Digital Network (ISDN): Introduction, motivation, new	
services, Network and protocol architecture, Transmission channels, User-Network	
interfaces, functional grouping, reference points, signaling, numbering, addressing,	
BISDN. DSL Technology: ADSL, Cable Modem, Traditional Cable Networks, HFC	9
Networks, Sharing, CM & CMTS and DOCSIS. SONET: Devices, Frame, Frame	
Transmission, Synchronous Transport Signals, and STS I, Virtual Tributaries, and	
Higher rate of service.	
Total	48
Course outcomes:	
On completion of the course student will be able to:	
1. Students will be able to analyze different switching methodologies.	
2. Students will be able to differentiate between signaling methods Telecommunication Networks.	used in
3. Students will be able to understand queuing systems and models.	
4. Students will exhibit a good knowledge on data communication networks and Is able to differentiate LAN, MAN, WAN.	SDN and be
5. Students will demonstrate an ability to work on various Telecommunication concepts.	on Network
6. Students will demonstrate knowledge on modern telecommunication concepts SONET.	like DSL &
Text Books:	
1. Tele communication switching system and networks – Thyagarajan Viswanath,	PHI, 2000.
2. J. E Flood, "Telecommunications Switching and Traffic Networks," Pearson	
2006.	,
2006. 3. Data Communication & Networking - B.A. Forouzan, TMH, 4th Edition, 2004 Reference Books:	
3. Data Communication & Networking - B.A. Forouzan, TMH, 4th Edition, 2004	
3. Data Communication & Networking - B.A. Forouzan, TMH, 4th Edition, 2004 Reference Books :	
 3. Data Communication & Networking - B.A. Forouzan, TMH, 4th Edition, 2004 Reference Books: Digital telephony - J. Bellamy, John Wiley, 2nd edition, 2001. 	4.

S.No	Subject Code	Name of the subject		Т	Р	Cr
1	21ETETP702A	Bio-Medical Signal Processing	3	0	0	3
2	21ETETP702B	Global Position Systems	3	0	0	3
3	21ETETP702C	Optical Communication	3	0	0	3

Professional Elective-IV

	Tools for VLSI onal Elective –IV)		
SEN	MESTER VII		
Subject Code	21ETETP701C	Internal Mark	ks 30
Number of Lecture Hours/Week	03	External Mar	ks 70
Total Number of Lecture Hours	48	Exam Hours	03
			Credits - 3
Course Objectives:			
This course will enable the students to:			
• Hardware software co-design.			
• Synthesis tools and VHDL mode	eling for digital circuit	S.	
Computational complexity issue	s in testing the circuits	5.	
• Simulation for various design cir	rcuits.		
Unit -1			Hours
Introduction: VLSI design metho	dologies and supp	orting CAD	
environment Schematic editors: Parsin			10
formats, Graphics & Plotting Layout. La			10
editor.	, 0	1	
Unit -2			
Layout Language and Analysis: Lay	out language- Param	eterized cells.	
PLA generators, Introduction to Silico			9
Placement & routing, Floor planning. Layout Analysis-Design rules, Object			
based DRC, Edge based layout operations. Module generators.			
Unit – 3		·	
Simulation Algorithms: Types of simu	ulation, Behavioral sin	mulator, logic	10
simulator, functional simulator & Circuit simulator. Introduction and			10
significance of Compiled code and Event-driven simulation algorithms.			
Unit – 4			
Optimization Algorithms: Greedy me	thods, simulated anne	aling, genetic	9
algorithm and neural models.			9
Unit – 5			
Testing ICs: Fault simulation, Aids			
Computational complexity issues: Big C			10
Recent topics in CAD-VLSI: Array	-	software co-	10
design, high-level synthesis tools and V	HDL modeling.		
~		Total	48
Course outcomes:			
On completion of the course student wil			
1. Understand VLSI design metho		ing CAD enviro	onment and
also understand the Plotting Lay	out, Layout Editor.		
2. Design the layouts in VLSI.	f ' 1 '	• • .	
 Understand Simulation techniques for various design circuits. Explain Optimization Algorithms for the design circuits. 			
	-		
5. Analyze the Computational com Text Books:	plexity issues in testin	ig the circuits.	
	otion to CAD for V	VI SI" Vluuron	Acadomio
1. Stephen Trimberger," Introduc	LIIOII IO CAD IOP	vlsi, kiuwer	Academic

publisher, 2002

2. Naveed Shervani, "Algorithms for VLSI physical design Automation", Kluwer Academic Publisher, Second edition.

- 1. Gaynor E. Taylor, G. Russell, "Algorithmic and Knowledge Based CAD for VLSI", Peter peregrinus ltd. London.
- 2. Gerez, "Algorithms VLSI Design Automation", John Wiley & Sons.

Biomedical Signal Processing	
(Professional Elective –IV)	

SE	MESTER VII			
Subject Code	21ETETP702A	Internal Mar	ks	30
Number of Lecture Hours/Week	03	External Man	rks	70
Total Number of Lecture Hours	48	Exam Hours		03
			Credit	ts – 3
Course Objectives:				
This course will enable the students to:				
• Describe the Detection of biom	edical signals in noise			
• Analyze the Spectral analysis		ty - interaction	n with	other
physiological signals		•		
• Understand the categorization	of EEG activity - re	cording technic	ques -	EEG
applications	•	C C	•	
• Analyze the stochastic models -	- Non-linear modeling	of EEG		
Unit -1			Hou	
	Examples of Diama	diaal aignala	HOL	ILS
Introduction To Biomedical Signals				
ECG, EEG, EMG etc., Tasks in Biome	0	0		
Aided Diagnosis. Origin of bio poter Fourier Transform and Time Frequence			1(h
signals- Processing of Random &Stocl			10	J
Properties and effects of noise in bi				
biomedical instruments.	ometical mstruments	- Pintering in		
Unit -2				
Concurrent, Coupled and Correlated	d Processes - Illustrat	ion with case		
studies – Adaptive and optimal filterin				
Detection of biomedical signals in nois				
embedded in another -Maternal-F		0	1()
interference. Event detection – case studies with ECG & EEG - Independent				
Component Analysis - Cocktail party problem applied to EEG signals -				
Classification of biomedical signals	1 11	U		
Unit – 3				
Cardio Vascular Applications: Basi	ic ECG - Electrical A	ctivity of the		
heart- ECG data acquisition - ECG pa	rameters & their estim	ation - Use of	1(`
multi-scale analysis for ECG parame	ters estimation - Nois	se &Artifacts-	1(J
ECG Signal Processing: Baseline Wandering, Power line interference,				
Muscle noise filtering – QRS detection	-Arrhythmia analysis			
Unit – 4				
Data Compression: Lossless & Loss	y- Heart Rate Varia	bility – Time		
Domain measures - Heart Rhythm repre-	-	alysis of heart	9	
rate variability - interaction with other	physiological signals			
Unit – 5				
Neurological Applications: The elect	1 0	•		
waveform - categorization of EEG ac		-		
applications- Epilepsy, sleep disorders,	, brain computer interfa	ace. Modeling		
EEG-linear,			9	
Stochastic models – Non-linear model	-		,	
characteristics and processing – Mo	-	•		
segmentation - Joint Time-Frequency a	•	alysis of EEG		
channels - coherence analysis of EEG of	channels.			
Course outcomes:				

On completion of the course student will be able to

- 1. Understand the need of biomedical signals
- 2. Describe the Detection of biomedical signals in noise
- 3. Understand ECG, ECG parameters estimation
- 4. Analyze the Spectral analysis of heart rate variability interaction with other physiological signals
- 5. Understand the categorization of EEG activity recording techniques EEG applications
- 6. Analyze the stochastic models Non-linear modeling of EEG

Text Books:

- 1. D.C.Reddy ,"Biomedical Signal Processing: Principles and techniques" ,Tata McGraw Hill, New Delhi
- 2. Willis J Tompkins, Biomedical Signal Processing -, ED, Prentice Hall

- 1. Biomedical Signal Processing, MetinAkay, Academic Press
- 2. Biomedical Signal Processing: Volume 2: Compression and Automatic Recognition, ArnonCohen, CRC Press
- 3. Biomedical Signal Processing: Advances in Theory, Algorithms and Applications, Ganesh Naik, Springer

Global Positioning Systems (Professional Elective –IV) SEMESTER VII				
Subject Code	21ETETP702B	Internal Marks	30	
Number of Lecture Hours/Week	03	External Marks	70	
Total Number of Lecture Hours	48	Exam Hours	03	
Credits – 3				

Course Objectives:	
This course will enable the students to:	
• To introduce fundamental blocks of global positioning system	
• To analysis on signal characteristics of GPS	
• Explore to the GPS Design analysis	
 Illustrate about differential GPS 	
 Introduce about applications of GPS 	
Unit -1	Hours
Introduction: Basic concept, system architecture, GPS and GLONASS	
Overview, Satellite Navigation, Time and GPS, User position and velocity	10
calculations, GPS, Satellite Constellation, Operation Segment, User	10
receiving Equipment, Space Segment Phased development, GPS aided Geo-	
augmented navigation (GAGAN) architecture.	
Unit -2	
Signal Characteristics: GPS signal components, purpose, properties and	
power level, signal acquisition and tracking , Navigation information	
extraction, pseudo range estimation, frequency estimation, GPS satellite	9
position calculation, Signal structure, anti spoofing (AS), selective	
availability, Difference between GPS and GALILEO satellite construction.	
Unit – 3	
GPS Receivers & Data Errors: Receiver Architecture, receiver design	
options, Antenna design, GPS error sources, SA errors, propagation errors,	
ionosphere error, troposphere error, multipath, ionosphere error, estimation	10
using dual frequency GPS receiver.	
Multipath Mitigation: Methods of multipath mitigation, Ephemeris data	
errors, clock errors.	
Unit – 4	
Differential GPS: Introduction, LADGPS, WADGPS, Wide Area	
Augmentation systems, GEO Uplink subsystem, GEO downlink systems,	9
Geo Orbit determination, Geometric analysis, covariance analysis, GPS	,
/INS Integration Architectures	
Unit – 5	
GPS Applications: GPS in surveying, Mapping and Geographical	
Information System, Precision approach Aircraft landing system, Military	
and Space application, and intelligent transportation system. GPS orbital	10
parameters, description of receiver independent exchange format (RINEX),	10
Observation data and navigation message data parameters, GPS position	
determination, least squares method	
Course outcomes:	
On completion of the course student will be able to	
1. Explain about fundamental blocks of global positioning system	
2. signal characteristics of GPS are analyzed	
3. Explore to the GPS Design analysis.	
4. Illustrate about differential GPS	
5. Explain and trained towards applications of GPS	
Text Books:	
1. Mohinder S.Grewal, Lawrence R.Weill, Angus P.Andrews, "Global	positioning
systems, Inertial Navigation and Integration", Wiley 2007.	<u> </u>
Reference Books:	
1. E.D.Kaplan, Christopher J. Hegarty, "Understanding GPS Prin	nciples and
	•

Applications", Artech House Boston 2005.

OPTICAL	COMMUNICATIONS	5	
	ssional Elective-IV)		
Subject Code	MESTER-VII 21ETETP702C	Internal Marks	30
Number of Lecture Hours/Week	3	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
	50		Credits-03
CourseObjectives:			
This course will enable students to:			
• Familiarize with basic concepts and	d theory of optical com	munication.	
• Understand the signal loss with t	heir computation and	dispersion mechan	ism
occurring in optical fiber cable.			
 Analyze the operation of LEDs, las 	-	to detectors.	
• Understand the different types of fi			
• Understand the performance of opt	ical systems.		
Unit-1	· · · · · · · · · · · · · · · · · · ·		Hours
Over view of optical fiber communication system, advantages of optical fiber communication optical fiber communica			
Introduction, Ray theory transmission, To			
Numerical Aperture, Skew rays, cylin			11
coupling, Step Index fibers, Graded In			
wavelength, Mode Field Diameter, Effect			
Unit-2		_	
Signal distortion in optical fibers: - Sign	al distortion in optical f	ibers-Attenuation,	
Absorption, Scattering and Bending losse	s, Core and Cladding lo	osses, Information	
capacity determination, Group delay, Ty			10
Wave-guide dispersion, Polarization-Mo		nodal dispersion,	10
Pulse broadening in Graded index fiber, E	Exercise problems.		
Unit-3			
Optical Sources -LEDs, Structures, M			
Modulation, Power bandwidth product. In conditions, External quantum efficiency, a			
frequencies, Reliability of LED & ILD. Op	-		
and APD, Detector response time, Te			12
Comparison of Photo detectors, Exercise		guili,	
Unit-4	L		
OpticalfiberConnectors-Connectortypes	,Singlemodefiberconne	ctors,Connector	
return loss, Fiber Splicing- Splicing techn			
alignment and joint loss-Multimode fiber			8
Unit-5	-		
Optical system design -Point-to-point lin	ks- component choice (and considerations	
Link power budget, Rise time budget wit	-		9
WDM, Necessity, Principles, Measurem			-
pattern.		······································	
-			
Total			50

Courseoutcomes:

Oncompletionofthecoursestudentwill beableto

- 1 Understandbasicconceptsofopticalfibers
- 2 Analyzedifferentlossesoccursinopticalfibersand
- 3 UnderstandtheoperationofLEDs,laser diodes,andPINphotodetectors
- 4 Illustratedifferenttypesofopticalconnectors
- 5 Analyze opticalsystemdesign.

TextBooks:

- 1. Optical Fiber Communications Gerd Keiser, Mc Graw-Hill International edition,3rdEdition,2000.
- 2. OpticalFiber Communications–JohnM. Senior,PHI, 2ndEdition, 2002.

- FiberOpticCommunications– D.K.Mynbaev,S.C.GuptaandLowellL.Scheiner,PearsonEducation,2005.
- 2. Text Book on Optical Fiber Communication and its Applications S.C.Gupta, PHI,2005.
- 3. Fiber Optic Communication Systems Govind P. Agarwal, John Wiley, 3rd Edition, 2004.
- 4. Fiber Optic Communications Joseph C. Palais, 4th Edition, Pearson Education, 2004.

Professional Elective-V

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21ETETP703A	RF System Design	3	0	0	3
2	21ETETP703B	Internet of Things	3	0	0	3
3	21ETETP703C	Radar and Satellite Communications	3	0	0	3

	RF System Desig	n	
(Professional Elective-		
	SEMESTER VII		
Subject Code	21ETETT703A	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
	·	Cree	dits -03
Course objectives:			
This course will enable the studer	nts to		
• Acquire the importance of F	RF Issues and variou	s considerations for design	
• Understand the filter design		C C	
• Understand the active comp	onents and applicati	ons	
 Design RF Amplifiers 			
• Analyze the characteristics of	-		
• Analyze High frequency mo	odels using oscillator	rs and mixers	
Unit -I			Hours
RF ISSUES		, . ,	
Importance of RF	U	comagnetic spectrum,	9
RF behavior of passive comp considerations, scattering parame			
Unit –II	ters, sinitir chart and	applications.	
RF FILTER DESIGN			
Overview, Basic resonator and the	filter configuration.	special filter realizations.	9
smith chart based filter design, co	-	-r,	
Unit-III			
ACTIVE RF COMPONENTS A	AND APPLICATIO	ONS	
RF diodes, BJT, RF FET'S, High	~		
Matching And Biasing Netw	_		12
components, microstrip line mate	ching networks, amj	plifter classes of operation	
and biasing networks Unit –IV			
RF AMPLIFIER DESIGNS			
Characteristics, amplifier power	relations stability	considerations constant	
gain circles, constant VSWR cir	-		10
and multistage amplifiers.		, <u>8</u>	
Unit-V			
OSCILLATORS			
Basic oscillator model, High Fre		onfiguration, Applications	
and analysis, qualitative treatmen	t		08
MIXERS & APPLICATIONS	1 .1 .	1 1 1 11 17 7	
Basic characteristic of mixers, wir and demodulator circuits	eiess synthesizers, p	nase locked loops, detector	
Course outcomes:			
On completion of the course stude	ent will be able to		
on completion of the course stud			
1. To acquire the importance	of RF Issues and v	arious considerations for dea	sion

- 3. To understand the active components and applications
- 4. To design RF Amplifiers
- 5. To analyze the characteristics of RF Amplifiers
- 6. To analyze High frequency models using oscillators and mixers

- 1. Reinhold Ludwig and Powel Bretchko, RF Circuit Design Theory and Applications, Pearson Education Asia, First Edition, 2001.
- 2. Joseph.J. Carr, Secrets of RF Circuit Design , McGraw Hill Publishers, Thi rd Edition,2000.

- 1. Mathew
- M. Radmanesh, Radio Frequency & Microwave Electronics, Pearson Educati on Asia, Second Edition, 2002.
- 2. Ulrich L. Rohde and David P. New Kirk, RF / Microwave Circuit Design, John Wiley & Sons USA 2000.
- 3. RolandE. Best, Phase Locked Loops: Design, simulation and applications, Mc Graw Hill Publishers 5TH edition 2003

INTERNET OF THINGS						
(Professional Elective-V)						
SEMESTER VII						
Subject Code	21ETETP703B	Internal Marks	30			
Number of Lecture Hours/Week	03	External Marks	70			
Total Number of Lecture Hours	50	Exam Hours	03			
Credits – 03						

Course Objective This course will enable students to:	
1. To study fundamental concepts of IoT	
2. To understand roles of sensors in IoT	
3. To Learn different protocols used for IoT design	
4. To be familiar with data handling and analytics tools in IoT	
5. Understand the role of IoT in various domains of Industry.	
Unit -1	Hours
Fundamentals of IoT: Introduction, Definitions & Characteristics of IoT, IoT	110015
Architectures, Physical & Logical Design of IoT, Enabling Technologies in IoT,	10
History of IoT, AboutThings in IoT, The Identifiers in IoT, About the Internet	10
in IoT, IoT frameworks, IoT andM2M.	
Unit-2: Sengers Notworks + Definition Types of Sensors Types of Actuators	
Sensors Networks : Definition, Types of Sensors, Types of Actuators,	
Examples and Working, IoT Development Boards: Arduino IDE and Board	10
Types, RaspberriPi Development Kit, RFID Principles and components,	10
Wireless Sensor Networks: History and Context, The node, Connecting nodes,	
Networking Nodes, WSN and IoT.	
Wireless Technologies for IoT: WPAN Technologies for IoT: IEEE 802.15.4,	
Zigbee, HART, NFC, Z-Wave, BLE, Bacnet, Modbus. IP Based Protocols for IoT	10
IPv6, 6LowPAN, RPL, REST, AMPQ, CoAP, MQTT.Edge connectivity and	
protocols.	
Unit – 4 Deter Handling 8. Angleting, Jutus besting, Die leter Tennes, of the	
Data Handling& Analytics: Introduction, Bigdata, Types of data,	
Characteristics of Big data, Data handling Technologies, Flow of data, Data	10
acquisition, Data Storage, Introduction to Hadoop. Introduction to data	10
Analytics, Types of Data analytics, Local Analytics, Cloudanalytics and	
applications.	
Unit – 5 Applications of LaTe Home Automation Smort Citics Energy Datail	
Applications of IoT: Home Automation, Smart Cities, Energy, Retail	0
Management, Logistics, Agriculture, Health and Lifestyle, Industrial IoT, Legal	8
challenges, IoT design Ethics, IoT inEnvironmental Protection.	40
Total	48
Course outcomest	
Course outcomes: On completion of the course student will be able to	
1. Understand the various concepts, terminologies and architecture of IoT sy	stems
2. Use sensors and actuators for design of IoT.	
 Understand and apply various protocols for design of IoT systems 	
 Use various techniques of data storage and analytics in IoT 	
 Understand various applications of IoT. 	
Text Books:	
1. Hakima Chaouchi, — "The Internet of Things Connecting Objects to the second	ne Weh"
1. Tukina Chaoaon, The memory of things Conneeding Objects to u	
ISBN :978-1- 84821-140-7, Wiley Publications	

- 2. Olivier Hersent, David Boswarthick, and Omar Elloumi, "The Internet of Things:Key Applications and Protocols", WileyPublications
- 3. Vijay Madisetti and ArshdeepBahga, "Internet of Things (A Hands-on-Approach)",1st Edition, VPT, 2014.

Reference Books:

- 1. J. Biron and J. Follett, "Foundational Elements of an IoT Solution", O'Reilly Media,2016.
- 2. Keysight Technologies, "The Internet of Things: Enabling Technologies and Solutions for Design and Test", Application Note, 2016.
- 3. Daniel Minoli, "Building the Internet of Things with IPv6 and MIPv6: The EvolvingWorld of M2M Communications", ISBN: 978-1-118-47347-4, Willy Publications
- 4. Pethuru Raj and Anupama C. Raman, "The Internet of Things: Enabling Technologies, Platforms, and Use Cases", CRC Press

Web References:

- 1. https://onlinecourses.nptel.ac.in/noc17_cs22/course
- 2. http://www.cse.wustl.edu/~jain/cse570-15/ftp/iot_prot/index.html

Radar and Satellite Communications						
(Professional Elective –V)						
SEMESTER VII						
Subject Code	21ETETP704C	Internal Marks	30			
Number of Lecture Hours/Week	03	External Marks	70			
Total Number of Lecture Hours	48	Exam Hours	03			
Credits – 3						

Course Objectives:	
This course will enable the students to:	
• The goal of the course is to introduce students to the fundamentals of radar satellite communication.	r and
• To expose them to examples of applications and trade-offs that typica	lly occur in
engineering system design, and to ask them to apply the knowledge in desi	•
• This course contributes to the educational objectives - Fundamental	
specialization, design skills, and self – learning.	Kilowicuge,
Unit -1	II
Introduction to Radar	Hours
Introduction to radar, Radar block diagram and operation, Radar	
frequencies, Applications of radar, Prediction of range performance,	
Minimum detectable signal, Receiver noise, Probability density function,	10
SNR, Integration of radar pulses, Radar cross-section of targets, PRF and	
range ambiguities, Transmitter power, System losses.	
Unit -2	
Radar Technology	
Doppler Effect, CW radar, FM CW radar, multiple frequency CW radar.	
MTI radar, Delay line canceller, Range gated MTI radar, Blind speeds,	
Staggered PRF, Limitations to the performance of MTI radar, Non-coherent	9
MTI radar. Tracking radar: sequential lobbing, conical scan, Mono pulse:	
amplitude comparison and phase comparison methods, Radar antennas.	
Radar displays.	
Unit – 3	
Introduction to Satellite Communication	
Orbital aspects of Satellite Communication: Introduction to geo-	10
synchronous and geostationary satellites, Kepler's laws, locating the satellite	10
with respect to the earth, Subsatellite point, Look angles, Mechanics of	
launching a synchronous satellite, Orbital effects, Indian scenario in communication satellites	
Unit – 4	
Spacecraft and Earth station	
Satellite sub-systems: Attitude and Orbit control systems, Telemetry,	
Tracking and command control system, Power supply system, Space craft	10
antennas, and multiple access techniques, comparison of FDMA, TDMA,	
and CDMA. Earth station equipments, tracking systems	
Unit – 5	
Satellite Link Design	
Introduction to satellite link design, basic transmission theory, system noise	9
temperature and G/T ratio, design of down link and uplink, design of satellite	7
links for specified C/N, satellite data communication protocols.	
Course outcomes:	
On completion of the course student will be able to	
1. Analyze the RADAR equation and required parameters	
2. Understand various RADAR technologies and concept of radar tracking	
3. Learn the communication satellite mechanics and keplers laws.	
 Analyze various orbital parameters and orbital effects. Explain AOCS and various types of access techniques. 	
 Explain AOCS and various types of access techniques. Analyze satellite link design and calculate C/N 	
Text Books:	
I CAL DUURD	

- 1. Merril. I. Skolnik, "Introduction to Radar Systems", 2/e, MGH, 1981.
- 2. Mark A. Richards, James A. Scheer and William A. Holm, "Principles of Modern Radar: Basic Principles," YesDee Publishing Pvt. Ltd., India, 2012.

- 1. Byron Edde, "Radar: Principles, Technology, Applications", Pearson, 2008.
- 2. Timothy Pratt and Charles Bostian, "Satellite Communications", John Wiley, 1986.
- 3. Dennis Roddy, "Satellite Communications", MGraw Hill, Millan, 4th edition, 2013

	Course Structure for IV B.Tech ECT Under the Regulations of SITE-21							
	Semester -VIII							
S.No	Subject Code	Name of the subject	L	т	Ρ	С		
1	xxxxxx	Project work, seminar and internship in industry	0	0	24	12		
Total Semester Credits						12		

Open Elective Courses Offered By Department of Electronics and Communication Technology

Open Elective Courses Offered by the Department of Electronics and Communication Technology to other Departments:

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21XXETOX0XA	Introduction to Signals and Systems	3	0	0	3
2	21XXETOX0XB	Digital Logic Design	3	0	0	3
3	21XXETOX0XC	Principles of Communication Systems	3	0	0	3

Open Elective-II

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21XXETO605A	Electronic Measurements	3	0	0	3
2	21XXETO605B	Biomedical Electronics	3	0	0	3
3	21XXETO605C	Embedded C Programming	3	0	0	3

Open Elective-III

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21XXETO704A	Introduction to Photonics	3	0	0	3
2	21XXETO704B	IC Applications	3	0	0	3
3	21XXETO704C	Nano Electronics	3	0	0	3

Open Elective-IV

S.No	Subject Code	Name of the subject	L	Т	Р	Cr
1	21XXECO705A	Remote Sensing and GIS	3	0	0	3
2	21XXECO705B	Satellite Communications	3	0	0	3
3	21XXECO705C	Image Processing and Pattern Recognition	3	0	0	3

Introd	uction to Signals and System (Open Elective)	IS		
Subject Code	21XXETOX0XA	Internal M	[arks	30
Number of Lecture Hours/Week	03	External N		70
Total Number of Lecture Hours	48	Exam Hou	ırs	03
Credits – 03		I		
Course Objectives This course will enable students to 1. Learn various signals, system 2. Know the Fourier analysis of 3. Perform signal conversion by 4. Make use of applying various 5. Extend the transform analysis Unit -1 Introduction to Signals and Sy Singularity functions and related for	continuous-time periodic sign applying sampling theorem. signal and system properties to discrete time sequences stems: Definition of Signal	als and finite ene to LTI systems s and Systems,	rgy sig Hour	'S
signals. Classification of Signals	s, Operations on signals. C	lassification of		
Fourier Series: Fourier series reproduced by Dirichlet's conditions, Trigonome series. Fourier Transform: Fourier transsistandard signals, properties of Fourier	etric Fourier series and Exponent	onential Fourier	1	0
Unit -3 Sampling Theorem: Representation theorem, impulse sampling, Natur signal from its samples, effect of un Review of Laplace Transforms, Pro- between L.T and F.T of a signal.	al and Flat-top Sampling, Render sampling–Aliasing.	econstruction of	1	0
Unit – 4 Analysis of Linear Systems: Line Response of a linear system, Tra convolution and graphical represe auto-correlation of signals, Relation	nsfer function of a LTI systentiation of convolution. Cross	em, Concept of -correlation and	1	0
Unit – 5 Z–Transforms: Concept of Z- T between Laplace, Fourier and Z tra on ROC for various classes of sig transform. Applications of signals and Syste	ansforms. Region of converge gnals, Properties of Z-transfo ms: Modulation for communi	ence, constraints rms, Inverse Z-	1	0
of signals and Feedback control sys	5171115.			
		Total	4	8

Course outcomes

On completion of the course student will be able to

- 1. Understand various signals and systems and demonstrate their properties.
- 2. Develop Fourier analysis of continuous-time periodic signals and continuous-time finite energy signals.
- 3. Apply sampling theorem for signal conversion from continuous- time signals to discrete-time.
- 4. Illustrate various operations on LTI systems.
- 5. Apply Z-transform to analyze discrete-time signals.

Text Books

- 1. A.V. Oppenheim, A.S. Willsky and S.H. Nawab, "Signals and Systems", 2nd Edition, PHI, 2009.
- 2. A Anand Kumar, "Signals and Systems", PHI Publications.

- 1. B.P. Lathi, "Signal Processing & Linear Systems", 1st Edition, Oxford University Press, 2006
- 2. Simon Haykin and Van Veen, "Signals & Systems", 2nd Edition, John Wiley India, 2011.

Digital Logic Design (Open Elective)			
Subject Code	21XXETOX0XB	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
			Credits -03

Course Objectives:

This course will enable students to

- 1. Introduce the concepts and techniques associated with the number systems and Boolean algebra.
- 2. Design various combinational circuits and sequential circuits.
- 3. Know the different memories and PLD's.

Unit -1	Hours
Number Systems And Boolean Algebra: Number representation of different radix, conversion	
of bases, r-l's complements and r's complements of signed and unsigned numbers, weighted and	10
non-weighted codes; Boolean theorems, principle of complementation & duality, De-morgans	
theorems, Basic logic operations and gates, Standard SOP and POS Forms, Minimization of logic	
functions using Boolean theorems and K-Map.	
Unit -2	
Combinational Circuit Design: Design with basic logic gates, Design of Half adder, full adder	
4 bit parallel adder, BCD Adder, Carry look ahead adder circuit, adder- subtractor circuit,	
Comparators, Multiplexer, Demultiplexer, priority encoder, decoders, comparators, realization of	10
Boolean functions using decoders and multiplexers.	
Unit – 3	
Sequential Circuit Design-I: Memory elements and their excitation functions SR, JK, T, and D	
latches and flip-flops, Conversion from one flip-flop to another flip-flop, master slave JK flip-	
flop, edge-triggered flip-flop.	10
Unit – 4	
Sequential Circuit Design-II: Design of synchronous and asynchronous counters, Design of	
registers, finite-state machine, Realization of circuits using various flip-flops, minimization and	
transformation of sequential machines.	8
Unit – 5	
Memories and PLD's – PAL, PLA, PROM, ROM Architecture, Types of ROMS & Applications,	
RAM Architecture, Static & Dynamic RAMs, Introduction to FPGA and CPLD.	10
Total	48
Course outcomes:	
Upon completion of the course, students will be able to	
1. Understand the basic number systems, conversions and Boolean algebra.	
2. Design digital systems using combinational circuits.	
3. Design digital systems using sequential circuits.	
4. Understand the concepts of logic families and corresponding logic levels.	
5. Design digital system using PLDs and Understand the construction and working of memor	ies
TEXT BOOKS:	
1. John F. Wakerly, "Digital Design Principles & Practices", 3 rd Edition PHI/Pearson	Educat
i som i vakery, bigtar besign i merpes & fractees, 5 Editor i in/reason	Luucat

Asia,2005.

2. Morris Mano, Michael D Ciletti , "Digital Design" ,4thEdition, PEA

- 1. W.H.Gothmann, "Digital Electronics- An introduction to theory and practice", 2nd Edition, PHI, 2006.
- 2. Charles H. Roth Jr, "Fundamentals of Logic Design", 5th Edition, Jaico Publishers. 2008
- 3. D.V. Hall, "Digital Circuits and Systems",1st Edition, Tata McGraw Hill,1989.
- 4. Charles Roth, "Digital System Design using VHDL", 2nd Edition Tata McGrawHill, 2012.
- 5. Stephen Brown and ZvonkoVramesic, **"Fundamentals of Digital Logic with VHDL Design"**, 2nd Edition, McGraw Hill, 2005.

PRINCIPLES (OF COMMUNIC (Open Elective)	ATION SYSTEMS	
Set is st Carls		Lutan al Manlar	20
Subject Code Number of Lecture Hours/Week	21XXETOX0XC 03	Internal Marks External Marks	<u>30</u> 70
Total Number of Lecture Hours	48	External Marks Exam Hours	03
Total Number of Lecture Hours	48		<u>05</u> lits – 03
Course Objectives:			1105 05
This course will enable students to			
1. Analyze the performance of a		als.	
2. Characterize analog signals in			
3. Characterize the influence of		-	
4. Determine the performance of			
5. Understand the concepts of n	oise and signal.	-	
Unit -1			Hours
Amplitude modulation: Introduc	tion, Amplitude Mod	ulation: Time & Frequency –	
Domain description, switching mo	· · ·		
Double side band-suppressed ca			
description, Ring modulator, Cohe	rent detection, Costas	Receiver, Quadrature Carrier	10
Multiplexing.			
Single side and vestigial side ban			
Modulation, Frequency Translat			
Example: VSB Transmission of A	nalog and Digital Tel	evision	
Unit -2	iona Enamera Ma	dulation. Nomer Dand EM	
Angle modulation : Basic definit Wide Band FM, Transmission bar			10
		is, Generation of FM Signals,	
Demodulation of FM Signals, FM Unit -3	Stereo Multiplexing,		
Random variables & process: 1	ntroduction Probabi	lity Conditional Probability	
Random variables, Several Rando		•	
random variable, Moments, Rand		0	10
function: Properties of autocorrela			
$\frac{1}{1}$			
Noise in analog modulation: In	ntroduction. Receiver	r Model, Noise in DSB-SC	
receivers and AM receivers, Thres			10
FM threshold effect, FM threshold			
Unit – 5	· · · ·	•	1
Digital Modulation: Introduction	n, Why Digitize Ana	alog Sources? The Sampling	
process, Pulse Amplitude Modula		0 1 0	8
Modulation, Generation of PPM W			
Process, Quantization Noise, Pulse	Code Modulation.		
		Total	48
Course outcomes:			
On completion of the course stude	nt will be able to		
1. Analyze the performance of an	alog modulation sche	emes in time and frequency do	mains
2. Analyze the performance of an	-	1 1	
3. Characterize analog signals in	0 0		
		min 10100	

- 4. Characterize the influence of channel on analog modulated signals
- 5. Determine the performance of analog communication systems in terms of SNR

1. H Taub& D. Schilling, Gautam Sahe, Principles of Communication Systems –TMH, 2007, 3rd Edition.

- 2. B.P. Lathi, Communication Systems-BSPublication, 20062.
- 3. Simon Haykin, Principles of Communication Systems –John Wiley, 2 nd Edition

- 1. George Kennedy and Bernard Davis, Electronics & Communication System -TMH 2004.
- 2. R.P. Singh, SP Sapre, Communication Systems-Second Edition TMH,2007

	ectronic Measurement	S	
Subject Code	Open Elective-II) 21XXETO605A	Internal Mark	s 30
Number of Lecture Hours/Week	03	External Mark	
Total Number of Lecture Hours	48	Exam Hours	03
		Credits -	
 Course Objectives: This course will enable the students to Understand the performance of Understand working of various Analyze the functioning of vari Familiarize with different signa Design AC bridges which can measurement and Error: Performation characteristics: Accuracy, Resolution Sensitivity. Errors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement, Design AC bridges when the terror terrors in Measurement terrors i	characteristics. 5 meters in Electronic Meas ous types of oscilloscopes. 11 generators & wave analyz neasure Inductance, Capaci nce characteristics of inst on, Precision, Expected	zers. tance, and Resistar ruments, Static value, Error,	nce. Hours 10
Fidelity, Lag and Dynamic error. Unit -2 Voltmeters, Ammeters: DC Voltmete True RMS responding voltmeter. Amm Unit – 3 Oscilloscopes CRT features, vertical sweep, trigger pulse, delay line, sync se	neter, Ohmmeters, series ty amplifiers, horizontal def	pe, shunt type.	9
oscilloscope, sampling oscilloscope, method of frequency measurement. Unit – 4 Signal Generator- Fixed and variable square wave signal generators, Functio sweep, Wave Analyzers, Harmonic Di	, AF oscillators, Standard a n Generators, Square pulse,	nd AF sine and Random noise,	10
Unit – 5			
AC Bridges Measurement of induc Anderson bridge, Owen's bridge. Mea De Sauty bridge. Wheat stone bridge. circuits, Precautions and techniques us	surement of capacitance -S Wien Bridge, Sources of	chering Bridge, errors in bridge	9
		Total	48
 Course outcomes: On completion of the course student w 1. Interpret the performance ch Electronic Measurements. 2. Understand the functional cha 3. Discriminate a signal / wavefor 4. Understand signal generator's 5. Construct AC bridges which can 	aracteristics and principle or racteristics of voltmeter an m with various types of os	d ammeter	ı

- 1. Electronic instrumentation, second edition H.S.Kalsi, Tata McGraw Hill, 2004.
- 2. A. K. Sawhney, Electronics and Electrical Measurements, Dhanpat Rai & Sons. ISBN -81-7700-016-0

- Modern Electronic Instrumentation and Measurement Techniques A.D. Helfrick and W.D. Cooper, PHI, 5th Edition, 2002.
- 2. Electronic Test Instruments, Analog and Digital Measurements Robert A.Witte, Pearson Education, 2nd Ed., 2004.
- 3. Electronic Instrumentation & Measurements David A. Bell, PHI, 2nd Edition, 2003.

BIO	MEDICAL ELECRONICS		
	(Open Elective-II)		
Subject Code	21XXETO605B	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70

Total Number of Lecture Hours	48	Exam Hours 03
		Credits – 03
Course Objectives:		
This course will enable students to	C	
1. Understand the concepts of b	io-physiology and bio-potential.	
2. Know the various bio-chemica	l and electrical measurements.	
	es like Ultrasonic Imaging System	
4. Identify with Physical and Med		
5. Understand the different Mode	ern medical instruments.	
Unit -1		Hours
•	tential Recording: Sources of l	
	ential electrodes, biological ampli	fiers, ECG, 10
EEG, EMG,PCG, typical wavefor	this and signal characteristics.	
Unit -2 Biochamical and Nan Electrical	Dependen Moscupomente pU	
	Parameter Measurement: pH, D Cardiac output, respiratory, bloc	
temperature and pulse measureme	1 I V	in pressure, 10
Unit -3	int, blood een counters.	
	kers, DC Defibrillator, Dialyser,	Ventilators.
•	stems, and Ultrasonic Imaging Syst	
Unit – 4		
Physical Medicine and Biotelem	netry: Diathermies- Shortwave, ul	trasonic and
•	ions, Surgical Diathermy, Biotelen	
Unit – 5		
Recent Trends in Medical Inst	trumentation: Telemedicine, Inst	ulin Pumps, 00
Radio pill, Endo microscopy, Brai	in machine interface, Lab on a chip	09 0.
	Total	48
Course outcomes:		
On completion of the course stude		
1. Understand the bio-physiol	6 /	
2. Understand the bio-chemica	al and electrical measurements.	
3. Understand the ASSIST devi	ces.	
4. Understand Physical and Mo	edical Biotelemetry.	
TEXT BOOKS:	• • •	
1. Leslie Cromwell — Biomedic	al Instrumentation and Measurem	ent∥, Prentice Hall of
India, New Delhi, 2007.		
	d Instrumentation by G.s. Sawhn	ey, IK International 1ST
edition, 30 nov 2011.		
· · · · · · · · · · · · · · · · · · ·		
REFERENCES:		
1. Khandpur, R.S., —Handbo	ook of Biomedical Instrumentation	, TATA Mc Graw -Hill,
New Delhi,2003.		
2. John G.Webster, —Medica	I Instrumentation Application and	d Design , 3rd Edition.
		0 11/2

3. Joseph J.Carr and John M.Brown, —Introduction to Biomedical Equipment Technology JohnWiley and Sons, New York, 2004

En	nbedded C Programming (Open Elective –II)		
Subject Code	21XXETO605C	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
Credits – 03			

Course Objectives:	
This course will enable students	
1. To gain knowledge about embedded system and c-programming.	
2. To understand about the basics of embedded c.	
To demonstrate about various data types in embedded c.	
To understand about various operations in embedded c.	
5. To gain knowledge about microcontroller program in embedded c.	
Unit -1	Hours
Embedded system and c-programming:	
Introduction to embedded system, Programming in embedded system,	8
Fundamentals of C Programming Language, Input and Output methods in C	
programming, C Programming Structure.	
Unit -2	
Basics Of Embedded C: Difference between C and Embedded C, Variables in	10
Embedded C, Control Structure in Embedded C, Function in Embedded C,	10
Constants in Embedded C, Conditional Statements.	
Unit -3	
Storage classes and data types:	
Arrays, Pointers and String Basics in embedded C, Arrays, Pointers and String	10
Basics, Variables, Types, and Debugging. Basic learning of number systems,	10
Binary, decimal, hexadecimal, octal number systems.	
Unit – 4	
Operations in embedded c:	
Data Types in Embedded C: char, unsigned char, int, unsigned int, signed int,	10
Arithmetic operations in Embedded C.	10
Bit-wise operations : & 1 ^ << >> programming for the symbols, Bit masking in Embedded C, Bit monitoring in Embedded C.	
Unit – 5	
Microcontroller programming:	
Program Flow Control, Advanced Types, Constants, and Expressions, keil and	
proteus software installation, Concept of Microcontroller, How it is different from	
Microprocessor, How to write program in embedded C, Steps to burn/embed a	10
program in the microcontroller.	
Total	48
Course outcomes:	
On completion of the course student will be able to	
1. Gain knowledge in embedded system and c-programming.	
2. Understand the basics of embedded c.	
Demonstrate about data types in embedded c.	
4. Understand about operations in embedded c	
5. Gain knowledge of programming in embedded c.	
Text books:	
1. Embedded C programming Techniques and applications of C and PIC MCUS	
2. Embedded C programming and the atmal AVR by Barnette / cox / O'cull.	

References:

- 1. Programming Embedded Systems in C and C++ (Michael Barr)
- 2. The Engineering of Reliable Embedded Systems (Michael J. Pont)

	UCTION TO PHOTONIC	CS	
	Open Elective-III)		
Subject Code	21XXETO704A	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
		Credits	- 03
Course Objectives:			
This course will enable students to			
1 II. denotes dates beste meta-tales	- f 1 ('		

- 1. Understand the basic principles of photonics.
- 2. Illustrate about the geometrical optics in photonics.
- 3. Understand the principles of wave optics.
- 4. Illustrate about the statistical properties of light.
- 5. Describe the semiconductor junction characteristics.

Unit -1	Hours
Introduction To Photonics: Introduction, Science of light – evolution, ray/wave/statistical/quantum optics. Applications in Our Daily Lives. Introduction to Light Sources.	10
Unit -2	
Geometrical Optics: Light as a Ray. Blew of Reflection including Plane Mirrors. Law of Refraction including Optical Fiber Applications. Prisms and Thin Lenses, Wave phenomena – Interference, Diffraction.	10
Unit -3	
Principles Of Wave Optics: Interference and Interference Applications, Diffraction and Diffraction Gratings. Polarization Principles, light – evolution, ray/wave/statistical/quantum optics, Wave phenomena – Interference, Diffraction.	10
Unit – 4	
Statistical Properties of Light: Coherence, theory of photons, Photon properties - energy, flux, statistics, Interaction of photons with atoms, Light amplification, Laser fundamentals.	09
Unit – 5	
SemiconductorJunctionCharacteristics:Detectors-P-njunctions.Semiconductor light sources, Semiconductor light detectors, Interactionof light with RF and acoustic waves, Nonlinear optics.Applicationsofphotonics:FiberOpticCommunicationsandNon-Communication Fundamentals and Applications.	09
Total	48
 Course outcomes: On completion of the course student will be able to Understand the basic principles of photonics. Discuss about the geometrical optics in photonics. To Understand the principles of wave optics. To illustrate about the statistical properties of light. To Describe the semiconductor junction characteristics. 	
Text books:	
1. Fundamentals of Photonics, Saleh & Teich.	
References:	
 Robert O. Naess (2001). Physics curriculum: Optics for Technology Student Saddle River, NJ: Prentice-Hall 	s. Upper

IC APPLICATIONS			
	(Open Elective-III)		
Subject Code	21XXETO704B	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
		Credit	ts – 03
Course Objectives:			
This course will enable students to			
1 To Understand the basic	concepts and applications of ana	llog ICs.	
2 To Illustrate linear and n	on-linear applications of analog I	Cs.	
3 To Analyze IC 555 timers	, VCO and PLL.		
4 To Analyze combination	al logic circuits.		
5 To Analyze sequential lo	gic circuits.		
Unit -1		Н	lours

Introduction To Analog ICs: Basic concept of analog ICs, Power management of analog IC Applications, application of analog IC as Operational amplifier(IC 741). Characteristics, functioning, specifications and parameters of IC 741. Unit -2	08
Linear Applications: Summing, scaling and averaging amplifiers, V-I and I-V converters, Differentiators and Integrators. Non-Linear Applications: Comparators, Multi vibrators, Square wave generators.	10
Unit -3	
Specialized ICs: 555 Timer- Block Schematic, pin Description & Applications, Introduction to VCO and PLL. Voltage Regulators: Introduction, IC voltage regulators, 723 general purpose regulators.	12
Unit – 4 Digital ICs-1: Introduction to combinational logic circuits, Decoder (74x138), Priority Encoder (74x148), Multiplexer (74150 and 74x151), comparator (7485). Unit – 5	10
Digital ICs-2: Introduction to Sequential logic circuits, D flip-flop (IC7474)JK Flip-flop(IC7476), shift register using IC7474, Universal shift Register(IC74X194), synchronous counters using flip-flops, Decade counter using IC 7476.	10
Total	50
 Course outcomes: On completion of the course student will be able to 6. Understand the analog ICs and its applications 7. Illustrate the linear and non-linear applications of ICs. 8. Demonstrate the IC special applications IC 555 timer, PLL and VCO. 9. Design combinational logic circuits. 	
 Text books: 1. Ramakanth A. Gayakwad, Op-Amps & Linear ICs , 4th Edition , Pearson, 2. Wakerly J.F. Digital Design: Principles and Practices, 4th Edition, Pearson 2008. 	
REFERENCES:	
 D. Roy Choudhury, Linear Integrated Circuits, 2nd Edition, New Age Inte Private Limited, 2003. 	ernational
 R. P. Jain, Modern Digital Electronics, McGraw Hill Education (India Private 4th edition, 2012. 	Limited),
 Sergio Franco, Design with Operational Amplifiers & Analog Integrated Cir edition, McGraw Hill, 1988. 	cuits, 3rd
4. Gray and Meyer, Analysis and Design of Analog Integrated Circuir International, 2005.	ts, Wiley

	NANO ELECTRON	ICS	
	(Open Elective-III	[)	
Subject Code	21XXETO704C	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
			Credits – 03
 Course Objectives: The objective of device physics and materials technol. This course will enable students to Understand the fundamental Acquire knowledge about na Analyze the Nano Electronic at the Induction: Recent past, the press basic Nano electronics, Nano chara 	ogy to enable the Nano el ls and overview of nano e no electronics and nano o architectures. Principle and working of ent and its challenges, F	ectronics. electronics. computer architectur Spintronics. uture, Overview of	
Basic CMOS Process flow. Unit -2 Nano electronics & Nano comp	uter architectures: Int	roduction to Nano	
computers, Nano computer Archit (QCA), QCA circuits, Single electro – Interface engineering – Proper Limitations.	ecture, Quantum DOT n circuits, molecular circu	cellular Automata uts, Logic switches	10
Unit – 3			
Nano Fabrication:Nano fabrication Metallic/Semiconducting nano struct STM/AFM- SEM & Soft-lithography Inorganic/Organic layers.	ures (e-beam/X-ray, Opti		10
Unit – 4			
Spintronics : Introduction, Overview Polarization Theories of spin Inject Spintronic devices and applications,	ction, spin relaxation ar	nd spin dephasing,	10
Unit – 5			
Memory Devices And Sensors: ferroelectrics – Ferroelectric random ferroelectric thin film properties a	access memory –Fe-RA and integration – calor	M circuit design – imetric -sensors –	9
electrochemical cells – surface and b resistive semiconductor gas sensors -	-		

Course outcomes:

On completion of the course student will be able to

- 1. Understand the fundamentals and overview of nano electronics
- 2. Analyze the I-V characteristics of resonant tunneling diode and Single electron transistor.
- 3. Understand the principle and working of spintronics
- 4. Become familiar with recent research progress related to new devices and materials, and its application in nano electronics field.

Text Books:

- 1. Nano electronics & Nano systems: From Transistor to Molecular & Quantum Devices: Karl Goser, Jan Dienstuhl and others
- 2. Katsuhiko Ogata, Modern Control Engineering, Pearson, 4thEdition, 2012

Reference Books:

- 1. Concepts in Spintronics Sadamichi Maekawa
- 2. Spin Electronics David Awschalom.

Web References:

3. https://youtu.be/wdNFCWLuC10

F	REMOTE SENSING AND GIS OPEN ELECTIVE-IV		
Subject Code	21XXECO705A	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
Pre-requisite		Credits – 03	
Course Objectives:			
This course will enable students to			
1. Understand the concept of photogrammetry and its significance.			

2. Explain the basic concept of remote sensing and limitations.	
3. Understand the vector data model and topology rules.	
4. Explain the raster data model, elements and importance of source map and data e	diting
Unit -1	Hours
Introduction to Photogrammetry: Principles& types of aerial photograph,	Hours
geometry of vertical aerial photograph, Scale & Height measurement on single	
vertical aerial photograph, Height measurement based on relief displacement,	
Fundamentals of stereoscopy, fiducial points, parallax measurement using	09
fiducial line.	
Unit -2	
Remote Sensing: Basic concept of remote sensing, Data and Information,	
Remote sensing data Collection, Remote sensing advantages & Limitations,	
Remote Sensing process. Electromagnetic Spectrum, Energy interactions with	10
atmosphere and with earth surface features (soil, water, vegetation), Indian	10
Satellites and Sensors characteristics, Resolution, Map and Image and False color	
composite, introduction to digital data, elements of visual interpretation	
techniques.	
Unit -3	
Remote Sensing: Basic concept of remote sensing, Data and Information,	
Remote sensing data Collection, Remote sensing advantages & Limitations,	
Remote Sensing process.	10
Electromagnetic Spectrum, Energy interactions with atmosphere and with earth	10
surface features (soil, water, vegetation), Indian Satellites and Sensors	
characteristics, Resolution, Map and Image and False color composite,	
introduction to digital data, elements of visual interpretation techniques. Unit – 4	
Vector Data Model: Representation of simple features- Topology and its	
importance; coverage and its data structure, Shape file; Data models for	
composite features Object Based Vector Data Model; Classes and their	10
Relationship; The geobase data model; Geometric representation of Spatial	
Feature and data structure, Topology rules	
Unit – 5	
Raster Data Model: Elements of the Raster data model, Types of Raster Data,	
Raster Data Structure, Data Conversion, Integration of Raster and Vector data.	
Data Input: Metadata, Conversion of Existing data, creating new data; Remote	09
Sensing data, Field data, Text data, Digitizing, Scanning, on screen digitizing,	
importance of source map, Data Editing	
Course Outcomes:	
The student will be able to	
1. Retrieve the information content of remotely sensed data	
2. Analyze the energy interactions in the atmosphere and earth surface features	
3. Interpret the images for preparation of thematic maps	
4. Apply problem specific remote sensing data for engineering applications	
5. Analyze spatial and attribute data for solving spatial problems	
6. Create GIS and cartographic outputs for presentation	

- 1. Remote Sensing and GIS Lillesand and Kiefer, John Willey 2008.
- 2. Remote Sensing and GIS B. Bhatta by Oxford Publishers 2015.
- 3. Introduction to Geographic Information System Kang-Tsung Chang, McGraw-Hill 2015

Reference Books:

1. Concepts & Techniques of GIS by C. P. Lo Albert, K.W. Yonng, Prentice Hall (India) Publications.

2. Principals of Geo physical Information Systems – Peter A Burragh and Rachael A. Mc Donnell, Oxford Publishers 2004.

3. Basics of Remote sensing & GIS by S. Kumar, Laxmi Publications

	TE COMMUNICATIONS PEN ELECTIVE-IV		
Subject Code	21XXECO705B	Internal Marks	30
Number of Lecture Hours/Week	03	External Marks	70
Total Number of Lecture Hours	50	Exam Hours	03
Credits – 03			
Course Objectives:			
This course will enable students to			

- 1. To understand the basic concept of Satellite communications and its applications.
- 2. To illustrate the concept of subsystem Satellite communication.
- 3. To Design the satellite link.
- 4. To demonstrate the various types of multiple access techniques and architecture of earth Station design.
- 5. To demonstrate the concepts of GPS and its architecture and satellite navigation.

Unit -1	Hours
INTRODUCTION Origin of Satellite Communications, Historical Back-ground, Basic	
Concepts of Satellite Communications, Frequency allocations for Satellite Services,	12
Applications, Future Trends of Satellite Communications.	1
Unit -2	
SATELLITE SUB SYSTEMS: Attitude and orbit control system, telemetry, tracking,	00
Command and monitoring, power systems, communication subsystems, Satellite antenna	08
Equipment reliability and Space qualification.	1
Unit -3	
SATELLITE LINK DESIGN: Basic transmission theory, system noise temperature and	00
G/T ratio, Basics of satellite links- down links, up link. Link Design example.	08
Unit – 4	
MULTIPLE ACCESS: Frequency division multiple access (FDMA), Time division	
Multiple Access (TDMA) Frame, DAMA, Code Division Multiple access (CDMA).	12
EARTH STATION TECHNOLOGY: Introduction, Transmitters, Receivers, Antennas,	1
Tracking system.	1
Unit – 5	
GEO-STATIONARY SATELLITE SYSTEMS: Orbit consideration, coverage and	
frequency considerations, Delay & Throughput considerations, System considerations,	1
Operational NGSO constellation Designs.	10
SATELLITE NAVIGATION: Radio and Satellite Navigation, Introduction to Global	1
positioning system (GPS). GPS Receivers and codes.	
Total	
	50

Course outcomes:

On completion of the course student will be able to

- 1. Understand the satellite communications and its applications and future trends in satellite Communications.
- 2. Illustrate the satellite subsystems.
- 3. Demonstrate the satellite uplink and satellite down-link.
- 4. To Demonstrate the types of multiple access techniques.

TEXT BOOKS:

- 1. Satellite Communications Timothy Pratt, Charles Bostian and Jeremy Allnutt, WSE, Wiley Publications, 2ndEdition, 2003.
- Satellite Communications Engineering Wilbur L. Pritchard, Robert A Nelson and Henri G.

REFERENCES:

- 1. Satellite Communications: Design Principles M. Richharia, BS Publications, 2nd Edition, 2003.
- 2. Satellite Communication D.C Agarwal, Khanna Publications, 5th Ed.
- 3. Fundamentals of Satellite Communications K.N. Raja Rao, PHI, 2004.
- 4. Satellite Communications Dennis Roddy, McGraw Hill, 2nd Edition, 1996.

en Elective-IV) 21XXECO705C	Internal Marks	
21XXECO705C	Internal Marka	
	internal Marks	30
03	External Marks	70
50	Exam Hours	03
Credits – 03		
:		
hage processing and differen	t image transforms.	
	50 Credits – 03 : nage processing and differen	50 Exam Hours Credits – 03

- Understand process of smoothing and sharpening images using frequency domain filters.
- Learn various image processing techniques like image enhancement, restoration, segmentation and compression.

Hours

• Understand pattern recognition.

Unit -1

INTRODUCTION: Introduction to Image Processing, Fundamental steps in digital image processing, components of an image processing system, image sensing and

acquisition, image sampling and quantization, some basic relationships between pixels, an introduction to the mathematical tools used in digital image processing. INTENSITY TRANSFORMATIONS AND SPATIAL FILTERING: Background,	08
Some basic intensity transformation functions, histogram processing, fundamentals of spatial filtering, smoothing spatial filters, sharpening spatial filters, Combining spatial enhancement methods.	
Unit -2	
FILTERING IN THE FREQUENCY DOMAIN: Preliminary concepts, The Basics of filtering in the frequency domain, image smoothing using frequency domain filters, Image Sharpening using frequency domain filters, Selective filtering.	10
Unit -3	
IMAGE RESTORATION AND RECONSTRUCTION: A model of the image degradation / Restoration process, Noise models, restoration in the presence of noise only-Spatial Filtering, Periodic Noise Reduction by frequency domain filtering, Linear, Position –Invariant Degradations, Estimating the degradation function, Inverse filtering, Minimum mean square error (Wiener) filtering.	11
Unit – 4	
 IMAGE COMPRESSION: Fundamentals, Basic compression methods: Huffman coding, Golomb coding, Arithmetic coding. IMAGE SEGMENTATION: Fundamentals, point, line, edge detection, thresholding, region –based segmentation. 	9
Unit – 5	
INTRODUCTION TO PATTERN RECOGNITION: Problems, applications, design cycle, learning and adaptation, examples, Probability Distributions, Parametric Learning - Maximum likelihood and Bayesian Decision Theory- Bays rule, discriminate functions, loss functions and Bayesian error analysis	10

Total	48
Course outcomes:	
On completion of the course student will be able to	
11. Perform image manipulations and different digital image processing techr	niques.
12. Able to understand the concepts of filtering in the frequency domain.	
13. Understand the concepts of image restoration and reconstruction.	
14. Perform basic operations like – segmentation, compression, techniques of	n image.
15. Understand the concepts of pattern recognition	
TEXT BOOKS:	
1. R. C. Gonzalez and R. E. Woods, Digital Image Processing, 3rd edition,	Prentice Hall,
200	
2. Jayaraman, S. Esakkirajan, and T. Veerakumar," Digital Image Proc	essing", Tata
McGraw-Hill Education, 2011.	
3. Richard O. Duda, Peter E. Hart, David G. Stork, "Pattern Classification"	", 2nd Edition
John Wiley & Sons,2001.	
477	

REFERENCES:

1. Anil K.Jain, "Fundamentals of Digital Image Processing", Prentice Hall of India, 9th Edition, Indian Reprint, 2002.

2. B.Chanda, D.Dutta Majumder, "Digital Image Processing and Analysis", PHI, 2009.

3. Bishop, "Pattern Recognition and Machine Learning", Springer, 2006.

SUBJECTS FOR HONORS

POOL-1

Instrumentation and Control Systems: (any four of the following subjects which are not chosen as professional electives are to be considered for Honors Degree)

S. No.	Subject Code	Subject	L-T-P	Credits		
1	21ETETHXXX	Data Acquisition systems	3-1-0	4		
2	21ETETHXXX	Adaptive Control Systems	3-1-0	4		
3	21ETETHXXX	Bio-Medical	3-1-0	4		
		Instrumentation				
4	21ETETHXXX	Digital Control Systems	3-1-0	4		
5	21ETETHXXX	Process Control	3-1-0	4		
		Instrumentation				
6	21ETETHXXX	Transducers & sensors	3-1-0	4		
7	21ETETHXXX	MEMS	3-1-0	4		
8	21ETETHXXX	Intelligent & Smart	3-1-0	4		
	Instrumentation					
In addi	In addition to any of the four subjects, MOOCs/NPTEL Courses for 04 credits (02 courses@					
2	credits each) are com	oulsory in the domain of Elect	ronics and Com	munication		
		Engineering				

POOL-2

Integrated circuits and Systems: (any four of the following subjects which are not chosen as professional electives are to be considered for Honors Degree)

S. No.	Subject Code	Subject	L-T-P	Credits	
1	21ETETHXXX	VLSI Technology and	3-1-0	4	
		Design			
2	21ETETHXXX	CMOS Analog IC Design	3-1-0	4	
3	21ETETHXXX	CMOS Digital IC design	3-1-0	4	
4	21ETETHXXX	Design for Testability	3-1-0	4	
5	21ETETHXXX	System on Chip	3-1-0	4	
6	21ETETHXXX	Programmable Logic	3-1-0	4	
		Devices and ASIC			
7	21ETETHXXX	Scripting Language	3-1-0	4	
8	21ETETHXXX	Low Power VLSI Design	3-1-0	4	
In addit	In addition to any of the four subjects, MOOCs/NPTEL Courses for 04 credits (02 courses@				
2	credits each) are comp	oulsory in the domain of Elect	ronics and Com	munication	
		Engineering			

POOL-3

Communication Engineering: (any four of the following subjects which are not chosen as a professional electives are to be considered for Honors Degree)

S. No.	Subject Code	Subject	L-T-P	Credits	
1	21ETETHXXX	Wireless Sensor Networks	3-1-0	4	
2	21ETETHXXX	Software defined radio	3-1-0	4	
3	21ETETHXXX	Data Communications &	3-1-0	4	
		Computer Networks			
4	21ETETHXXX	Cognitive radio	3-1-0	4	
5	21ETETHXXX	5G Communications	3-1-0	4	
6	21ETETHXXX	Satellite communication	3-1-0	4	
7	21ETETHXXX	Optical Communication	3-1-0	4	
8	21ETETHXXX	Global navigational satellite	3-1-0	4	
		systems			
In addi	In addition to any of the four subjects, MOOCs/NPTEL Courses for 04 credits (02 courses@				
2	credits each) are com	pulsory in the domain of Electi	onics and Com	munication	
		Engineering			

POOL-4

Signal processing (any four of the following subjects which are not chosen as professional electives are to be considered for Honors Degree)

S. No.	Subject Code	Subject	L-T-P	Credits
1	21ETETHXXX	Speech Signal Processing	3-1-0	4
2	21ETETHXXX	Video Signal Processing	3-1-0	4
3	21ETETHXXX	Adaptive Signal Processing	3-1-0	4
4	21ETETHXXX	Bio- Medical Signal	3-1-0	4
		Processing		
5	21ETETHXXX	DSP Processors and	3-1-0	4
		Architectures		
6	21ETETHXXX	Wavelet Theory	3-1-0	4
7	21ETETHXXX	Multirate Systems And	3-1-0	4
		Filter Banks		
8	21ETETHXXX	Mathematical methods	3-1-0	4
		for signal processing		
In addit	tion to any of the four	subjects, MOOCs/NPTEL Cour	rses for 04 cred	its (02 courses@
2	credits each) are com	oulsory in the domain of Electi	ronics and Com	munication

Engineering

GENERAL MINOR TRACKS

S. No.	Subject Code	Subject	L-T-P	Credits
1	21ETETHXXX	Cellular and Mobile	3-1-0	4
		Communication		
2	21ETETHXXX	Switching Theory and Logic	3-1-0	4
		Design		
3	21ETETHXXX	Digital Data	3-1-0	4
		Communications		
4	21ETETHXXX	Signals and systems	3-1-0	4
5	21ETETHXXX	Electromagnetic Waves and	3-1-0	4
		Radiating Systems		
6	21ETETHXXX	Antenna Theory	3-1-0	4
7	21ETETHXXX	Linear IC Applications	3-1-0	4
8	21ETETHXXX	Digital Signal processing	3-1-0	4
9	21ETETHXXX	Analog Communication	3-1-0	4
10	21ETETHXXX	Microwave and Radar	3-1-0	4
		engineering		
In addi	tion to any of the fou	r subjects, MOOCs/NPTEL Cour	ses for 04 credi	its (02 courses@
2	credits each) are com	pulsory in the domain of Electr	onics and Com	munication
		Engineering		

Dat	ta Acquisition Systems POOL-1		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	LDIC	Credits -	- 04
 Course Objectives: Student will be ab Understand the concept of DAS Explain ADCs, DACs and Data Understand monolithic data content 	S and characteristics a Converters	analysis	
Unit -1			Hours
INTRODUCTION : Objective of a DA Components used in DAS– Convert settling time, Monotonicity.			12
Unit -2			
ANALOG TO DIGITAL CONVE Converters, Parallel feedback – Succe Dual slope integration – Voltage to free of ADCS. NON-LINEAR DATA CONVERT Some common NDACS and NADCS using optimal sized ROM – High sp Switched capacitor NDCS. ADC AP Digital signal processing systems – PC measurement instruments – Electronic Unit – 3	essive approximation – R Juency – Voltage to Time – ERS (NDC): Basic NDC – Programmable non-line beed hybrid NADC – PL PLICATIONS: Data Acc CM voice communication weighing machines.	amp comparison – - Logarithmic types C configurations – ar ADCS – NADC LS based NADC – puisition systems – systems – Test and	13
 DIGITAL TO ANALOG CONVER Parallel R– 2R, Weighted resistor, inveordinary binary. DATA CONVERTER APPLICA programmable V/I sources – Arthroprogrammable gain amplifiers – Analog Unit – 4 	rted ladder, D/A decoding TIONS: DAC applica bitrary waveform gener	- Codes other than tions – Digitally ators – Digitally	13
Monolithic data converters : typica Interfacing of DACS and ADCS to a µ	•	DACS and ADCS.	13
Unit – 5 Error budget of DACS and ADCS reduction Techniques in DAS, Error b and an ADC			13

Students will be able to:

- 1. Identity a data acquisition system.
- 2. Prescribe a sensor type to measure a specific environmental change
- 3. Determine what type of amplifier is needed for a specific sensor output.
- 4. Familiar with different forms of signal conditioning.
- 5. Familiar with different methods of Analog-to-Digital conversion.
- 6. Identify the type of interface used to get a digital signal into a microprocessor

Text Books:

1. Electronic data converters fundamentals and applications – Dinesh K. Anvekar, B.S. Sonde – Tata McGraw Hil

- 1. Electronic Analog/ Digital conversions Hermann Schmid Tata McGraw Hill.
- 2. E.R. Hanateck, User's Handbook of D/A and A/D converters Wiley
- 3. Electronic instrumentation by HS Kalsi- TMH 2 ndEdition, 2004.
- 4. Data converters by G.B. Clayton

POOL-1 IETETHXXX 3-1-0 64 ontrol Systems g control system of optimal con- iation and prince ic method for s ptive Control, control system rs, Why adaptive rent configuration AS, Mathemative-varying system rent configuration rent co	Extern Exam ms for nonlin trol for solvir cipal of optim solving proble , Essential as m: Feedback ve control?	Credits – ear systems ng problems nality for sol ems spects of adaptive reference ion, and	
64 ontrol Systems g control system of optimal con- iation and princ ic method for s optive Control, control system rs, Why adaptive rent configuration AS, Mathemat	Exam ms for nonlin trol for solvir cipal of optim solving proble , Essential as m: Feedback ve control?	Hours Credits – ear systems ng problems hality for sol ems spects of adaptive reference ion, and	03 04 ving Hours 12
g control Systems of optimal con ation and princ ic method for s ptive Control, control system rs, Why adaptive rent configuration AS, Mathemat	ms for nonlin trol for solvir cipal of optim solving proble , Essential as m: Feedback ve control?	Credits – ear systems ng problems nality for sol ems spects of adaptive reference ion, and	ving Hours 12
g control system of optimal com iation and princ ic method for s optive Control, control system rs, Why adaptive rent configuration AS, Mathemat	ms for nonlin trol for solvir cipal of optim solving proble , Essential as m: Feedback ve control?	ear systems al problems ality for sol ems spects of adaptive reference ion, and	ving Hours 12
g control system of optimal com iation and princ ic method for s optive Control, control system rs, Why adaptive rent configuration AS, Mathemat	ms for nonlin trol for solvir cipal of optim solving proble , Essential as m: Feedback ve control?	ng problems hality for sol ems spects of adaptive reference ion, and	ving Hours 12
AS, Mathemat	ical descript	ion, and	13
Adaptive Syst Gradient metho MRAS based of d on stability t bach.	od), MIT rule on Lyapunov	stability	13
ation, Covariant and Indirect S	tion models Recursive F st squares, F nce resulting, Self-tuning re	, model Parameter Recursive variable	13
ler, Pole Place and STR.	ment approad	ch to self	
Z	zation, Covaria t and Indirect	ation, Covariance resulting,	extended Least squares, Recursive vation, Covariance resulting, variable t and Indirect Self-tuning regulators,

Alternatives	to Adaptive Control: Why not Adaptive Control? Robust High gain
feedback co	ntrol, Variable Structure schemes, Practical aspects, application and
Perspectives	s on adaptive control.
Course out	comes:
Student will	be able to:
1. Desi	gn identifiers and adaptive controllers for linear systems
2. Desi	gn Adaptive feedback linear zing control systems for nonlinear systems
3. App	ly the concept of different types of optimal control for solving problems
4. App	ly the concept of calculus of variation and principal of optimality for solving problems
5. App	ly the concept of Linear Quadratic method for solving problems
6. App	ly the concept of adaptive control technique for solving problems
Text Books	:
1. B L	andau, Adaptive Control - The Model Reference Approach, New York; Marcel
Dek	ker, 1979.
2. K. J	Astrom and B. Wittenmark, Adaptive Control, Addison Wesley Publication

Company, 1989. References Books:

- 1. B. Roffel, P. J. Vermeer, P. A. Chin, Simulation and Implementation of self Tuning Controllers, Prentice-Hall, Englewood cliffs, NJ, 1989.
- 2. R. Isermann, K. Lashmann and D. Marko, Adaptive Control Systems, Printice-Hall International (UK) Ltd. 1992.
- 3. K. S. Narendra and A. M. Annaswamy, Stable Adaptive Systems

BIO-MEI	DICAL INSTRUMENT	ATION	
	POOL-1		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Biology	Credits – 04	
 This course will enable the students t Recognize different types of and their application in med Apply principles and conception potentials Apply principles and conception devices for detection of mar diagnostic devices 	f transducers, ongoing pro- ical measurements ots of engineering to quar- ots of sensing and engine	ntify and model measu ering to (i) design diag	arements of gnostic
Unit -1			Hours
Sources of Bioelectric potentials Potentials, Propagation of Action Electrodes: Electrode theory, Bio Pot introduction to bio-medical signals	n Potentials, The Bio	electric Potentials.	12
Unit -2			
The Cardiovascular System: The H Blood Pressure, Characteristics of E Measurements, Electrocardiograph Measurement of Blood Flow and Car of Heart Sounds, Event detection, PC Heart beats, ECG rhythm analysis detection of events and waves, analys Potentials, correlation analysis of contraction. Unit – 3	Blood Flow, Heart Sound by, Measurement of diac output, Plethysmogr QRS & T-Waves in ECG s, the di-crotic notch in sis of exercise ECG, anal	ds, Cardio Vascular Blood Pressure, aphy, Measurement t, the first & second the carotid pulse ysis of event related	13
Patient Care & Monitory and M elements of Intensive Care Monitory Patient Monitoring equipment, Other Instrumentation For Monit the physiology of respiratory system breathing, respiratory theory equipment Unit – 4	y, Diagnosis, Calibration coring Patients, pace main, tests and instrumentation	and reparability of akers, defibrillators, on for mechanics of	13
Bio telemetry and Instrumentation bio telemetry, Physiological para components of bio telemetry system, in patient care – The blood, tests o chemical tests.	meters adaptable to l , implantable units, appli	bio telemetry, the cations of telemetry	13
Unit – 5		<u> </u>	
X-ray and radioisotope instrume equipment: Generation of Ionizing rays, special techniques, instrument	radiation, instrumentatio	on for diagnostic X-	13

radiation therapy - Physiological effects of electrical current, shock Hazards from	
electrical equipment, Methods of accident prevention, Modern Imaging Systems:	
Tomography, Magnetic resonance Imaging System, Ultrasonic Imaging System,	
Medical Thermography	

- 1. Apply principles and concepts of electronics to analyze input and output signals in medical electronics
- 2. Apply principles and concepts of electronics to design filters for de-noising of medical measurements
- 3. Recognize different types of transducers, ongoing progress in improving their design, and their application in medical measurements
- 4. Apply principles and concepts of engineering to quantify and model measurements of bio potentials
- 5. Apply principles and concepts of sensing and engineering to (i) design diagnostic devices for detection of markers in bio fluids, and (ii) be able to evaluate quality of diagnostic devices
- 6. Apply engineering tools to evaluate parameters needed for point-of-care health screening and mobile-health, and design of appropriate point-of-care diagnostic devices

Text Books:

- 1. Biomedical Instrumentation and Measurements C. Cromwell, F.J. Weibell, E.A.Pfeiffer Pearson education.
- 2. Biomedical signal analysis Rangaraj, M. Rangayya Wiley Inter science John Willey & Sons Inc

Reference:

- 1. Hand Book of Bio-Medical Instrumentation R.S. Khandpur, (TMH)
- 2. Introduction to Bio-Medical Engineering Domach, (Pearson)
- 3. Introduction to Bio-Medical Equipment Technology Cart, (Pearson)

Subject Code	TAL CONTROL SYSTE POOL-1	MS	
	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
Pre-requisite	Control systems	Credits – 04	
Course Objectives:			
 This course will enable the students Design a pure, two-pole syst percent overshoot, peak time Calculate the z-plane locatio performance information like Create discrete equivalents the Able to Construct a discrete-output variables at particular function. Unit -1 Sampling and Reconstruction: 	tem that satisfies specified p e, settling time, and DC gain on of a pair of dominant pol- e percent overshoot, settling from given continuous-time -time difference equation co time instances from a system	n. es given time-domai g time, and peak tim e systems ontaining input varia em's discrete-time tr	n e. bles and
 Sampling theorem, Reconstruction of signal. The Z – Transforms: Introduction Z – transforms, Theorems of Z – Tranz Z- Transforms. Z-Plane Analysis of Discrete-Tim solving difference equations; Pulse of sampled – data systems, mapping and Complementary Strips. 	, Linear difference equation nsforms, the inverse Z – traine Control System : Z-Tran transforms function, block	ns, pulse response, nsforms, Modified nsform method for a diagram analysis	12
Unit 2			
Unit -2 State Space Analysis: State Space F Transfer Function Matrix solving transition matrix and its Properties, Matrix, Discretization of continuous	discrete time state space Methods for Computation	e equations, State of State Transition	13
State Space Analysis : State Space F Transfer Function Matrix solving transition matrix and its Properties,	discrete time state space Methods for Computation of stime state – space equation ility : Concepts of Co bility and Observability, ontrollability and Observab sis of closed loop systems in use of the Bilinear Transfor	e equations, State of State Transition ns ntrollability and Duality between ility conditions for n the Z-Plane, Jury	13

State Feedback Controllers and Observers: Design of state feedback controller	
e	
through pole placement - Necessary and sufficient conditions, Ackerman's	
formula, State Observers – Full order and Reduced order observers. Introduction	13
to Kalman filters, State estimation through Kalman filters, introduction to adaptive	
controls.	

- 1. Design a pure, two-pole system that satisfies specified performance specifications like percent overshoot, peak time, settling time, and DC gain.
- 2. Calculate the z-plane location of a pair of dominant poles given time-domain performance information like percent overshoot, settling time, and peak time.
- 3. Create discrete equivalents from given continuous-time systems,
- 4. Able to Construct a discrete-time difference equation containing input variables and output variables at particular time instances from a system's discrete-time transfer function.
- 5. Numerically compute the value of any system variable (e.g., state variable or output variable) at any discrete, time instant given initial conditions and input waveforms.

Text Books:

- 1. K. Ogata "Discrete-Time Control systems" Pearson Education/PHI, 2nd Edition.
- 2. M.Gopal "Digital Control and State Variable Methods"- TMH

REFERENCE BOOKS:

- 1. Kuo "Digital Control Systems"- Oxford University Press, 2nd Edition, 2003.
- 2. M. Gopal "Digital Control Engineering".

PROCESS C	CONTROL INSTRUME POOL-1	NIATION	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Control systems	Credits – 04	
Course Objectives:	2		
 This course will enable the students Understand the different secu Issues for designing Distribut Know the latest communication 	ırity design approaches, E ted control system.		
Unit -1	<u>U</u>	•	Hours
P & ID symbols. Process charac regulation. Control system param Discontinuous controller modes: tw modes. Continuous controller mod representation and description of PI, modes to linear, step and square way	neters: control lag, de vo position, multi position nodes: Mathematical r les. Composite control mo PD, PID control modes. I	ad time, cycling. on, floating control epresentation and odes: Mathematical	13
Unit -2			
Electronic Controller mode impler OP amplifiers.	nentation: Designing of F	P, PI, PD, PID using	12
Unit – 3			
Pneumatic controller mode implem using flapper – nozzle system. Final control: Actuators – Electrica	_	n of P, PI, PD, PID	13
<u>Unit – 4</u>			
Final control: Actuators – Contro percentage control valves, valve sizi			13
Unit – 5			
Programmable controllers & Dig Ladder Diagram, Programmable con simple applications. Digital Contr computer based controller.	ntroller program from the	ladder diagram of	13
Course outcomes:			
 Students will be able to: 1. Understand the popular processor 2. Design and development of constraints 3. Understand the different secure 4. Issues for designing Distribution 5. Know the latest communication Text Books: 1. Process control Instrumentation 2000. 	lifferent PLC programmir rrity design approaches, E ted control system. ion technologies like HAB	ng for simple process Engineering and opera RT and Field bus prot	ocol.
Reference Books:			
1. Principles of Process control	by D. Patranabis- TMH 2 Fata MoGraw – Hill publi		Delhi 1984

TRAN	SDUCERS AND SENS	ORS	
	POOL-1		20
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P Total Number of Lecture Hours	3-1-0	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
Pre-requisite Course Objectives:	Physics, Electronics	Credits	- 04
 This course will enable the students Choose proper sensor comparimeasurements of physical param Predict correctly the expected per Locate different type of sensors Set up testing strategies to evaluand transducers Unit -1 	ing different standards a neters like pressure, flow, a performance of various sense used in real life application	acceleration, etc sors ns and paraphrase the	eir importance
Introduction : functional elements	of an instrument gener	alized performance	nours
characteristics of instruments – sta Zero order, first order, second order and impulse response. Response of a and to transient input Experiment parameters, loading effects under dy	atic characteristics, dynar instruments – step respo general form of instrument tal determination of me	nic characteristics. nse, ramp response its to periodic input	13
Unit -2			
Transducers for motion and displacement, translation and rotatio gauges, LVDT, synchros, capacitance optical devices, nozzle – flapper tr ultrasonic transducers. Magnetic a relative acceleration measurements, vibration pickups. Gyroscopic senso Unit – 3	anal resistive potentiomete e pickups, Piezo-electric transducers, digital displac and photoelectric pulse seismic acceleration pick	ransducers, electro- ement transducers, counting methods,	12
TRANSDUCERS FOR FORCE transducers, Photo-electric transdu measurement dynamometers. TRANSDUCERS FOR FLOW M anemometers, Electro-magnetic fla TRANSDUCERS FOR PRESSUR transducers, liquid systems, gas syste conductivity gauges, ionization gauge	ucers, variable reluctand MEASUREMENT: Hot ow meters, laser Doppl E MEASUREMENT: M ems, very high pressure tra	ce pickup, torque wire and hot-film ler velocity meter Ianometers, elastic	13
Unit – 4			
TRANSDUCERS FOR TEMPI expansion methods, Thermometers Thermocouples, Materials cont thermometers, Thermistor, junction a Optical pyrometers, Dynamic respon Transducers for liquid level measur fiber optic sensors.	(liquid in glass), press figuration and techni semiconductors, Sensors, nse of temperature sensors	sure thermometers, ques. Resistance Radiation methods, s heat flux Sensors,	13
Unit – 5			
Smart sensors : Introduction, primat trends in sensor technology – film set Nano-sensors.	•	-	13

- 1. Use concepts in common methods for converting a physical parameter into an electrical quantity
- 2. Classify and explain with examples of transducers, including those for measurement of temperature, strain, motion, position and light
- 3. Choose proper sensor comparing different standards and guidelines to make sensitive measurements of physical parameters like pressure, flow, acceleration, etc
- 4. Predict correctly the expected performance of various sensors
- 5. Locate different type of sensors used in real life applications and paraphrase their importance
- 6. Set up testing strategies to evaluate performance characteristics of different types of sensors and transducers, develop professional skills in acquiring and applying the knowledge outside the classroom through design of a real-life instrumentation system.

Text Books:

- 1. Doebelin, E.O., "Measurement systems Application and Design", McGraw Hill.
- 2. D. Patranabis, "Sensors and Transducers", PHI, 2nd Edition.

Reference:

- 1. Instrumentation Measurement & Analysis, by B.C. Nakra, K.K. Choudry, (TMH)
- 2. Transducers and Instrumentation, by D.V.S. Murthy (PHI)

MEMS	
POOL-1	

Subject Code	21ETETHXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite		Credits -	
Course Objectives:		Crouits	0.
This course will enable the students	s to:		
• Understand the basic overvi		vstems with broad cate	egory of
MEMS& Micro system app			. <u></u>
 Understanding the working 		s	
 Understand the Scaling Law 			nd
Microsystems			lite
 Understand the Micro system 	m Fabrication Process and	Analyze the differen	t Micro
manufacturing process and		a Analyze the unrefer	
Unit -1	applications.		Hours
Overview of MEMS and Micros	systems. MEMS and Mic	prosystems Typical	110015
MEMS and Micro-system produc	-		
system and Microelectronics, The			
design and manufacture, Micro-s			13
Microsystems in the automotive in	-		15
industries: Health care industry,	• • • •		
Consumer products, Telecommunic			
Unit -2	utons. Markets for Micros	systems	
Working Principles of Microsys	tems. Introduction Micro	o-sensors. Acoustic	
Wave Sensors, Biomedical sensors			
sensors, Thermal sensors. Micro act			
memory alloys, Piezoelectric cryst			12
actuators: Micro-grippers, Micro-			
accelerators, Micro-fluidics.		iero pumpo, miero	
Unit – 3			
Scaling Laws in Miniaturization:	Introduction to scaling, So	caling in Geometry.	
Scaling in Rigid-Body Dynamics,			
Electromagnetic Forces, Scaling	-		
Scaling in Heat Transfer.		,	10
Materials for MEMS and Micros	systems: Introduction, Sul	ostrates and wafers.	13
Active substrate materials, Silicon	•		
Silicon piezo resistors, Gallium		-	
Polymers, Packing materials.			
Unit – 4			
Micro system Fabrication Pro	cess: Photolithography.	Ion Implantation.	
Diffusion, Oxidation, Chemical Va	U 1 ·	1	
Deposition by Epitaxy, Etching			13
Applications: Bulk Micro manufact		Ũ	15
Micromachining- any one examp			
example of application	**	-	
1 11			
Unit – 5	- Internalis (* 1	name tan D' o	
Applications of MEMS-Switching	-		10
switching, Mechanical switches, applications, Mechanical RF switch			13

- 1. Understand the basic overview of MEMS and Microsystems with broad category of MEMS& Micro system applications.
- 2. Understanding the working principles of Microsystems
- 3. Understand the Scaling Laws in Miniaturization and Materials for MEMS and Microsystems
- 4. Understand the Micro system Fabrication Process and Analyze the different Micro manufacturing process and Applications.
- **5.** Study and Analyze the different types of RF switches, Various Switching Mechanism and their applications.

Text Books:

- 1. Tai-Ran Hsu, "MEMS and Microsystems: Design and Manufacture", Tata McGraw Hill, (2002).
- 2. Gabriel M. Rebeiz, "RF MEMS Theory, Design and Technology", Wiley India Pvt Ltd

- 1. Stephen D. Senturia, "Microsystem Design", Springer International Edition, (2010).
- 2. Mohamed Gad-el-Hak, "The MEMS Handbook", CRC Press,(2002).
- 3. Chang Liu, "Foundations of MEMS", Second Edition, Pearson Publication

Intelligent and Smart Instrumentation	
POOL-1	

Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Instrumentation	Credits – 04	
Course Objectives:	·		
This course will enable the students	to:		
• To be study the cor	cepts of intelligent sen	sor devices, their	performance
characteristics and signa	l and system dynamics.		-
• To address the issues in	dealing signal conditionin	ng operations such as	calibration
linearization and competent			
• To use artificial intellige	ence in sensor signal proces	sing to solve real wor	ld problems
• To deal with interfacing	protocols in wireless netwo	orking platform.	
Unit -1			Hours
Introduction: Definition of intell	igent instrumentation, type	es of instruments,	
Static Characteristics: Accuracy	and Precision, Error,	Correction, and	
Uncertainty, Repeatability, Reprodu			
Sensitivity, Offset, and Dead B			12
Characteristics, Error Modeling, D	-	-	
Dynamic Sensitivity, Input-Output	Impedances, Historical Pe	erspective, Current	
status, software based instruments.			
Unit -2			
Intelligent Sensors: Classification			10
Virtual sensors, Self Adaptive Sen	_		13
Temperature Compensating Intellig	ent Sensors, Pressure Senso	r, Indirect Sensing	
$\frac{\text{Unit}-3}{\text{Unit}-3}$	<u> </u>	T : : : :	
Linearization, Calibration, and			
Positive and Negative Coefficient R			
Nonlinear ADC- and Amplifier-B Linearization, Microcontroller-Base	· · ·		13
Based Linearization Nonlinear, A	,		15
Calibration, Conventional Calibrati	-		
Drift Compensation, Lead Wire Co		iisation, Error and	
Unit – 4	inpensation		
Sensors with Artificial Intellig	ence: Artificial Intelligen	ce Sensors with	
Artificial Intelligence, Multidimen	6		13
Instrumentation, ANN-Based Intell			13
Sensors	.gene 2 ensors, 1 e225 20gre	2	
Unit 5		L. L	
Unit – 5 Intelligent Senson Standards on	d Protocolar IEEE 1451	Standard STIM	
Intelligent Sensor Standards an			13
Intelligent Sensor Standards an TEDS, NCAP, Network Technolog	gies, LonTalk, CEBUS, J1	850 Bus, 1 Signal	13
Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n-	gies, LonTalk, CEBUS, J1	850 Bus, 1 Signal	13
Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n- Course outcomes:	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol	850 Bus, 1 Signal	
Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n- Course outcomes: 1. To develop the design method	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol	850 Bus, 1 Signal	
Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n- Course outcomes: 1. To develop the design metho problems.	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol odologies for measurement	850 Bus, 1 Signal and instrumentation	of real work
 Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n-Course outcomes: To develop the design methor problems. To be study the concepts of 	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol odologies for measurement intelligent sensor devices,	850 Bus, 1 Signal and instrumentation	of real world
 Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n-Course outcomes: To develop the design methor problems. To be study the concepts of and signal and system dynamical system	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol odologies for measurement intelligent sensor devices, nics.	850 Bus, 1 Signal and instrumentation their performance cl	of real world
 Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n-Course outcomes: To develop the design methor problems. To be study the concepts of and signal and system dynamical and system dynamical. 	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol odologies for measurement intelligent sensor devices, nics. lealing signal conditioning	850 Bus, 1 Signal and instrumentation their performance cl	of real work
 Intelligent Sensor Standards an TEDS, NCAP, Network Technolog Logic and Format, MI Bus, Plug-n- Course outcomes: To develop the design methors To be study the concepts of and signal and system dynamic 	gies, LonTalk, CEBUS, J1 Play Smart Sensor Protocol odologies for measurement intelligent sensor devices, nics. lealing signal conditioning ion	850 Bus, 1 Signal and instrumentation their performance cl g operations such as	of real worl naracteristic calibratior

5. To deal with interfacing protocols in wireless networking platform.

Text Books:

- 1. Manabendra Bhuyan, —Intelligent Instrumentation: Principles and Applications CRC Press,2011
- 2. G. C. Barney, —Intelligent Instrumentation^{II}, Prentice Hall, 1995.
- 3. J.B DIXIT, A. yadav Laxmi Publications, Ltd., 01-Sep-2011

VLSI TI	ECHNOLOGY AND DI POOL-2	ESIGN	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	DSD	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
• Learn about the MOS fab	rication process and sho	rt channel effects.	
• Learn about the basic rule	-		
 Analyze various combina 		d sequential systems	
Unit -1	uronar rogie neen orns an		Hours
MOS Transistors			liouis
Introduction, The Structure of MO	S Transistors The Fluid	1 Model The MOS	
Capacitor, The MOS Transistor, N		-	
Electrical Characteristics of MOS	1		12
Trans conductance gm, Figure		0	
Modulation, MOS Transistors as a S			
Unit -2	witch, Hunshinssion Gut		
MOS Fabrication Technology			
Introduction, Basic Fabrication Pro-	cesses Wafer Fabricatio	n Oxidation Mask	
Generation, Photolithography, Diffu			
CMOS Fabrication Steps, n-Well P			13
Latch-Up Problem and Its Preventi			10
Short-Channel Effects-Channel Leng			
Lowering, Channel Punch Through,			
$\frac{1}{1} = \frac{1}{1} = \frac{1}$			
Layout Design Rules			
Scaling Theory, Scalable CMOS D	esign Rules, CMOS Pro	cess Enhancements,	13
Transistors, Interconnects, Circuit El			
Unit – 4	· · · · · · · · · · · · · · · · · · ·		
Combinational Logic Networks			
Layouts for logic networks. Delay th	rough networks. Power o	optimization. Switch	13
logic networks. Combinational logic		1	
Unit – 5	~		
Sequential Systems			
Memory cells and Arrays, clock	ing disciplines sequent	tial circuit Design	13
Performance Analysis, Power optimi			15
Course outcomes:	ization, Design vandation	ii aliu testilig.	
1. Understand the basics of MO	S transistors and also the	characteristics of MO	S transistor
2. Learn about the MOS fabrica			5 (14)1515(0)
 Learn about the Wos fabrica Learn about the basic rules in 		lamer criters.	
4. Analyze various combination	•	quantial exetame	
Text Books:	iai iugic iitiwuiks allu se	quentiai systems.	
1. Principals of CMOS VLSI I	Design_N H FWasta V	Eshraphian and Editi	on Addiso
Wesley.		Esinaginan, 2nu Eulu	on, Audiso
2. CMOS Digital Integrated (Circuite Analysis and I	Design - Sung Mo K	ang Vue
2. UNUS DIgital integrated V	circuits milarysis allu I	Design – Sung-MO M	Lang, Tust
Leblebici, TMH, 3rd Ed., 201 3. Low-Power VLSI Circuits an	11.	NGER DURI ISHED	2

4. Modern VLSI Design – Wayne Wolf, 3rd Ed., 1997, Pearson Education.

- 1. Digital Integrated Circuit Design Ken Martin, Oxford University Press, 2011.
- 2. Digital Integrated Circuits A Design Perspective, Jan M. Rabaey, Anantha Chandrakasan, BorivojeNikolic, 2nd Ed., PHI

CIVI	OS ANALOG IC DESIC POOL-2	ÎN	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	48	Exam Hours	03
Pre-requisite	VLSI Design	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
• Analyze analog circuits at least	ast to the first order.		
• Appreciate the trade-offs inv	olved in analog integrated	l circuit design.	
• Understand and appreciate the	ne importance of noise and	l distortion in analog c	ircuits.
Unit -1	•		Hours
Basic MOS Device Physics – Gene	ral Considerations, MOS	I/V Characteristics,	
Second Order effects, MOS Device	models. Short Channel	Effects and Device	12
Models. Single Stage Amplifiers -Ba	asic Concepts, Common Se	ource Stage, Source	14
Follower, Common Gate Stage, Cas	code Stage		
Unit -2			
Differential Amplifiers - Single	Ended and Differential	Operation, Basic	
Differential Pair, Common Mode R	Response, Differential Pai	r with MOS loads,	13
Gilbert Cell. Passive and Active Cur	rent Mirrors-Basic Curren	nt Mirrors, Cascode	13
Current Mirrors, Active Current Min	rors		
Unit – 3		-	
Frequency Response of Amplifier		-	
Stage, Source Followers, Common			13
Noise – Types of Noise, Representat		oise in single stage	10
amplifiers, Noise in Differential Pair	rs.		
Unit – 4			
Feedback Amplifiers – General Co			
Loading. Operational Amplifiers – C			13
Two Stage Op Amps, Gain Boostin			15
limitations, Slew Rate, Power Suppl	y Rejection, Noise in Op A	Amps. Stability and	
Enganon or Commandation			
rrequency Compensation.			
Frequency Compensation. Unit – 5			
Unit – 5 Characterization of Comparator,			
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro	oving the Performance		13
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa	oving the Performance		13
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes:	oving the Performance arators.		13
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal	oving the Performance arators. og integrated circuits.		13
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea	oving the Performance rators. og integrated circuits. ast to the first order.	of Open-Loop	13
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea 3. Appreciate the trade-offs inv	oving the Performance arators. og integrated circuits. ast to the first order. olved in analog integrated	of Open-Loop	
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea 3. Appreciate the trade-offs inv 4. Understand and appreciate the	oving the Performance arators. og integrated circuits. ast to the first order. olved in analog integrated	of Open-Loop	
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea 3. Appreciate the trade-offs inv 4. Understand and appreciate the Text Books:	oving the Performance rators. og integrated circuits. ast to the first order. olved in analog integrated he importance of noise and	of Open-Loop	ircuits.
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea 3. Appreciate the trade-offs inv 4. Understand and appreciate the Text Books: 1. B.Razavi, "Design of Analoged	oving the Performance rators. og integrated circuits. ast to the first order. olved in analog integrated he importance of noise and	of Open-Loop	ircuits.
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro Comparators, Discrete-Time Compa Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea 3. Appreciate the trade-offs inv 4. Understand and appreciate the Text Books: 1. B.Razavi, "Design of Analog Edition2016.	oving the Performance arators. og integrated circuits. ast to the first order. olved in analog integrated he importance of noise and og CMOS Integrated Circ	circuit design. distortion in analog c	ircuits. IcGraw Hil
Unit – 5 Characterization of Comparator, Open-Loop Comparators, Impro- Comparators, Discrete-Time Compa- Course outcomes: 1. Design MOSFET based anal 2. Analyze analog circuits at lea 3. Appreciate the trade-offs inv 4. Understand and appreciate the Text Books: 1. B.Razavi, "Design of Analog	oving the Performance arators. og integrated circuits. ast to the first order. olved in analog integrated he importance of noise and og CMOS Integrated Circ	circuit design. distortion in analog c	ircuits. IcGraw Hil

- 1. T. C. Carusone, D. A. Johns & K. Martin, "Analog Integrated Circuit Design", 2nd Edition, Wiley, 2012.
- 2. P.E.Allen &D.R. Holberg, "CMOS Analog Circuit Design", 3rd Edition, Oxford University Press, 2011.
- 3. R. Jacob Baker, "CMOS Circuit Design, Layout, and Simulation", 3rd Edition, Wiley, 2010.
- 4. Recent literature in Analog IC Design.

СМ	OS DIGITAL IC DESIG POOL-2	SN	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	DSD	Credits – 04	
Course Objectives:			
 This course will enable the students Demonstrate advanced know Alternative CMOS Logics, E Classify different semicon combinational and sequential Analyze complex engineering 	wledge in Static and dy stimation of Delay and Pe ductor memories. 3. A MOS logic circuits.	ower, Adders Design. Analyze, design and	implement
conducting research.			
Unit -1			Hours
MOS Design Pseudo NMOS Logic – Inverter, Inv Output Low voltage, Gain at gate thro Fall time, Pseudo NMOS logic gat logic.	eshold voltage, Transient	response, Rise time,	12
Unit -2			
Combinational MOS Logic Circui MOS logic circuits with NMOS lo NAND gate, Complex Logic circui using NMOS gates and CMOS gat CMOS transmission gates, Designin Unit – 3	ads, Primitive CMOS log its design – Realizing B es, AOI and OAI gates,	oolean expressions CMOS full adder,	13
Sequential MOS Logic Circuits Behaviour of bistable elements, SR CMOS D latch and edge triggered fl		d flip flop circuits,	13
Unit – 4			
Dynamic Logic Circuits Basic principle, Voltage Bootstrap circuits, Dynamic CMOS transmiss CMOS circuits.		1	13
Unit – 5			
Semiconductor Memories Types, RAM array organization, DR DRAM cell and refresh operation, S cells, Flash Memory NOR flash and	RAM operation Leakage		13
 Course outcomes: Demonstrate advanced know Alternative CMOS Logics, E Classify different semicondu Analyze, design and implement Analyze complex engineering conducting research. Solve engineering problems a ICs. 	stimation of Delay and Pe ctor memories. ent combinational and sec g problems critically in th	ower, Adders Design. Juential MOS logic cir le domain of digital IC	cuits. design for

- 1. Digital Integrated Circuit Design Ken Martin, Oxford University Press, 2011.
- 2. CMOS Digital Integrated Circuits Analysis and Design Sung-Mo Kang, Yusuf Leblebici, TMH, 3rd Ed., 2011.

- 1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective Ming-BO Lin, CRC Press, 201.
- 2. Digital Integrated Circuits A Design Perspective, Jan M. Rabaey, Anantha Chandrakasan Borivoje Nikolic, 2nd Ed., PHI.

DES	SIGN OF TESTABILIT POOL-2	Y	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	DSD	Credits -	- 04
 Course Objectives: This course will enable the students Analyse the various test g Identify the design for t circuits. 	generation methods for states estability methods for co	ombinational & seque	
Recognize the BIST tech Unit -1	inques for improving test	aonnty.	Hours
Introduction to Testing Testing Phi VLSI Testing, VLSI Technology 7 Fault Modeling: Defects, Errors and Levels of Fault Models, Single Stuck	Frends affecting Testing, Faults, Functional Versus	Types of Testing,	12
Unit -2			
Logic and Fault Simulation Simulation for Design Verification Simulation, Algorithms for True Simulation		-	13
Unit – 3			
Testability Measures SCOAP Controllability and Obser Digital DFT and Scan Design: Ad-H Design, Variations of Scan.		•	13
Unit – 4			
Built-In Self-Test The Economic Case for BIST, Rand Pattern Generation, Response Compa Per-Clock, TestPer- Scan BIST Syste BIST, Delay	action, Built-In Logic Blo	ck Observers, Test-	13
Unit – 5			
Boundary Scan Standard Motivation, System Configuration w Boundary Scan Test Instructions, Pir Description Language: BDSL Descr	n Constraints of the Stand	ard, Boundary Scan	13
 Course outcomes: Apply the concepts in tes Tackle the problems assolevels so as to significant Analyse the various test g Identify the design for t circuits. Recognize the BIST tech 	ciated with testing of sem ly reduce the testing costs generation methods for sta estability methods for co	iconductor circuits at e s. atic & dynamic CMOS ombinational & seque	earlier design S circuits.

Text Books:

1. Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits - M.L. Bushnell, V. D. Agrawal, Kluwer Academic Publishers.

REFERENCE BOOKS:

- 1. Digital Systems and Testable Design M. Abramovici, M.A.Breuer and A.D Friedman, Jaico Publishing House.
- 2. Digital Circuits Testing and Testability P.K. Lala, Academic Press.

	SYSTEM ON CHIP		
Subject Code	POOL-2 21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	<u> </u>	Exam Hours	03
	Emdded systems	Credits – 04	03
Pre-requisite Course Objectives:	Emaded systems	Cleans – 04	
This course will enable the students	to		
 Identify and formulate a approaches Design SoC based system Realize impact of SoC or 	a given problem in the n for engineering application n electronic design philoso	ions ophy and Macro-elect	-
	ntrepreneurship & skill de	evelopment	TT
Unit -1			Hours
ASIC: Overview of ASIC types,			10
approaches for SOC architectural	1	U	12
methodologies, Application Specific	Instruction Processor (A.	SIP) concepts.	
Unit -2 NISC:			
NISC Control Words methodolog Architecture Description Language Application Specific Instruction s computer (NISC)- design flow, mod Generic Netlist Representation - A for and synthesis of embedded processo	es (ADL) for design a set Processors (ASIP), 1 eling NISC architectures a prmal language for specifi	nd verification of No-Instruction-Set- and systems, use of	13
Unit – 3			
Simulation : Different simulation n gate level, switch level, transistor vectors, Low power FPGA. Reconfigurable systems : SoC rela logic, Minimization of interconnects	r/circuit simulation, desited modeling of data path	gn of verification design and control	13
Unit – 4			
Low power SoC design / Digital s perspectivepower gating, clock gativoltage scaling, Dynamic clock free block optimization, building block consumption verification.	ing, adaptive voltage sca quency and voltage scalin	ling (AVS), Static g (DCFS),building	13
Unit – 5		·	
Synthesis: Role and Concept of gra constructs, Walks, trails paths, conno- nodal and admittance graph. Techno- approaches for synthesis, optimiza Single core and Multi core systems, for minimization of power consumpt	ectivity, components, map logy independent and tec ation constraints, Synthe dark silicon issues, HDL	pping/visualization, hnology dependent sis report analysis coding techniques	13
Course outcomes: 1. Identify and formulate a given 2. Design SoC based system for 3. Realize impact of SoC on el incline towards entrepreneurship Text Books:	engineering applications ectronic design philosop	-	

- 1. Hubert Kaeslin, "Digital Integrated Circuit Design: From VLSI Architectures to CMOS Fabrication", Cambridge University Press, 2008.
- 2. B. Al Hashimi, "System on chip-Next generation electronics", The IET, 2006

- 1. Rochit Rajsuman, "System-on- a-chip: Design and test", Advantest America R & D Center, 2000.
- 2. P Mishra and N Dutt, "Processor Description Languages", Morgan Kaufmann, 2008.
- 3. Michael J. Flynn and Wayne Luk, "Computer System Design: System-on-Chip", Wiley, 2011.

PROGRAMMA	ABLE LOGIC DEVICE	S AND ASIC	
Subject Code	POOL-2 21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
	DSD	Credits – 04	05
Pre-requisite	DSD	Credits – 04	
Course Objectives: This course will enable the students	to:		
Describe architecture of p			
 Explain programmable m 	-		
 Recall IC fabrication tech 	-	witch	
 Relate design and implen 	-	Switch	
 Low power design techni 			
Unit -1	iques and methodologies		Hours
INTRODUCTION TO ASICS, C		RDADV DESICN.	110015
Types of ASICs - Design flow –			
Combinational logic Cell Sequent		Ū.	12
Transistor parasitic capacitance – L	6		12
architecture.	ogical choit - Library ce	II design – Liorary	
Unit -2			
AND PROGRAMMABLE ASIC EPROM and EEPROM technology LCA – Altera FLEX - Altera MAX blocks	- PREP benchmarks - A	Actel ACT - Xilinx	13
Unit – 3 PROGRAMMABLE ASIC INTER DESIGN SOFTWARE AND LOW LCA - Xilinx EPLD - Altera MAX FLEX – Design systems - Logic Sy language - PLA tools EDIF- CFI des	LEVEL DESIGN: Entry: 5000 and 7000 - Altera Monthesis - Half gate ASIC	Actel ACT -Xilinx MAX 9000 - Altera	13
Unit – 4 SILICON ON CHIP DESIGN: Voi	ice over IP SOC - Intellect	anal Property – SOC	
Design challenges- Methodology and for integrationSOC verification-Set to	d design-FPGA to ASIC c		13
Unit – 5			
PHYSICAL AND LOW POWER I tips and guideline for physical design dissipation-low power design techn tools- tips and guideline for low pow	n- modern physical design iques and methodologies	techniques- power	13
Course outcomes:			
1. Recognize need for programm			
2. Describe architecture of progr			
3. Explain programmable metho			
4. Recall IC fabrication techniqu			
5. Relate design and implemen	tation flow for PLDs 6.	low power design tee	chniques an
methodologies			

Text Books:

- M.J.S. Smith, —Application Specific Integrated Circuits, Pearson Education, 2008
 Wayne Wolf, —FPGA-Based System Design, Prentice Hall PTR, 2009.
- 3. Farzad Nekoogar and Faranak Nekoogar, -From ASICs to SOCs: A Practical Approach I, Prentice Hall PTR, 2003.

SC	SCRIPTING LANGUAGE				
Subject Code	POOL-2 21ETETHXXX	Internal Marks	30		
L-T-P	3-1-0	External Marks	70		
Total Number of Lecture Hours	64	Exam Hours	03		
Pre-requisite	07	Credits -			
Course Objectives:		Cicuits	04		
 This course will enable the students t Gain fluency in programming Create and run scripts using 1 Demonstrate the use of PERL applications 	g with scripting languages PERL/TCL/PYTHON in	CAD Tools	eb		
Unit -1			Hours		
Introduction to Scripts and Scrip languages, scripting today, Character PERL: Introduction to PERL, Nar Scalar expressions, Control structur working with arrays, Lists and hashe and regular expressions, Subroutines	ristics and uses of scriptines mes and values, Variables, Built-in functions, C es, Simple input and outp	ng languages. es and assignment, collections of Data,	12		
Unit -2 Advanced PERL: Finer points of Loworking with files, Type globs, Ex Libraries and modules, Objects, Obj	val, References, Data str jects and modules in act	ructures, Packages,	13		
Unit – 3 TCL: The TCL phenomena, Philosop data in TCL, Control flow, Data s Working with Strings, Patterns, Files	tructures, Simple input/	output, Procedures,	13		
Unit – 4 Advanced TCL: The eval, source, of packages, Namespaces, trapping applications 'Internet-aware',' Nuts- issues, TCL and TK integration.	errors, Event-driven	programs, Making	13		
Unit – 5					
PYTHON: Introduction to PYTHC functions, Built-in functions and M Handling			13		
Course outcomes: 1. Gain fluency in programm 2. Create and run scripts usin 3. Demonstrate the use of PE applications	g PERL/TCL/PYTHON	in CAD Tools	web		
Text Books: 1. The World of Scripting Lang 2. PYTHON Web Programm Publications.					
References: 1. TCL/TK: A Developer's G 2. Core PYTHON Programm			es.		

3.Learning Perl, Randal L. Schwartz, O' Reilly publications 6th edition 2011.4.Linux: The Complete Reference", Richard Peterson McGraw Hill Publications, 6th Edition, 2008.

LOW POWER VLSI DESIGN POOL-2				
Subject Code	21ETETHXXX	Internal Marks	30	
L-T-P	3-1-0	External Marks	70	
Total Number of Lecture Hours	64	Exam Hours	03	
Pre-requisite	VLSI DESIGN	Credits - 04		
Pre-requisite Course Objectives: This course will enable the students Identify the sources of power Understand the impact of po Characterize and model pow Understand the basic analysi Understand leakage sources a Unit -1 Sources of Power Dissipation Introduction, Short-Circuit Power Dynamic Power for a Complex of Activity, Leakage Power Dissipati Band-to-Band Tunneling Current, Su Effects Unit -2 Supply Voltage Scaling for Low Po Device Feature Size Scaling, Consta Architectural-Level Approaches: Pan	to: dissipation in digital IC wer on system performant ver consumption is methods and reduction techniques. Dissipation, Switching Gate, Reduced Voltage on, p–n Junction Rever ib threshold Leakage Cur ower ant-Field Scaling, Consta	systems ace and reliability. Power Dissipation, Swing, Switching rse-Biased Current, rent, Short-Channel nt-Voltage Scaling,	Hours 12	
Power, Combining Parallelism with Level Transformations: Multilevel V Scaling Interfaces, Static Timing A Scaling Unit – 3	h Pipelining, Voltage So Voltage Scaling Challeng Analysis Dynamic Volta	caling Using High- es in MVS Voltage	13	
Switched Capacitance MinimizationProbabilistic Power Analysis: Random logic signals, probability and frequency, probabilistic power analysis techniques, signal entropy.Bus Encoding: Gray Coding, One-Hot Coding, Bus-Inversion, T0 Coding, Clock Gating, Gated-Clock FSMs FSM State Encoding, FSM Partitioning, Pre computation, Glitching Power MinimizationUnit – 4			13	
Unit – 4Leakage Power MinimizationFabrication of Multiple Threshold Voltages, Multiple Channel Doping, MultipleOxide CMOS, Multiple Channel Length, Multiple Body Bias, VTCMOSApproach, MTCMOS Approach, Power Gating, Clock Gating Versus PowerGating, Power-Gating Issues, Isolation Strategy, State Retention Strategy, Power-Gating Controller, Power Management, Combining DVFS and PowerManagementUnit – 5			13	
Unit – 5 Low power clock distribution & Si Low power clock distribution: Pow driver versus distributed buffers, Z package co design for clock network	wer dissipation in clock Zero skew versus tolerab	distribution, single	13	

Simulation Power Analysis: SPICE circuit simulators, gate level logic					
simulation, capacitive power estimation, architecture level analysis, data					
correlation analysis of DSP systems, Monte Carlo Simulation					
Course outcomes:					
1. Identify the sources of power dissipation in digital IC systems & understand the					
impact of power on system performance and reliability.					
2. Characterize and model power consumption & understand the basic analysis methods.					
3.Understand leakage sources and reduction techniques.					
Text Books:					
1. Low-Power VLSI Circuits and Systems, Ajit Pal, SPRINGER PUBLISHERS					
2. Practical Low Power Digital Vlsi Design, Gary Yeap Motorola, Springer Science					
Business Media, LLC.					
Reference Books:					
1. Low Power CMOS Design – Anantha Chandrakasan, IEEE Press/Wiley International,					
1998.					
2. Massoud Pedram, Jan M. Rabaey, "Low power design methodologies ", Kluwer					
Academic					
Publishers.					
2 Low Power CMOS VISI Circuit Design A Pollemour M I Elemesri Kluwer					

3. Low Power CMOS VLSI Circuit Design – A. Bellamour, M. I. Elamasri, Kluwer Academic Press, 1995.

WIREI	LESS SENSOR NETWO POOL-3	ORKS	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Computer Networks	Credits	- 04
Course Objectives:			
 This course will enable the students to Understand the hardware sensor for various applicate Understand radio standard sensor network based systems an performance of wireless sensor 	details of different types ions. ds and communication pro tems and application. d programming languages	otocols to be used for s for wireless sensor :	wireless
Unit -1	ensor networks systems a		Hours
Introduction and overview of sens	or network architecture a	and its applications	nouis
sensor network comparison with Ad l hardware and software details.		1	12
Unit -2			
Hardware: Examples like mica2, m			
and Sun SPOT, Software (Operating	g Systems): tiny OS, MA	NTIS, Contiki, and	13
RetOS.			
Unit – 3			
Programming tools: C, nesC. Pe	erformance comparison of	of wireless sensor	
networks simulation.			13
Experimental platforms : open sour	rce (ns-2) and commercia	l (QualNet, Opnet)	
Unit – 4			
Overview of sensor network protoc per layer): Physical, MAC and routi protocols, multi-hop and cluster ba Bluetooth, BLE (Bluetooth low energy	ng/ Network layer protoco ased protocols, Fundame	ols, node discovery	13
Unit – 5			
Data dissemination and processing management systems, data storage Energy preservation and efficiency; related to Localization, connecti mechanisms; coverage issues; senso research, and Enabling technologies	e; query processing. Sp security challenges; faul vity and topology, Se or Web; sensor Grid, Ope	ecialized features: t- tolerance, Issues ensor deployment en issues for future	13
Course outcomes:			
 Design wireless sensor net Understand the hardware d sensor for various applicati Understand radio standards sensor network based sys Use operating systems and performance of wireless se Handle special issues re 	letails of different types or ions. s and communication prot tems and application. l programming languages ensor networks systems a	f sensors and select r tocols to be used for for wireless sensor r nd platforms.	ight type of wireless nodes,
challenges	chared to sensors like c		

- 1. H. Karl and A. Willig, "Protocols and Architectures for Wireless Sensor Networks", John Wiley & Sons, India, 2012.
- 2. C. S. Raghavendra, K. M. Sivalingam, and T. Znati, Editors, "Wireless Sensor Networks", Springer Verlag, 1stIndian reprint,2010.

REFERENCES:

- 1. F. Zhao and L. Guibas, "Wireless Sensor Networks: An Information Processing Approach", Morgan Kaufmann, 1st Indian reprint, 2013.
- 2. YingshuLi, MyT. Thai, Weili Wu, "Wireless sensor Network and Applications", Springer series on signals and communication technology, 2008.

SOFI	WARE DEFINED RAI POOL-3	DIO	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communications	Credits – 04	•
Course Objectives:			
Unit -1 Introduction: The Need for Sof	blems critically in the fultirate signal processing etrum exploitation for con ques for the development software defined radios a	in SDR, as well as a S ducting research. of scientific and techr nd their usage for cogn s Software Radio,	mart antenn
Characteristics and benefits of soft Radio, RF Implementation issues- Range- The Principal Challenge of Topologies- Enhanced Flexibility Importance of the Components to Ov and Their Issues- Noise and Distortio Unit -2	The Purpose of RF From Receiver Design – RF R of the RF Chain with erall Performance- Transp	nt – End, Dynamic eceiver Front- End Software Radios- mitter Architectures	12
Multi Rate Signal Processing: Intro Polyphase Filters Digital Filter Bar Using Multirate Digital Filters. Di Comparison of Direct Digital S Approaches to Direct Digital Synthe Components due to Periodic jitter- B Direct Digital Synthesis Systems- I direct Digital Synthesis- Generation Techniques. Unit – 3	nks- Timing Recovery in igital Generation of Sig ynthesis with Analog esis- Analysis of Spuriou and Pass Signal Generati Hybrid DDS-PLL System	n Digital Receivers nals: Introduction- Signal Synthesis- s Signals- Spurious on- Performance of ns- Applications of	13
Analog to Digital and Digital to A converters Parameters of Practical da Analog to Digital and Digital to A data converter performance- Commo Unit – 4	ata converters. nalog Conversion: Tec	hniques to improve	13
Digital Hardware Choices: Intro Processors- Field Programmable Gat and ASICs- Power Management Iss and ASICs.	e Arrays- Trade-Offs in U	sing DSPs, FPGAs,	13
Unit – 5 Object – Oriented Representation Networks- Object Oriented Program Environments- Joint Tactical Radio Design: Introduction and Historical Information Transfer System, SI Spectrum Ware, CHARIOT Course outcomes:	nming- Object Brokers- System. Case Studies Perspective, SPEAK eas	Mobile Application in Software Radio sy- JTRS, Wireless ceiver Subsystem,	13

technologies for its implementation.

- 2. Analyze complex problems critically in the domains of Radio frequency implementation issues, Multirate signal processing in SDR, as well as a Smart antenna techniques for better spectrum exploitation for conducting research.
- 3. Apply appropriate techniques for the development of scientific and technological knowledge in designing software defined radios and their usage for cognitive radio.

Text Books:

- 1. Software Radio: A Modern Approach to Radio Engineering Jeffrey H. Reed, 2002, PEA Publication.
- 2. Software Defined Radio: Enabling Technologies- Walter Tuttle Bee, 2002, Wiley Publications.

REFERENCE BOOKS:

- 1. Software Defined Radio for 3G Paul Burns, 2002, Artech House.
- 2. Software Defined Radio: Architectures, Systems and Functions Markus Dillinger, KambizMadani, Nancy Alonistioti, 2003, Wiley.
- 3. Software Radio Architecture: Object Oriented Approaches to wireless System Engineering Joseph Mitola, III, 2000, John Wiley & Sons.
- 4. R.F Microelectronics B. Razavi, 1998, PHI.
- 5. DSP A Computer Based Approach S. K. Mithra, 1998, McGraw-Hill

DATA COMMUNICATION & COMPUTER NETWORKS

	POOL-3		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communication systems	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
Know the Categories and	d functions of various Data	communication Netw	orks
• Design and analyze varie	ous error detection techniqu	es.	
• Demonstrate the mechan	nism of routing the data in n	etwork layer	
	f various Flow control and	•	lechanisms
Unit -1		0	Hours
Introduction to Data Communic Data Flow, Networks- Distribute Structures, Network Models, Ca Networks, The Internet - A Brief Standards - Protocols, Standards, S Network Models, Layered Tasks, Protocol Suite, Addressing Intr Characteristics, WiFi: 802.11 Wirel Unit -2	ed Processing, Network of ategories of Networks In History, The Internet Too Standards Organizations, In OSI model, Layers in OS roduction, Wireless Link	Criteria, Physical hterconnection of day, Protocol and hternet Standards. SI model, TCP/IP as and Network	12
Data Link Layer: Links, Access N Layer, The Services Provided by th Detection vs Correction, Forward e Detection and Correction Techniqu Cyclic Redundancy Check (CRC) protocols, Noisy less Channels ar Protocols, Random Access, AL Protocols. 802.11 MAC Protocol, II Unit – 3	the Link Layer, Types of error correction Versus Retrores, Parity Checks, Check su , Framing, Flow Control, and Noisy Channels, HDLC OHA, Controlled access	rors, Redundancy, ransmission Error- umming Methods, and Error Control , Multiple Access	13
The Network Layer: Introduction Models, Virtual Circuit and Dat Datagram Networks, Origins of Vo Input Processing, Switching, Outpu Plane. The Internet Protocol(IP): For Datagram format, Ipv4 Addressing IPv6	tagram Networks-Virtual-C C and Datagram Networks at Processing, Queuing, The warding and Addressing	Circuit Networks, , Inside a Router- e Routing Control in the Internet-	13
Unit – 4			
Transport Layer: Introduction at Between Transport and Network La Internet, Multiplexing and Demultip Segment Structure, UDP Checks Building a Reliable Data Transfer Protocols, Go- Back-N(GBN), S Transport: TCP - The TCP Connec Time Estimation and Timeout, R Connection Management, Principle Costs of Congestion, Approaches to	ayers, Overview of the Tran olexing, Connectionless Trar um, Principles of Reliable Protocol, Pipelined Reliable elective Repeat(SR), Con- ection, TCP Segment Struct celiable Data Transfer, Flo s of Congestion Control - 7	sport Layer in the hsport: UDP -UDP e Data Transfer- ble Data Transfer inection Oriented cture, Round-Trip ow Control, TCP The Cause and the	13

Unit – 5				
Application Layer: Principles of Networking Applications – Network				
Application Architectures, Processes Communicating, Transport Services				
Available to Applications, Transport Services Provided by the File Transfer:	13			
FTP,- FTP Commands and Replies, Electronic Mail in the Internet- STMP,	15			
Comparison with HTTP, DNS-The Internet's Directory Service - Service				
Provided by DNS, Overview of How DNS Works, DNS Records and messages.				
Course outcomes:				
1. Know the Categories and functions of various Data communication Networks				
2. Design and analyze various error detection techniques.				
3. Demonstrate the mechanism of routing the data in network layer				
4. Know the significance of various Flow control and Congestion control Mechanism	IS			
5. Know the Functioning of various Application layer Protocols.				
Text Books:				
1. Computer Networking A Top-Down Approach – Kurose James F, Keith W, 6th Edition,				
Pearson.				
2. Data Communications and Networking Behrouz A. Forouzan 4th Edition McGr	awHill			
Educatio				
REFERENCES:				
1. Data communication and Networks - Bhusan Trivedi, Oxford university press, 2016				
2. Computer Networks Andrew S Tanenbaum, 4th Edition, Pearson Education				
3. Understanding Communications and Networks, 3rd Edition, W. A. Shay, C.	engage			
Learning				

COGNITIVE RADIO
POOL-3

Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite		Credits – 04	
Course Objectives:			
This course will enable the students	to:		
• Develop the cognitive ra	idio, as well as techniques	s for spectrum holes of	letection that
cognitive radio takes adv	antages in order to exploi	t it.	
 Understand technologies 	to allow an efficient use o	f TVWS for radio con	nmunications
based on two spectrum s	haring business models/po	olicies.	
 Understand fundamental 	issues regarding dynamic	spectrum access, the r	adio resource
	g, as well as a number of	optimization technique	ues for better
Spectrum exploitation			
Unit -1			Hours
Introduction to Cognitive Radio			
architecture, functions of cognitiv			12
components of cognitive radio, spec		alysis and decision,	12
potential applications of cognitive ra	adio.		
Unit -2			
Sensing: Spectrum sensing, detection			
sensing, geo-location database and	1 0	s models (spectrum	13
of commons, real time secondary sp	ectrum market).		
Unit – 3			
Optimization Techniques of		llocation: Linear	
programming, convex programming			13
Non-Linear Programming, inte	ger programming, dyna	mic programming,	
stochastic programming			
Unit – 4		.	
Dynamic Spectrum Access and Ma			13
architectures, centralized dynamic sp		dynamic spectrum	
access, learning algorithms and prot	ocols.		
Unit – 5			
Spectrum Trading: Introduction to		-	
trading, radio resource pricing, brid			
(utility, auction theory), classifica			13
auctions, concurrent, sequential).	e	0	
Network layer and transport layer i	ssues, cross- layer design	for cognitive radio	
networks			
Course outcomes:	· · · · · · · · · · · · · · · · · · ·	. 1	
1. Understand the fundamental			
2. Develop the cognitive radio	-	for spectrum noies c	letection that
cognitive radio takes advanta		TUNC for rodio cor	
3. Understand technologies to			munications
based on two spectrum sharin4. Understand fundamental issues			dia recourse
4. Understand fundamental iss management and trading, a			
Spectrum exploitation	s well as a number of (punnzauon techniqu	es for dette
Text Books:			
1. Ekram Hossain, DusitNiyato	7hu Han "Dunamia Sn	ectrum Access and M	nagement in
Cognitive Radio Networks",	• •		inagement II
Cognitive Radio Networks,	Cambridge University Pr	033,2007.	

2. Kwang-Cheng Chen, Ramjee Prasad, "Cognitive radio networks", John Wiley & Sons Ltd., 2009.

REFERENCE BOOKS

- 1. Bruce Fette, "Cognitive radio technology", Elsevier, 2nd edition, 2009.
- 2. HuseyinArslan, "Cognitive Radio, Software Defined Radio, and Adaptive Wireless Systems", Springer, 2007.
- 3. Francisco Rodrigo Porto Cavalcanti, Soren Andersson, "Optimizing Wireless Communication Systems" Springer, 2009.
- 4. Linda Doyle, "Essentials of Cognitive Radio", Cambridge University Press, 2009

5	G COMMUNICATION POOL-3		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communication Systems	Credits	
Course Objectives:	Communication Systems	Cicuits	01
 This course will enable the students Learn 5G Technology ad Learn the key RF, PHY, Learn Device to device c Implementation options to the students 	vances and their benefits MAC and air interface chan communication and millimet		cation
Unit -1			Hours
Overview of 5G Broadband Wire technologies 1G to 4G (LTE, L' requirements, Regulations for 5G, S	TEA, LTEA Pro), An O	Overview of 5G	12
Unit -2			
The 5G wireless Propagation C propagation scenarios and challeng mm Wave MIMO Systems. Unit – 3		0 1	13
Transmission and Design Tech transmission over 5G, Modulation 7 multiplexing (OFDM), generalized filter bank multi-carriers (FBMC) a Multiple Accesses Techniques – or (OFDMA), generalized frequency orthogonal multiple accesses (NOM	Techniques – Orthogonal fr frequency division multip and universal filtered multi- thogonal frequency division division multiple accesses	requency division blexing (GFDM), -carrier (UFMC). multiple accesses	13
Unit – 4			
Device-to-device (D2D) and communications – Extension of 40 management for mobile broadband communications	G D2D standardization to 5	,	13
Unit – 5			
Millimeter-wave Communication scenarios, beam forming, physical management, Massive MIMO propa in Massive MIMO, Massive MIM MIMO, Pilot Contamination, Spatia	layer techniques, interferen agation channel models, Ch O with Imperfect CSI, Mu	nce and mobility nannel Estimation	13
Course outcomes:			
 Learn 5G Technology advanc Learn the key RF, PHY, MAG Learn Device to device comm Implementation options for 50 	C and air interface changes in nunication and millimeter wa		
Text Books:			
 Martin Sauter "From GSM I to Mobile Networks and Mo Afif Osseiran, Jose.F.Mor Networks", Cambridge Univ 	bile Broadband", Wiley-Bla sserrat, Patrick Marsch, '	ickwell.	

- 3. Athanasios G.Kanatos, Konstantina S.Nikita, Panagiotis Mathiopoulos, "New Directions in Wireless Communication Systems from Mobile to 5G", CRC Press.
- 4. Theodore S.Rappaport, Robert W.Heath, Robert C.Danials, James N.Murdock "Millimeter Wave Wireless Communications", Prentice Hall Communications.

References:

- 1. Jonathan Rodriguez, "Fundamentals of 5G Mobile Networks", John Wiley & Sons.
- 2. Amitabha Ghosh and Rapeepat Ratasuk "Essentials of LTE and LTE-A", Cambridge University Press

SATEL	LITE COMMUNICATI POOL-3	ONS	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communication Systems	Credits	- 04
 Derive the expression for link design. Understand the various t station design. 	, applications and subsyste c G/T ratio and to solve sor ypes of multiple access ter of GPS and its architectur ite Communications, Histo nications, Frequency alloc ds of Satellite Communicat LAUNCHERS : Orbital	ne analytical problem chniques and archite re. rical Back-ground, ations for Satellite tions. Mechanics, Look	ns on satellite
launch vehicles, Orbital effects in co Unit -2 ORBITAL MECHANICS AND Angle determination, Orbital pertu launch vehicles, Orbital effects in co Unit – 3	LAUNCHERS : Orbital rbations, Orbit determinations	Mechanics, Look tion, launches and	13
SATELLITE SUB SYSTEMS: A tracking, Command and monitoring, Satellite antenna Equipment reliabili SATELLITE LINK DESIGN: temperature and G/T ratio, Design satellite links for specified C/N, Sys	, power systems, communi ity and Space qualification Basic transmission theo of down links, up link	cation subsystems, ry, system noise	13
Unit – 4		I	
MULTIPLE ACCESS: Freque Intermodulation, Calculation of C/I Frame structure, link design using T Onboard processing, DAMA, Code spectrum transmission and reception EARTH STATION TECHNOLO Antennas, Tracking systems, Terrest	N. Time division Multiple DMA, Examples. Satellite Division Multiple access DGY: Introduction, Transp	e Switched TDMA (CDMA), Spread mitters, Receivers,	13
Unit – 5 LOW EARTH ORBIT AND GEO Orbit consideration, coverage and fre considerations, System consideration SATELLITE NAVIGATION & TH Radio and Satellite Navigation, GPS and codes, Satellite signal acquisit levels, GPS receiver operation, GPS Course outcomes: 1. Understand the concepts, app	equency considerations, Dens, Operational NGSO con HE GLOBAL POSITIO Position Location principation, GPS Navigation Me C/A code accuracy, Diffe	elay & Throughput nstellation Designs NING SYSTEM: les, GPS Receivers ssage, GPS signal rential GP	13

- 2. Derive the expression for G/T ratio and to solve some analytical problems on satellite link design.
- 3. Understand the various types of multiple access techniques and architecture of earth station design.
- 4. Understand the concepts of GPS and its architecture.

- 1. Satellite Communications Timothy Pratt, Charles Bostian and Jeremy Allnutt, WSE, Wiley Publications, 2ndEdition, 2003.
- Satellite Communications Engineering Wilbur L. Pritchard, Robert A Nelson and Henri G. Suyderhoud, 2ndEdition, Pearson Publications, 2003.

REFERENCES:

- 1. Satellite Communications : Design Principles M. Richharia, BS Publications, 2nd Edition, 2003.
- 2. Satellite Communication D.C Agarwal, Khanna Publications, 5th Ed.
- 3. Fundamentals of Satellite Communications K.N. Raja Rao, PHI, 2004
- 4. Satellite Communications Dennis Roddy, McGraw Hill, 2nd Edition, 1996.

OPTICAL COMMUNICATION

	POOL-3		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communication Systems	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
	fiber experiments in the lab	oratory	
	tic modes in waveguides,	-	ht lost going
6	n, dispersion of optical fiber	U	in iost going
č 1 i	hoto detectors and optical te		alvze ontical
fiber and light wave syst		est equipment to a	aryze opticar
. .	s for better communication v	with minimum loss	20
Unit -1	s for better communication (Hours
Overview of optical fiber commun	vication Historical develop	ment. The general	110015
system, advantages of optical fiber	-	•	
Introduction, Ray theory transmis			
angle, Numerical Aperture, Skew			12
Mode coupling, Step Index fibers,			14
off wavelength, Mode Field Dia	•		
problems	meter, Encenve Kenaeuve	maex, Related	
Unit -2			
Fiber materials:- Glass, Halide, A	ctive glass Chalgenide glas	ss Plastic optical	
fibers. Signal distortion in optical f		-	
e 1	· · · · ·	U U	
Bending losses, Core and Cladding losses, Information capacity determination, Group delay, Types of Dispersion:- Material dispersion, Wave-guide dispersion,			13
Polarization-Mode dispersion, Intermodal dispersion, Pulse broadening in Graded			
index fiber, Related problems.	inodul dispersion, i dise orod		
Unit -3			
Optical fiber Connectors-Conne	ctor types. Single mode f	fiber connectors.	
Connector return loss.	••••••••••••••••••••••••••••••••••••••		10
Fiber Splicing - Splicing techniques, Splicing single mode fibers, Fiber alignment			13
and joint loss- Multimode fiber join			
Unit – 4			
Optical sources - LEDs, Structu	res. Materials. Quantum ef	fficiency. Power.	
Modulation, Power bandwidth produ			
conditions, External quantum effic			10
frequencies, Reliability of LED & I	•		13
Optical detectors - Physical princip		tor response time,	
Temperature effect on Avalanche		_	
problems		, ,	
Unit – 5			
Source to Fiber Power Launchin	g - Output patterns, Power	coupling, Power	
launching, Equilibrium Numerical			
Optical receiver operation Fundation	-		
transmission, error sources, Receive	-		13
Probability of Error, Quantum limit		- '	
-	-	and chains and	
Optical system design - Point	-to- point links- Compon	ient choice and	

in Optical links, WDM, Necessity, Principles, Measurement of Attenuation and	
Dispersion, Eye pattern.	

Course outcomes:

- 1. Choose necessary components required in modern optical communications systems .
- 2. Design and build optical fiber experiments in the laboratory, and learn how to calculate electromagnetic modes in waveguides, the amount of light lost going through an optical system, dispersion of optical fibers.
- 3. Use different types of photo detectors and optical test equipment to analyze optical fiber and light wave systems.
- 4. Choose the optical cables for better communication with minimum losses
- 5. Design, build, and demonstrate optical fiber experiments in the laboratory

Text Books:

- 1. Optical Fiber Communications Gerd Keiser, Mc Graw-Hill International edition, 3 rd Edition, 2000.
- 2. Optical Fiber Communications John M. Senior, PHI, 2nd Edition, 2002.

RERFERENCES:

- 1. Fiber Optic Communications D.K. Mynbaev , S.C. Gupta and Lowell L. Scheiner, Pearson Education,2005.
- 2. Text Book on Optical Fiber Communication and its Applications S.C.Gupta, PHI, 2005.
- 3. Fiber Optic Communication Systems Govind P. Agarwal, John Wiley, 3rd Edition, 2004.
- 4. Fiber Optic Communications Joseph C. Palais, 4th Edition, Pearson Education, 2004.

GLOBAL NAVIGATIONAL SATELLITE SYSTEMS

	POOL-3		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communication Systems	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
• Understand global navig	ational satellite systems		
.	nal Navigational Satellite Sy	stem	
Develop GNSS Receiver			
Unit -1			Hours
Introduction, GNSS overview, Glo	bal Positioning System, Ru	ssian GLONASS	
system, Galileo satellite system, Chi			
Zenith Satellite System (QZSS), Na		-	
Augmentations, Markets and Applic		(i (u (i c)),	
Fundamentals of satellite Navigati		ng Time of arrival	12
Measurements: Two-Dimensional H		-	
Determination via Satellite-Generat		1	
orbits: Orbital Mechanics, Constella			
Ranging codes: Determining Satelli		······································	
Unit -2			
Global positioning system: over	rview: Space Segment O	verview. Control	
Segment Overview, User Segment	1 0	-	
			13
Satellite Constellation Description, Space Segment Phased Development, Control segment description: OCS Current Configuration, OCS Transition, OCS Planned			
Upgrades, User segment: GNSS Red		ion, o os i funitou	
Unit -3			
Navigation with Indian Constell	lation (NavIC): overview	space segment.	
NavIC control segment, Geodesy an		, space segment,	13
Navigation services , signals, applications and NavIC user equipment.			
Unit – 4			
GNSS Receiver: Acquisition: Singl	le Trial Detector, Tong Sear	ch Detector. M of	
N Search Detector, Combined Ton	-		
Techniques, Direct Acquisition of	0		13
Peak Code Search, carrier tracking			
sequence of initial receiver operatio		F,	
$\frac{1}{\text{Unit}-5}$			
GNSS errors: Introduction, Measur	rement errors: satellite clock	error enhemeris	
error, relative effects, atmospheric eff		· •	13
and shadowing effects, hardware bia		-	10
Course outcomes:	as errors, i sedorange error t	Judgets.	
1. Understand global navigation	al catellite systems		
2. Understand Indian regional N	-		
3. Develop GNSS Receiver	avizational Satemic System	L	
Text Books:			
1. Elliott D. Kaplan, Christop	her I Hegarty Understan	ding GPS/GNSS ~	inciples and
applications, third edition, at	. .	•	merpres alle
Reference Books:	itten nouse publishers, DOst	011, 2017	
1. G S Rao, Global Navigation	al satellite system. Tata Ma	Graw-Hill education	nrivate I te
New Delhi, 2010	ai satenne system, Tata MC		
INEW Dellil, 2010			

- 2. ISRO-IRNSS-ICD-SPS-1.1, Bangalore, 2017
- 3. Bhatta, B., 2010. Global Navigation Satellite Systems: Insights Into GPS, Glonass, Galileo, Compass, and Others, BS Publications, New Delhi
- 4. Grewal, M. S., Weill, L. R., Andrews, A. P., 2006. Global Positioning Systems, Inertial Navigation, and Integration, John Wiley & Sons, New York.
- 5. Hofmann-Wellenhof, B., Lichtenegger, H., Wasle, E., 2008. GNSS Global Navigation Satellite Systems, Springer, Verlag Wien.

SPEECH SIGNAL PROCESSING POOL-4

Subject Code	21ETETHXXX	Internal Marks	30	
L-T-P	3-1-0	External Marks	70	
Total Number of Lecture Hours	64	Exam Hours	03	
Pre-requisite	SS,DSP	Credits – 04		
Course Objectives:				
This course will enable the students	to:			
• Identify the time domain	speech signal parameters			
• Differentiate time and free	equency domain methods	of speech processing		
Attribute linear predictiv	e analysis for speech signa	als		
• Explain the solutions for	LPC equations			
Unit -1			Hours	
Mechanics of speech: Speech pro-	oduction: Mechanism of	speech production,		
Acoustic phonetics, The Acoustic Th	neory of Speech Productio	n: Uniform lossless		
tube, Effects of losses in the vocal tr	cact, Digital models for sp	eech signals: Vocal	12	
tract, Radiation, Excitation, A	Auditory perception: p	osycho acoustics.		
Representations of speech waveform	n: Sampling of speech sign	nals, Quantization.		
Unit -2				
Time and frequency domain met	hods for speech process	sing: Time domain		
parameters of Speech signal: Short-	-Time Energy, Average M	lagnitude, Average		
Zero crossing Rate, Silence Discrin	6			
Auto Correlation Function, Pitch	period estimation using	Auto Correlation		
Function.			13	
Short Time Fourier analysis:				
interpretations, Sampling rates in ti				
by Synthesis, Analysis synthesis systems: Phase Vocoder, Channel Vocoder,				
Median Smoothing, Spectrographic	displays			
Unit – 3				
Linear predictive analysis of sp		of linear predictive		
analysis: Auto correlation method, C				
Solution of LPC equations: Chole	•	-	13	
Application of LPC parameters: Pit	0 1			
analysis using LPC parameters, VE	ELP. Relations Between t	ne various Speech		
Parameters, CELP.				
Unit – 4	• • •			
Application of speech process	0 1	systems: General	10	
considerations in the design of voice	1 0	1 1 0	13	
voice response system, Speaker reco	gnition systems: Speaker	verification system,		
Speaker identification system. Unit – 5				
	ad diait na agaitian arratar	. Continuous disit		
Speech recognition systems: Isolat			12	
recognition system. Typical applica	1		13	
Wiring communication equipment, Course outcomes:	information retrieval syste			
	human analy production	and articulation		
1. Summarize the mechanism of				
 Identify the time domain spee Differentiate time and frequer 		eech processing		
4. Attribute linear predictive and	•	even processing		
5. Explain the solutions for LPC				
6.Implement the different algori		for speaker and speed	n recognition	
systems		101 Spouker and spool	i i i i i i i i i i i i i i i i i i i	

- 1. L.R.Rabinerand, R.W.Schaffer, Digital Processing of Speech signals, Prentice Hall, 2004
- 2. Ben Gold and Nelson Morgan, Speech and Audio Signal Processing, John Wiley and Sons Inc., Singapore, 2004

Reference Books:

- 1. Quatieri, Discrete-time Speech Signal Processing, PrenticeHall,2001
- 2. L.R. Rabiner and B. H. Juang, Fundamentals of speech recognition, Prentice Hall, 1999.

VIDE	CO SIGNAL PROCESSI	NG	
Subject Code	POOL-4 21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	SS,DSP,DIP	Credits -	
Course Objectives:	55,D5F,DIF	Cicuits	- 04
 This course will enable the students Understand the format characterization of video Understand the concept of Modeling of the video sig estimation algorithms 	tion of video, its per in frequency domain of Lattice theory and samp gnal in different methods a	oling of video signals and understand the dif	
	ent approaches / algorithm	ns	
Unit -1			Hours
Video formation, perception an specification – video capture and di television systems, Digital video a Video Signals.	isplay – Analog video ras	ster – Analog color	12
Unit -2 Video Sampling – Basics of the E Conversion of Signals Sampled on E of Video Signals Unit – 3			13
Video Modeling-Camera model, Il model, Two dimensional models. Two Dimensional Motion Estimati Block matching Algorithm. Unit – 4			13
Waveform Based Video Coding Temporal prediction and transform Two dimensional shape coding, Tex	coding, Content Depende	ent Video Coding –	13
Unit – 5 Video Compression Standards - S and H.263- Multimedia content desc		ephony with H.261	13
 Course outcomes: Understand the formation of voltation of video in frequency domain Understand the concept of Lat Modeling of the video signal is estimation algorithms Coding of video in different at Knowledge in Video compress Text Books: Video Processing and Comm 	video, its perception and re ttice theory and sampling in different methods and u pproaches / algorithms sion standards	of video signals inderstand the differen	nt motion
Prentice Hall, 2001. Reference Books: 1. Image processing, analysis, a R. Brooks Cole publishing, 1 2. Multidimensional, signal, in Academic press, 2006.	999.		-

ADAPT	IVE SIGNAL PROCES POOL-4	SING	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	SS,DSP	Credits – 04	
	hms to develop the adapti ilter theory for different p	•	
Unit -1			Hours
Introduction to Adaptive Sy Characteristics, Applications, Exan Linear Combiner - Description, Wei function - Gradient & Mean Square	pple of an Adaptive Sys ght Vectors, Desired Res	_	12
Unit -2			
Development of Adaptive Filter surface: Introduction to Filtering - S Filtering, Problem statement, Princip Error, Wiener- Hopf equations, performance surface – Methods. Unit – 3	Smoothing and Prediction ble of Orthogonality - Min	– Linear Optimum imum Mean Square	13
Ideas of Gradient Search meth Solution, Stability& Rate of converg Steepest Descent Algorithms: Grad Steepest Descent, Comparison of Le Unit – 4	gence, Learning Curve. lient Search by Newton's		13
LMS Algorithm & Applications Stability & Performance analysis Stochastic algorithms - Convergence Applications: Noise cancellation - telephone circuits, Adaptive Beam for	of LMS Algorithms - e of LMS algorithm. – Cancellation of Echoe	LMS Gradient &	13
Unit – 5			
RLS & Kalman Filtering : Introduc filtering problem, The Innovation Innovation Process- Expression of Kalman filtering	Process, Estimation of	of State using the	13
Course Outcomes:			
 Review the Adaptive System developing adaptive system Study of different algorit Application of adaptive f Study of RLS & Kalman Text Books:	em. hms to develop the adapti ïlter theory for different p	ve filter theory.	e opted for
 Adaptive Signal Processing - Adaptive Filter Theory - Sim 			Е.

Reference Books

- 1. Optimum signal processing: An introduction Sophocles .J. Orfamadis, 2nd Ed., 1988, McGraw-Hill, New York
- 2. Adaptive signal processing-Theory and Applications S.Thomas Alexander, 1986, Springer Verlag.
- 3. Signal analysis Candy, McGraw Hill Int. Student Edition, 2008
- 4. James V. Candy Signal Processing: A Modern Approach, McGraw-Hill, International Edition, 1988.

BIO- MEI	DICAL SIGNAL PROC POOL-4	ESSING	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	DSP,BME	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
• Understand different types of	f biomedical signal.		
• Identify and analyze differen	t biomedical signals.		
• Find applications related to b	biomedical signal procession	ing	
Unit -1			Hours
Acquisition, Generation of Bio-si			12
signals, Study of diagnostically sign	ificant bio-signal paramet	ters	
Unit -2		<u> </u>	
Electrodes For Bio-Physiologica	e	0	
electrolyte interface, polarization, e			
biomaterial used for electrode, Type			13
of electrodes, microelectrodes), Prac			
of bio-signals (signal conditioning	g) and Signal conversion	n (ADC's DAC's)	
Processing, Digital filtering			
<u>Unit – 3</u>			
Bio-Medical Signal Processing			12
processing by wavelet (time-frequen		omputation of signal	13
parameters that are diagnostically si	gnificant)		
Unit – 4	0 1 1 1 0 1		
Classification of Signals and Noise			13
random signals and non-stationar		eatment of various	15
biomedical signal processing method	as and applications		
Unit – 5			
Principal component analysis, Co	rrelation and regression,	Analysis of chaotic	
signals Application areas of Bio-			
(MRA) and wavelets, Principal		-	12
component analysis(ICA). Pattern	classification- supervised	d and unsupervised	13
classification, Neural networks, S	Support vector Machines	s, Hidden Markov	
models. Examples of biomedical sig			
Course outcomes:	_		
4 11 1 1 1 100	f biomodical signal		
1. Understand different types of			
2. Identify and analyze differen	t biomedical signals.	ina	
 Identify and analyze different Find applications related to b 	t biomedical signals.	ing	
 Identify and analyze differen Find applications related to b Text Books: 	t biomedical signals. biomedical signal procession		3.
 Identify and analyze differen Find applications related to b Text Books: W. J. Tompkins, "Biomedica 	it biomedical signals. biomedical signal processional Digital Signal Processional Digital Signal Processional	ng", Prentice Hall,199	
 Identify and analyze differen Find applications related to b Text Books:	it biomedical signals. biomedical signal processional Digital Signal Processional Digital Signal Processional	ng", Prentice Hall,199	
 Identify and analyze different Find applications related to be Text Books: W. J. Tompkins, "Biomedica Eugene N Bruce, "Biomedica Son's publication,2001. References: 	t biomedical signals. biomedical signal processional Digital Signal Processional Processional Signal Processing and	ng", Prentice Hall,199 I Signal Modeling", J	ohn Wiley &
 Identify and analyze differen Find applications related to b Text Books: W. J. Tompkins, "Biomedica Eugene N Bruce, "Biomedica Son's publication,2001. References: Myer Kutz, "Biomedical En 	t biomedical signals. biomedical signal processional Digital Signal Processional Processional Signal Processing and	ng", Prentice Hall,199 I Signal Modeling", J	ohn Wiley &
 Identify and analyze differen Find applications related to b Text Books: W. J. Tompkins, "Biomedica Eugene N Bruce, "Biomedica Son's publication,2001. References: Myer Kutz, "Biomedical En 2009. 	t biomedical signals. biomedical signal processional Digital Signal Processing cal Signal Processing and gineering and Design Ha	ng", Prentice Hall,199 I Signal Modeling", J ndbook, Volume I", N	ohn Wiley &
 Identify and analyze differen Find applications related to b Text Books: W. J. Tompkins, "Biomedica Eugene N Bruce, "Biomedica Son's publication,2001. References: Myer Kutz, "Biomedical En 	It biomedical signals. biomedical signal processional Digital Signal Processing cal Signal Processing and gineering and Design Har gnal Processing", McGrav	ng", Prentice Hall,199 I Signal Modeling", J ndbook, Volume I", N v Hill,2005.	ohn Wiley & McGraw Hill,

DSP PROC	ESSORS AND ARCHIT	ECTURES	
	POOL-4		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	DSP	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
• Able to distinguish between	the architectural features	of general purpose pr	rocessors and
DSP processors.			
• Understand the architectures	s of TMS320C54xx device	es and ADSP 2100 DS	SP devices.
• Able to write simple assemb	ly language programs usin	g instruction set of TM	1S320C54xx.
• Can interface various device		C	
Unit -1	.		Hours
Introduction to Digital Signal			
processing system, The sampling			
Fourier Transform (DFT) and Fa			
invariant systems, Digital filters, I	1	*	13
Accuracy in DSP Implementations:			
in DSP systems, Dynamic Range			
implementations, A/D Conversion	· · · · ·	tional errors, D/A	
Conversion Errors, Compensating f	ilter.		
Unit -2			
Architectures for Programmable			
DSP Computational Building Blo			12
Addressing Capabilities, Address G	-	ability and Program	
Execution, Speed Issues, Features f	or External interfacing.		
Unit – 3			
Programmable Digital Signal Pro	0	0 1 0	
devices, Data Addressing modes			
modes of TMS320C54XX Proce	essors, Memory space of	of TMS320C54XX	
processors, program control.			13
TMS320C54XX Instructions a	8	·	
Interrupts of TMS320C54XX proce	essors, pipeline Operation	of TMS320C54XX	
Processors.			
Unit – 4			
Analog Devices Family of DSP	Devices: Analog Device	es Family of DSP	
Devices ALU and MAC block diag			
ADSP 2100, ADSP2181 high perf	1		13
Processor- The Blackfin Processor		-	
Overview of Hardware Processing	Units and Register files, .	Address Arithmetic	
Unit, Control Unit, Bus Architectur	e and Memory, Basic Peri	pherals.	
Unit – 5			
Analog Devices Family of DSP	Devices: Analog Devic	es Family of DSP	
Devices ALU and MAC block diag			
	ram, Shifter Instruction, E	Base Architecture of	12
ADSP 2100, ADSP2181 high perf			13

	sing Units and Register files, Address Arithmetic	
Unit, Control Unit, Bus Archite	ecture and Memory, Basic Peripherals.	
Course outcomes:		
	f Digital Signal Processing and transforms.	
-	ween the architectural features of general purpose p	processors and
DSP processors.		
3. Understand the archited	tures of TMS320C54xx devices and ADSP 2100 D	SP devices.
-	embly language programs using instruction set of T	MS320C54xx.
5. Can interface various d	evices to DSP Processors.	
Text Books:		
. .	ng – Avtar Singh and S. Srinivasan, Thomson Publi	
2. A Practical Approac	h To Digital Signal Processing - K Padn	nanabhan, R.
•	nthi. S, New Age International, 2006/2009	
3. Embedded Signal Proce	essing with the Micro Signal Architecture Publishe	r: Woon-Seng
Gan, Sen M. Kuo, Wile	y-IEEE Press, 2007	
Reference Books		
1. Digital Signal Process ramani and M. Bhaskar	ors, Architecture, Programming and Application, 2002, TMH.	s–B. Venkata
2. DSP Processor Fundam	entals, Architectures & Features - Lapsley et al., S	. Chand & Co
3. Digital Signal Processi	ng Applications Using the ADSP-2100 Family, Am	y Mar, PHI
4. The Scientist and Engi	neer's Guide to Digital Signal Processing by Stev	ven W. Smith,
California Technical Pu	blishing	
5. Embedded Media Proc	essing, David J. Katz and Rick Gentile, Embedde	d Technology
Series, 2005.		

WAVELET THEORY

	POOL-4		
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite		Credits – 04	
Course Objectives:			
This course will enable the students	to:		
Unit -1			Hours
The Age of Wavelets –Introduct Fundamentally NewWavelets and Heisenberg's Uncertainty Ghost. His Via Mallat, Different Communities within Wavelet Communities, Intere Future	d Other Reality Tran story of Wavelet from M of Wavelets, Different Fa	sforms, Managing orlet to Daubechies amilies of Wavelets	12
Unit -2			
Introduction-Vector spaces – bases function spaces, orthogonal function orthogonal basis functions, orthom coefficients, complex fourier series,	ns, orthonormal function normality and the meth	ns, function spaces, od of finding the	13
Unit – 3			
Continuous Wavelet and Short tim mathematical preliminaries, continue the windowed Fourier transform(Sho principle and time frequency timing Wavelet Transform, Continuous Ver Unit – 4	ous time frequency repres ort Time Fourier Transfor g, properties of wavelets	sentation of signals, m), The uncertainty used in Continuous	13
Discrete Wavelet Transform -Haar s Spaces, Haar Wavelet Function, No Standardizing the Notations, Refine Bases, Support of a wavelet system,	rmalization of Haarbases ement Relation with Res	at different scales,	13
Unit – 5			
Biorthogonal Wavelets -Biorthogon Systems, Signal Representation Biorthogonal Analysis, Biorthogona Wavelet Systems.	using Biorthogonal	Wavelet System,	13
Course outcomes: 1. Understand windowed Fourier tran and wavelet transform. 2. Understand wavelet basis and chan 3. Understand multire solution analy frequency resolution properties 4. Implement discrete wavelet tran Wavelet packets 5. Design certain classes of wavelet wavelet transforms to different fields	racterize continuous and ysis and identify various sforms with multirate d s to specification and jus	discrete wavelet transfo wavelets and evaluate igital filters and can	orms their Time under stand

1. Insight into Wavelets: From theory to practice by K.P.Soman, Ramachandran, Resmi, PHI Learning PVT Ltd,2010

2. L.Prasad & S.S.Iyengar, Wavelet Analysis with Applications to Image Processing, CRC Press, 1997.

Reference Books

1. Wavelet Transforms - Introduction to Theory and Applications, Raghuveer M.Rao, Ajit Bopardikar, Pearson Education, Asia

2. Fundaments of Wavelets - Theory, Algorithms and Applications, Jaideva C.Goswami, Andrew K. Chan, John Wiley & Sons.

MULTIRATI	E SYSTEMS AND FILT POOL-4	'ER BANKS	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	External Warks Exam Hours	03
Pre-requisite	Communication System		03
Course Objectives:	Communication System	15 Cleans - 04	
This course will enable the students	to.		
 Describe the applications of Study of various filter banks 	-		
• Study of various filter banks			
• Analyze the efforts of quanti			
• Explain the overall multi-rat	e systems and filter banks		
Unit -1			Hours
Interconnection of Building Blocks Implementations, Some Application Filter Banks	s, The Polyphase represe		12
Unit -2 Maximally Decimated Filter Bank Alias Free QMF System, Power Sym Polyphase representation, Perfect Banks, Tree Structured Filter Banks Unit – 3	metric QMF Banks, M-C Reconstruction Systems	hannel Filter Banks,	13
Para unitary Perfect Reconstruct Matrices, Filter Bank Properties Ind Two channel FIR Para unitary (QMF Lattice, Transform Coding and	uced by Para unitariness. 2MF Banks , The Two cl		13
Unit – 4 Cosine Modulated Filter Banks: QMF Bank, Efficient Polyphase Stru Cosine Modulated Perfect Reconstru	ctures, Deeper Properties	0	13
Unit – 5			
Cosine Modulated Filter Banks: QMF Bank, Efficient Polyphase Stru Cosine Modulated Perfect Reconstru	ctures, Deeper Properties	-	13
 Course outcomes: Understand the concepts mution Describe the applications of Study of various filter banks Analyze the efforts of quantion Explain the overall multi-rate 	multi-rate systems zation e systems and filter banks		
 P.P.Vaidyanathan , PTR Pre and Filter Banks. N.J.Fliege , John Wiley and 	-		irate System
Reference Books1. Raghuveer Rao, Ajit BoIntroduction to Theory and A	-	cation Asia,Wavelet	Transforms

2. C. Sidney Burrus , R.A.Gopianath , Pretice Hall, Introduction to wavelet and wavelet Transform.

MATHEMATICAL	METHODS FOR SIGN POOL-4	AL PROCESSING	
Subject Code	21ETETHXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	SS	Credits – 04	
Course Objectives:			
This course will enable the students	to:		
 Solve the problem associated Understand probability theory Summarize the concepts ass problems associated with pow Recognize the usage of rando corresponding problems. 	y and conditional probabi sociated with multiple ra wer spectral density of the	andom variables and e output of the system	l.
Unit -1			Hours
Vectors: Representation and Dot Transposes, Inverses, Gaussian Eli Vector spaces: Column and row space basis, dimension, linear transformation subspaces, projection and least square	imination, factorization, ces, Solving Ax=0 and A ions, Orthogonality: Orth	rank of a matrix, x=b, Independence, and vectors and	12
Unit -2			
Determinants: Determinant forr Eigenvalues and Eigenvectors: c Hermitian and Unitary matrices.			13
Unit – 3			
Determinants: Spectral theorem, Ch singular value decomposition, Linea Review of Probability: Basic set probability, Conditional Probability,	r transformations. theory and set algebra	a, basic axioms of	13
Unit – 4			
Random variables PDF/PM marginal/joint/conditional density Variables, characteristic/moment gen variables, Law of Large numbers (str types, Inequalities Chebyshev/Marko	functions, transforma nerating functions, Rando ong and Weak), Limit the	m sums of Random	13
Unit – 5			
Random Processes: classification of processes, autocorrelation function properties. Examples of random pro- process, Random processes through	n and power spectral cocess models - Gaussia	density and their	13
Course Outcomes:			
 Understand and solve the pro Solve the problem associated Understand probability theory Summarize the concepts ass problems associated with pov Recognize the usage of recorresponding problems. 	with linear algebra y and conditional probabi sociated with multiple ra wer spectral density of the	ility andom variables and e output of the system	l.
Text Books: 1. Introduction to linear algebra	- Gilbert Strang, SIAM,	2016.	

2. Introduction to probability - Bertsekas and Tsitsiklis, Athena, 2008

Reference Books

- 1. Probability and Random processes for Electrical Engineers, Leon Garcia Addison Wesley, 2nd edition, 1994
- 2. Probability and Random Processes, Geoffrey Grimmett, David Stirzaker, 3rd Edition, Oxford University Press, 2001.
- 3. Probability and Stochastic Process, Roy D Yates, David J Goodman, 2nd edition Wiley, 2010

S. No.	Subject Code	Subject	L-T-P	Credits
1	21ETETMXXX	Cellular and Mobile	3-1-0	4
		Communication		
2	21ETETMXXX	Switching Theory and Logic	3-1-0	4
		Design		
3	21ETETMXXX	Digital Data	3-1-0	4
		Communications		
4	21ETETMXXX	Signals and systems	3-1-0	4
5	21ETETMXXX	Electromagnetic Waves and	3-1-0	4
		Radiating Systems		
6	21ETETMXXX	Antenna Theory	3-1-0	4
7	21ETETMXXX	Linear IC Applications	3-1-0	4
8	21ETETMXXX	Digital Signal processing	3-1-0	4
9	21ETETMXXX	Analog Communication	3-1-0	4
10	21ETETMXXX	Microwave and Radar	3-1-0	4
		engineering		
In addi	In addition to any of the four subjects, MOOCs/NPTEL Courses for 04 credits (02 courses@			
2	credits each) are con	npulsory in the domain of Electr	onics and Com	munication
		Engineering		

GENERAL MINOR TRACKS

Cellular	and Mobile Commu	nication	
	Minors		
Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Communication	Credits -	- 04
Course Objectives:			
 This course will enable the students Understand the basic cellular etc., and various cellular syst Understand the different typ communications. Understand the concept of p cell site and mobile Understand the frequency m effects in cellular environme Understand the architecture Unit -1 CELLULAR MOBILE RADIO S System, uniqueness of mobile radio consideration of the components o Analog and Digital Cellular systems CELLULAR CONCEPTS: Evolution reuse, frequency reuse ratio, Numb traffic: trunking and blocking, Grade	ar concepts like frequency tems. les of interferences influer propagation model and the panagement, channel assig nt and the concepts of har as of GSM and 3G cellular SYSTEMS: Introduction of Cellular system, Hexag ion of Cellular systems, Co er of channels in a cellul	ncing cellular and mote e different types anten and types of han r systems. to Cellular Mobile of cellular systems, gonal shaped cells, oncept of frequency ar system, Cellular	bile nas used at gation
pico and femto cells; Cell splitting, C Unit -2 INTERFERENCE: Types of in Interference, real time Co-Channel channel Interference Reduction Fact directional Antenna system, design their effects, diversity receiver, nonc Unit – 3 CELL COVERAGE FOR SIGNA and hilly terrain, effect of human ma	nterferences, Introductio interference, Co-Channel or, desired C/I from a nor of Antenna system, anter co channel interference-di L AND TRAFFIC: Sign	measurement, Co- mal case in a Omni nna parameters and fferent types.	13
and reflected paths, straight line pat propagation over water and flat oper antenna height gain, form of a point CELL SITE AND MOBILE AN their synthesis, Omni directional and reduction, space diversity antenna separation of cell site antennas, high Unit – 4	h loss slope, and general en area, near and long-dis to-point model. FENNAS: Sum and diffe atennas, directional antenn as, umbrella pattern an gain antennas.	formula for mobile stance propagation, erence patterns and nas for interference ntennas, minimum	13
FREQUENCY MANAGEMEN' Numbering and grouping, setup acce to cell sites and mobile units: fixed channel sharing and borrowing, over	ess and paging channels, c channel and non-fixed c		13

HANDOFF STRATEGIES: Concept of Handoff, types of handoff, handoff	
initiation, delaying handoff, forced handoff, mobile assigned handoff, intersystem	
handoff, vehicle locating methods, dropped call rates and their evaluation.	
Unit – 5	
DIGITAL CELLULAR NETWORKS: GSM architecture, GSM channels,	
multiple access schemes; TDMA, CDMA, OFDMA; architecture of 3G cellular	13
systems.	
Course outcomes:	
The student will be able to	
I. Explain the fundamentals of cellular radio system design and its basic element	ts.
II. Analyse the concepts of different co-channel, non-co-channel interference	
coverage on signal & traffic of a designed system.	
III. Identify the various types of antenna system design suitable for mobile comm	unications.
IV. Distinguish the number of radio channels, channel assignment and frequency	
used in mobile communications and analyse the different hand off & cell splitt	
and dropped call rate at cell site area.	ing teeninques
V. Summarize the different types of second generation system architectures s	such as GSM
TDMA and CDMA for mobile communication systems	den us obivi,
Text Books:	
	rd Edn 2006
1. Mobile Cellular Telecommunications – W.C.Y. Lee, Tata McGraw Hill, 2	
2. Principles of Mobile Communications – Gordon L. Stuber, Springer Int	ernational 2nd
Edition, 2007.	

Reference Books

- Wireless Communications Theodore. S. Rapport, Pearson education, 2ndEdn., 2002.
 Mobile Cellular Communication G Sasibhushana Rao Pearson

Switchin	g Theory and Logic (Minors)	Design	
Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Bolean Algebra	Credits	- 04
Course Objectives:			
 This course will enable the students To solve a typical number ba To optimize logic gates for d To understand concepts of decoders, encoders, code con To understand the basic cond To understand the basic cond 	se conversions and analyzing igital circuits using various Adders and Sub tractor overters, multiplexers and cept flip flops and analyze	us techniques rs and analyze diffe comparators.	rent types of
Unit -1	1		Hours
Number Systems and Codes: Num binary numbers, binary arithmetic, f 2's, 9's and 10's complement arithmetic system, weighted & non weighted is codes. Logic Gates and Logic Families: In NOR operations, Exclusive-OR and families, MOS families, characterist ECL, I2L, MOS, CMOS and BiCMO Unit -2	loating point representation metic, BCD, octal and he binary codes, error detec Digital signals, basic logic d Exclusive NOR operation ics of logic families, RTL	on of numbers, 1's, exadecimal number ting and correcting c gates, NAND and tions, bipolar logic	12
Boolean Algebra and Minimization theorems of Boolean algebra, canon maxterm expansions, Karnaugh-map Map, don't care conditions, do simplifications of K-Maps, Quine M Unit – 3	onical (SOP and POS) for os, simplification of logic esign examples, EX-O	orms, minterm and functions using K- R and EX-NOR	13
Combinational Logic circuits: Ad binary adder, carry look ahead adde multiplexers, demultiplexers, decode comparators and their applications. Unit – 4	r, BCD adder, binary mu	ltiplier and divider,	13
Sequential Logic circuits: Classifi latch, D flip flop, JK flip-flop T fli flops, registers and counters, shift re design using D, T, and JK flip flops, Unit – 5	p-flop, conversion and a gisters, ripple counters, sy	pplications of flip- ynchronous counter	13
Memories and Programmable L RAM, types of RAM, ROM, EEP Array, Programmable Array Logic, of Complex Programmable Logic De	ROM, ROM as PLD, Pr qualitative theoretical/arc	ogrammable Logic chitectural concepts	13
Course outcomes: At the end of the course students i. Classify different number syst ii. Use the concept of Boolean al	ems and apply to generate		

iii. Design different types of Adders and Subtractors

iv. Design different types of decoders, encoders, code converters, multiplexers and comparators

v. Understand the concept of Memories and Programmable Logic Devices

Text Books:

1. Digital Design - Morris. M. Mano, Michael D. Ciletti - Fourth Edition - PrenticeHall India, 2008.

2. Modern Digital Electronics – R.P.Jain - Fourth Edition – Tata McGraw Hill Education Private Limited, 2010.

Reference Books

1. Digital Design: Principles and Practices - J.F. Wakerly - Fourth Edition - Prentice Hall, 2005.

2. Fundamentals of Logic Design - Charles. H. Roth - Fifth Edition - Thomson Brooks/ Cole, 2005.

Digit	tal Data Communicat	ions	
Subject Code	(Minors) 21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	SS	Credits – 04	00
Course Objectives:	~~~		
This course will enable the students	s to:		
Student able to learn the			
Digital Modulation Scheme	S		
Basic Concepts of Data Con	nmunications, Interfaces a	nd Modems	
Error Correction			
Multiplexing			
Multiple Access Techniques	S		
Unit -1			Hours
Digital Modulation Schemes: BP			12
DPSK – Methods, Band Width Effi	ciency, Carrier Recovery,	Clock Recovery.	12
Unit -2			
Basic Concepts of Data Comm			
Communication Networks, Proto	,		
Configuration, Topology, Transmis	-		13
DCE interface, Categories of Netwo	orks – TCP/IP Protocol su	ite and Comparison	
with OSI mode			
$\frac{\text{Unit}-3}{2}$			
Error Correction: Types of Error	-	Check (VRC), LRC,	
CRC, Checksum, Error Correction			10
Data Link Control: Line Disciplin			13
Data Link Protocols: Asynchic Character Oriented Protocols, Bit-C			
Unit – 4	fiented Flotocol, and Link	Access Procedures	
	on Multiplaying (EDM), Time Division	
Multiplexing: Frequency Divisi Multiplexing (TDM), Multiplexing), Thile Division	
Local Area Networks: Ethernet, C		an Bus Tokan Ping	
FDDI. Metropolitan Area Networks		en Dus, Token King,	13
Switching: Circuit Switching, Pack		vitching	
Networking and Interfacing Dev	<u> </u>	0	
Other Devices.	rees. Repeaters, Bridges,	Routers, Outeway,	
Unit – 5			
Multiple Access Techniques: Fre	auency- Division Multin	e Access (FDMA)	
Time - Division Multiple Access	1 1 1		
(CDMA), OFDM and OFDMA. R		-	10
			1 7 7
		-	13
(CSMA/CA), Controlled Access	Multiple Access with C	ollision Avoidance	13

Course outcomes:

- 1. Understand working of waveform coding techniques and analyse their performance.
- 2. Analyze the performance of a baseband and pass band digital communication system in terms of error rate and spectral efficiency.

Text Books:

Data Communication and Computer Networking - B. A.Forouzan, 2nd Ed., 2003, TMH.
 Advanced Electronic Communication Systems - W. Tomasi, 5th Ed., 2008, PEI

	SIGNAL ANALYSIS Minors		
Subject Code	21ETETMXXX	Internal Marks	30
Subject Code L-T-P	3-1-0	External Marks	70
	64		
Total Number of Lecture Hours	04	Exam Hours	03
Pre-requisite		Credits – 04	
I. To introduce the terminology of si	•	. 1 . 1	
ii. To introduce Fourier tools throug			
iii. To introduce the concept of sam			
iv. To analyze the linear systems in			
v. To study Laplace transform and z	-transform to analyze sign	alls and systems.	
Unit -1	1 1 0 01 10		Hours
characteristics of Signals and Sys signals, Singularity functions and function signum function and ram signals, orthogonal signal space, functions, Mean square error, close Orthogonality in complex functions.	ons on signals: time-shi ling. Problems on of tems. Complex exponent related functions: impu- p function. Analogy be Signal approximation ed or complete set of ort	fting, time-scaling, classification and tial and sinusoidal ilse function, step tween vectors and using orthogonal	12
Unit -2			
properties of Fourier series, Dirichla and Exponential Fourier series, Com Fourier Transform : Deriving Fou- transform of arbitrary signal, Fou- transform of periodic signals, proper involving impulse function and Transform.	nplex Fourier spectrum. arier transform from Fou rier transform of standar ties of Fourier transforms,	rier series, Fourier rd signals, Fourier Fourier transforms	13
Unit – 3			
Sampling: Graphical and analytical sampling, Natural and Flat top Sa samples, effect of under sampling –	ampling, Reconstruction	of signal from its	11
$\frac{\text{Unit}-4}{1}$	· · 1	D C	
Analysis Of Linear Systems: Line linear system, Linear time invariar system, Concept of convolution in ti representation of convolution, Tr characteristics of linear systems. Di Signal bandwidth, system bandwidth Causality and Poly-Wiener criterion bandwidth and rise time. Correlation and Convolution: In correlation of functions, propertie spectrum, Parseval's theorem, Pow correlation function and energy/pow convolution and correlation, Detection by correlation, Extraction of signal f	at (LTI) system, Linear t me domain and frequency cansfer function of a L istortion less transmission th, Ideal LPF, HPF and E for physical realization, re- ntroduction to Cross-cor es of correlation function for density spectrum, Rela- er spectral density function on of periodic signals in the	ime variant (LTV) domain, Graphical LTI system. Filter through a system, BPF characteristics, elationship between relation and auto- n, Energy density ation between auto n. Relation between	13

Laplace Transforms : Review of Laplace transforms, Partial fraction expansion, Inverse Laplace transform, Concept of region of convergence (ROC) for Laplace transforms, constraints on ROC for various classes of signals, Properties of L.T's, Relation between L.T's, and F.T. of a signal. Laplace transform of certain signals using waveform synthesis. Z–Transforms : Fundamental difference between continuous-time and discrete-time signals, discrete time signal representation using complex exponential and sinusoidal components, Periodicity of discrete time using complex exponential signal, Concept of Z- Transform of a discrete sequence. Distinction between Laplace, Fourier and Z transforms. Region of convergence in Z-Transform, constraints on ROC for various classes of signals, Inverse Z-transform, properties of Z-transforms.	13
Course outcomes:	
Text Books:	
1. Signals, Systems & Communications - B.P. Lathi, BS Publications, 2003.	
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, PHI	, 2nd Edn.
Poforongo Books:	

Reference Books:

1. Signals & Systems - Simon Haykin and Van Veen, Wiley, 2nd Edition.

2. Principles of Linear Systems and Signals – BP Lathi, Oxford University Press, 2015

3. Fundamentals of Signals and Systems- Michel J. Robert, MGH International Edition, 2008.

4. Signals and Systems - T K Rawat, Oxford University press, 2011

Electromag	netic Waves and Radiatin Minors	g Systems	
Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Engineering Physics,	Credits - 04	
	Vector algebra		
Course Objectives:			
This course will enable the students			
• Understand vector analysis a	•		
Gain knowledge on coulomb	-	tions	
• Learn magneto statics and pa			
Analyze Maxwell's equation	is and plot the smith chart		
Unit -1			Hours
Vectors analysis: Vector algebra,	vector calculus - divergen	ce, gradient, curl,	
Laplacian; Coordinate systems - Car			12
Unit -2			
Electrostatics: Coulomb's law, Gau	ss's law, electric scalar pot	ential, Laplace and	
Poisson's equations, conduction and	polarization, boundary cor	nditions, resistance	13
and capacitance;			
Unit – 3			
Magnetostatics : Biot-Savart law,		-	10
Lorentz force, magnetization, bo	oundary conditions, magi	netic energy and	13
inductance; Unit – 4			
Electrodynamics : Maxwell's eq	untiona Foredou's indust	ion dignlagement	
current, Plane wave propagation in		· •	13
reflection and transmission of plane			15
Smith chart;	waves at media boundary, i	ransmission mes,	
Unit – 5			
Advanced Topics: Antenna fu	indamentals, dipole ant	enna, Microstrip	10
transmission lines, Waves along gui	-	, 1	13
Course outcomes:			
1. Understand the concept of vector a	nalysis and different co-or	dinate systems.	
2. Understand the Electrostatics and	boundary conditions		
3. Understand the Magnetostatics a	-		
4. Analyze the maxwell's equations	•		
5. Analyze advanced antenna fundar	mentals like microstrip ant	enna, wave guide stru	ictures
Text Books:	I		1:11 1000
1. J. D. Kraus and D. A. Fleisch, "El	• •		1111, 1999.
2. D. K. Cheng, 'Field and Wave Ele	ectromagnetics, Addison-	westey series, 1989.	
Dotononoo Doolaa			
Reference Books : 1 W H Havt "Engineering Electro	magnetic" 5th Ed TMU	1000	
Reference Books:1. W. H. Hayt, "Engineering Electro2. J. A. Edminister, "Schaum's Outl	-		s" 108/

Antenna Theory	
Minors	

Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	EMTL	Credits – 04	
 Course Objectives: This course will enable the students Understand the fundamental Understand antenna arrays a 	concepts of antennas and	-	
 Understand broad band anter Analyze aperture and reflect 	nna and broad cast antenna	as.	nas.
Unit -1	or antennas, maygen s pr	incipie, princed unter	Hours
Fundamental Concepts: Physical Hertzian dipole; Antenna paramo effective aperture, and reciprocity; F	eters: Radiation pattern,	gain, directivity,	12
Unit -2			
Antenna Arrays: Arrays of point s			13
multiplication, synthesis of binomia Unit – 3	I and Dolph-Chebyshev at	rays.	
Broadband Antennas: Log-period: antennas, broadcast antennas.	ic and Yagi antennas, free	uency independent	13
Unit – 4			
Aperture and Reflector Antennas: in an infinite ground plane, slot and		1	13
Unit – 5			
Printed Antennas: Radiation from techniques.	n rectangular and circula	r patches, feeding	13
 Course outcomes: 1. Understand concept of radiation 2. Understand antenna arrays, sy 3. Analyze frequency independent 4. Understand Huygens' principli 5. Understand the significance of 	nthesis of binomial and Dent antennas and broadcast e, radiation from apertures	olph-Chebyshev array antennas. s in an infinite ground	
Text Books: 1. Balanis, C.A., "Antenna Theor 2. Kraus, J.D. and Fleisch, D.A.,		•	
Reference Books: 1. Stutzman, W.L. and Thiele, H. & Sons. 2. Elliot P.S. "Antenna Theory	•		

2. Elliot, R.S., "Antenna Theory and Design", Revised edition, Wiley-IEEE Press.2003

Linear IC Applications Minors

Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	EDC,STLD	Credits – 04	

Course Objectives:

This course will enable the students to:

- To understand the basic operation & performance parameters of differential amplifiers.
- To understand & learn the measuring techniques of performance parameters of OP-AMP
- To learn the linear and non-linear applications of operational amplifiers and the analysis & design of different types of active filters using opamps
- To learn the internal structure, operation and applications of different analog ICs
- To Acquire skills required for designing and testing integrated circuits

Unit -1	Hours
INTEGRATED CIRCUITS: Differential Amplifier- DC and AC analysis of Dual input Balanced output Configuration, Properties of other differential amplifier configuration (Dual Input Unbalanced Output, Single Ended Input – Balanced/ Unbalanced Output), DC Coupling and Cascade Differential Amplifier Stages, Level translator.	12
Unit -2	
Characteristics of OP-Amps, Integrated circuits-Types, Classification, Package Types and Temperature ranges, Power supplies, Op-amp Block Diagram, ideal and practical Opamp Specifications, DC and AC characteristics, 741 op-amp & its features, Op-Amp parameters & Measurement, Input & Out put Off set voltages & currents, slew rate, CMRR, PSRR, drift, Frequency Compensation techniques.	13
Unit – 3	
LINEAR and NON-LINEAR APPLICATIONS OF OP-AMPS: Inverting and Non-inverting amplifier, Integrator and differentiator, Difference amplifier, Instrumentation amplifier, AC amplifier, V to I, I to V converters, Buffers. NonLinear function generation, Comparators, Multivibrators, Triangular and Square wave generators, Log and Anti log Amplifiers, Precision rectifiers. ACTIVE FILTERS, ANALOG MULTIPLIERS AND MODULATORS: Design & Analysis of Butterworth active filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and all pass filters. Four Quadrant Multiplier, IC 1496, Sample & Hold circuits.	13
Unit – 4	
TIMERS & PHASE LOCKED LOOPS: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger; PLL - introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566).	13
Unit – 5	
DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS : Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC.DAC and ADC Specifications, Specifications AD 574 (12 bit ADC)	13

Course outcomes:

- 1. Design circuits using operational amplifiers for various applications.
- 2. Analyze and design amplifiers and active filters using Op-amp.
- 3. Diagnose and trouble-shoot linear electronic circuits.
- 4. Understand the gain-bandwidth concept and frequency response of the amplifier configurations. Understand thoroughly the operational amplifiers with linear integrated circuits.

Text Books:

- 1. Linear Integrated Circuits D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003.
- 2. Op-Amps & Linear ICs Ramakanth A. Gayakwad, PHI,1987.

Reference Books:

- 1. Operational Amplifiers & Linear Integrated Circuits –Sanjay Sharma ;SK Kataria &Sons;2nd Edition,2010
- 2. Design with Operational Amplifiers & Analog Integrated Circuits Sergio Franco, McGraw Hill, 1988.
- 3. OP AMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cenage Learning India Ltd
- 4. Operational Amplifiers & Linear Integrated Circuits–R.F.Coughlin & Fredrick Driscoll, PHI, 6th Edition.
- 5. Operational Amplifiers & Linear ICs David A Bell, Oxford Uni. Press, 3rd Edition
- 6. Operational Amplifiers–C.G. Clayton, Butterworth & Company Publ. Ltd./Elsevier, 1971

Dig	gital Signal Processin Minors	g	
Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Signals and System	Credits – 04	
 Course Objectives: This course will enable the students Analyze the Discrete Time S Know the importance of FFT Learn the IIR Filter design pridigital filter structures Learn the FIR Filter design pridigital filter structures 	ignals and Systems algorithm for computation rocedures and Understand	the various implement	ntations of
digital filter structuresLearn the concepts of DSP P	rocessors		
Unit -1			Hours
INTRODUCTION: Introduction t signals & sequences, Classification systems. Response of LTI systems to coefficient difference equations. Fu- time signals and systems. Review equations using Z-transforms, System Unit -2	of Discrete time system o arbitrary inputs. Solution requency domain represent w of Z-transforms, solution	s, stability of LTI of Linear constant ntation of discrete	12
DISCRETE FOURIER SERIES & discrete Fourier series, DFS represen transforms: Properties of DFT, lin Fourier transforms (FFT) - Radix frequency FFT Algorithms, Applicat Unit – 3	tation of periodic sequence ear filtering methods ba -2 decimation in time a	es, Discrete Fourier sed on DFT, Fast	13
DESIGN OF IIR DIGITAL FIL approximations – Butter worth and C analog filters, Design Examples, Ar Basic structures of IIR systems, Tran	Chebyshev, Design of IIR nalog and Digital frequence	Digital filters from	13
Unit – 4 DESIGN OF FIR DIGITAL FILT of FIR Digital Filters, frequency re Window Techniques and Frequency FIR filters. Basic structures of FIR s	sponse. Design of FIR D v Sampling technique, Co	igital Filters using	13
Unit – 5 INTRODUCTION TO DSP PRO DSPs: Multiplier and Multiplier A memory access schemes in P-DSI memory, VLIW architecture, Pipel Peripherals. Architecture of TMS32 Arithmetic Logic Unit.	Accumulator, Modified b Ps ,Multiple Access Mer lining, Special addressing	bus structures and mory, Multiported g modes, On-Chip	13
Course outcomes: After going through this course the s 1. Apply the difference equation 2. Use the FFT algorithm for so	ns concept in the anayziati		ystems

- 3. Design a Digital filter (IIR) from the given specifications Realize the IIR structures from the designed digital filter.
- 4. Design a Digital filter (FIR) from the given specifications Realize the FIR structures from the designed digital filter
- 5. Apply the signal processing concepts on DSP Processor.

Text Books:

- 1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G.Manolakis, Pearson Education / PHI, 2007.
- 2. Discrete Time Signal Processing A.V.Oppenheim and R.W. Schaffer, PHI

Reference Books:

- 1. Digital Signal Processing: Andreas Antoniou, TATA McGraw Hill, 2006
- 2. Digital Signal Processing Paperback 16 December 2014 by Tarun Kumar Rawat (Author), Publisher : Oxford University Press (16 December 2014)
- 3. DSP Primer C. Britton Rorabaugh, Tata McGraw Hill, 2005.
- 4. Fundamentals of Digital Signal Processing using Matlab Robert J. Schilling, Sandra

Aı	nalog communications		
	Minors		
Subject Code	21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	Signal and Systems	Credits – 04	05
Course Objectives:	Signar and Systems	creans 04	
 This course will enable the students to i. Familiarize with the fundame ii. Familiarize with various tee iii. Distinguish the figure of me iv. Develop the ability to class transmitters and receivers v. Familiarize with basic techn 	entals of analog communication chniques for analog modulation erits of various analog modulati sify and understand various func- iques for generating and demod	and demodulat on methods ctional blocks o	of radio
modulated signals			
Unit -1			Hours
. AMPLITUDE MODULATION : I for modulation, Frequency Division Definition, Time domain and free modulation, power relations in AM w Modulator, Switching modulator, Det Envelope detector. Unit -2	n Multiplexing , Amplitude quency domain description, vaves, Generation of AM waves	Modulation, single tone s, square law	12
DSB & SSB MODULATION: Doubt time domain and frequency domain Balanced Modulators, Ring Modulator waves, COSTAS Loop. Frequency do method for generation of AM SSB M Phase discrimination method for Demodulation of SSB Waves, Ve description, Generation of VSB M Envelope detection of a VSB Wave pu Applications of different AM Systems Unit – 3	description, Generation of DS r, Coherent detection of DSB-S omain description, Frequency d Modulated Wave, Time domain generating AM SSB Modul stigial side band modulation odulated wave, Time domain ilse Carrier, Comparison of AM	BSC Waves, C Modulated iscrimination description, ated waves. : Frequency description, Techniques,	13
. ANGLE MODULATION : Basic c frequency modulation, Spectrum Ana FM, Wide band FM, Constant Avera Wave - Generation of FM Waves, Din Frequency discriminator, Zero crossin of FM & AM Unit – 4	lysis of Sinusoidal FM Wave, E age Power, Transmission bandw ect FM, Detection of FM Waw ag detector, Phase locked loop,	Narrow band width of FM es: Balanced Comparison	13
NOISE: Review of noise and noise communication Systems, Noise in DSI in Angle Modulation Systems, Thresh emphasis & de-emphasis	3& SSB System, Noise in AM S	ystem, Noise	13

Unit – 5	
TRANSMITTERS & RECEIVERS: Radio Transmitter - Classification of	
Transmitter, AM Transmitter, Effect of feedback on performance of AM	
Transmitter, FM Transmitter – Variable reactance type and phase modulated FM	
Transmitter, frequency stability in FM Transmitter. Radio Receiver - Receiver	13
Types - Tuned radio frequency receiver, Superhetrodyne receiver, RF section and	
Characteristics - Frequency changing and tracking, Intermediate frequency, AGC,	
FM Receiver, Comparison with AM Receiver, Amplitude limiting. Communication	
Receivers, extensions of super heterodyne principle and additional circuits.	
Course outcomes:	
i. Explain the basic elements of communication system, need for modulation a	and elaborately
about amplitude modulation.	
ii. Describe the time and frequency domain representation, generation and de	emodulation of
DSBSC, SSB and VSB modulation schemes.	
iii. Discuss the concepts of angle modulation.	
iv. Explain various issues in radio transmitters and receivers	
v. Describe pulse modulation schemes and estimate the noise in analog modulation	ion schemes
Text Books:	
1. Communication Systems - Simon Haykin, John Wiley, 2ndEd.,.	
2. Modern digital and analog communication systems, 4th edition B.P.Lath	i. Ding. Gupta
oxford publishers	, 2
Reference Books:	
1. Principles of Communication Systems – H Taub& D. Schilling, GautamSah	e TMH 2007
3rd Edition. 2. Analog and digital Communication Systems – B.P. Lathi, BS Pul	
Sid Edition. 2. 7 maiog and digital Communication Systems – D.I. Laun, DS I at	511 c ation, 2000

Microw	ave and Radar Engine	ering	
Subject Code	Minors 21ETETMXXX	Internal Marks	30
L-T-P	3-1-0	External Marks	70
Total Number of Lecture Hours	64	Exam Hours	03
Pre-requisite	EMTL,Communication	Credits – 0	
Course Objectives:	Littl, communeutor	eredits o	•
This course will enable the students	to:		
Unit -1			Hours
Microwave Components: Rectang	gular cavity resonators; Q of	f a cavity resonator;	
Re-entrant cavities; Slow-wave stru	icture; Microwave hybrid ci	rcuits; S-parameters	
and their properties; Waveguide te			12
twists; Two hole directional couple	er; S- Matrix; Circulators ar	nd Isolators; Hybrid	
couplers.			
Unit -2			
Microwave Linear Beam and Cro			
at high frequency; Klystron-Veloo	•		
loading; Reflex klystron-Velocity			
electronic admittance; Helix tra			13
Conventional current; Electric field			
Magnetron-Types and Principles of	f operation; Modes of oscilla	ation; Strapping; pi-	
mode separation.			
Unit – 3			
. Microwave Devices: Transisto			
Structure; Operation; Characteristic	s and Power frequency limitation	ations of microwave	
transistors; Tunnel diodes and Fiel	dEffect Transistors. Transfe	er Electron Devices:	
Gunn diode; Gunn effect; Principle	and Mode of operation; Mi	crowave generation	13
and amplification Tunnel Diode; PI	N diode and Crystal diode. N	Iodulator; Switches,	15
Avalanche Transit- Time Devices	s: Physical Structure; Prin	ciple of operation;	
Characteristics; Power output and E	fficiency of IMPATT, TRA	PATT and BARITT	
diodes; Parametric amplifiers.			
Unit – 4			
. Microwave Measurement: Micro	owave bench; Precautions; P	ower measurement;	12
Bolometric method; Attenuation;	VSWR; Impedance, Freque	ency and Q of the	13
Cavity.		-	
Unit – 5			
Principles and Applications of Ra	dar: Basic Radar, Radar Bl	ock Diagram, Radar	
Frequencies, Applications of Radar,	,	U ,	
Radar: Introduction to Doppler and MTI Radar, delay line cancellers, staggered PRF. Range gated Doppler filter, limitations to MTI performance. Tracking with Radar,			10
			13
Monopulse Tracking, Conical Scan and Sequential Lobing, Limitations to Tracking			
		0	
Accuracy, Low Angle Tracking, Tr		n of Trackers.	

Text Books:

- 1. Microwave Devices and Circuits by Samuel Y. Liao, 3rd Ed., Pearson Education.
- 2. Foundations of Microwave Engineering by R .E. Collin, TMH Pub.
- 3. Introduction to Radar Systems by M.I Skolnik, TMH Pub. Co.

Reference Books

- 1. Microwave Principles by Reich.
- 2. Microwaves, Gupta, New Age International Publishers.
- 3. Microwave and Radar Engg., M. Kulkarni, Umesh Publication.